



**SUMMARY TEST DATA
ON
PAT-3D1G3D5G-CD-SFF-NF**

PL23226/1831

Customer: _____ Tested By: A. Lopez / S. Palacio
 SO No: _____ Temperature: +25°C
 Model No: PAT-3D1G3D5G-CD-SFF-NF Date: 08/22/18
 Serial No: PL23226/1831 Drawing No: 27634320 Rev: A1

TEST. ITEM NO	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA QC
1	Frequency Range:	3.1 TO 3.5 GHz	3.1 TO 3.5 GHz (See Plot)	PMI QA 1
2	Input Power:	+10 dBm CW	+22dBm (See Graph)	
3	Power Handling (Without Damage):	0.5 W Peak Power 256us Pulse Width, 0.06 Duty Factor.	Pass By Design	
Low Loss State:				
4	Insertion Loss:	1.10 dB MAX (BI-DIRECTIONAL)	0.79dB (See Plot)	
5	Variation In Loss:	± 0.2 dB Over Any 10 MHz	±0.00025dB	
6	Vswr:	1.20 : 1 MAX	1.19:1 (See Plot)	
7	Insertion Loss Matching:	± 0.2 dB From Golden Unit	± 0.1 dB	
8	Phase Matching:	± 3° From Golden Unit	0° / -0.937°	
Attenuation State:				
9	Attenuation:	30 dB ± 1.0 dB (BI-DIRECTIONAL)	30.35 – 30.64dB	
10	Variation In Attenuation:	± 0.2 dB Over Any 10 MHz	± 0.0018dB	
11	Vswr:	1.25 : 1 MAX	1.24:1 (See Plot)	
12	Insertion Loss Matching:	± 0.20 dB From Golden Unit	± 0.19 dB	
13	Phase Matching:	± 3° From Golden Unit	1.18° / -1.32°	
Noise Figure:				
14	Low Loss State:	1.10 dB MAX. (BI-DIRECTIONAL)	0.79dB	
15	Attenuation State:	30 dB ±1.0 dB MAX.	30.64 dB	
16	Switching Speed:	0.5 us MAX	0.420 us	
17	Logic Levels:	PIN D > PIN E: Low Loss State PIN E > PIN D: Attenuation State (INPUT INTO 26LS32 RECEIVER)	Pass	
18	Dc Power Supply:	+15V @ 250 mA MAX -15V @ 100 mA MAX	+15V @ 124 mA -15V @ 15 mA	PMI QA 1

QA/QC Approval:  PMI QA 1 Date: 8/31/18

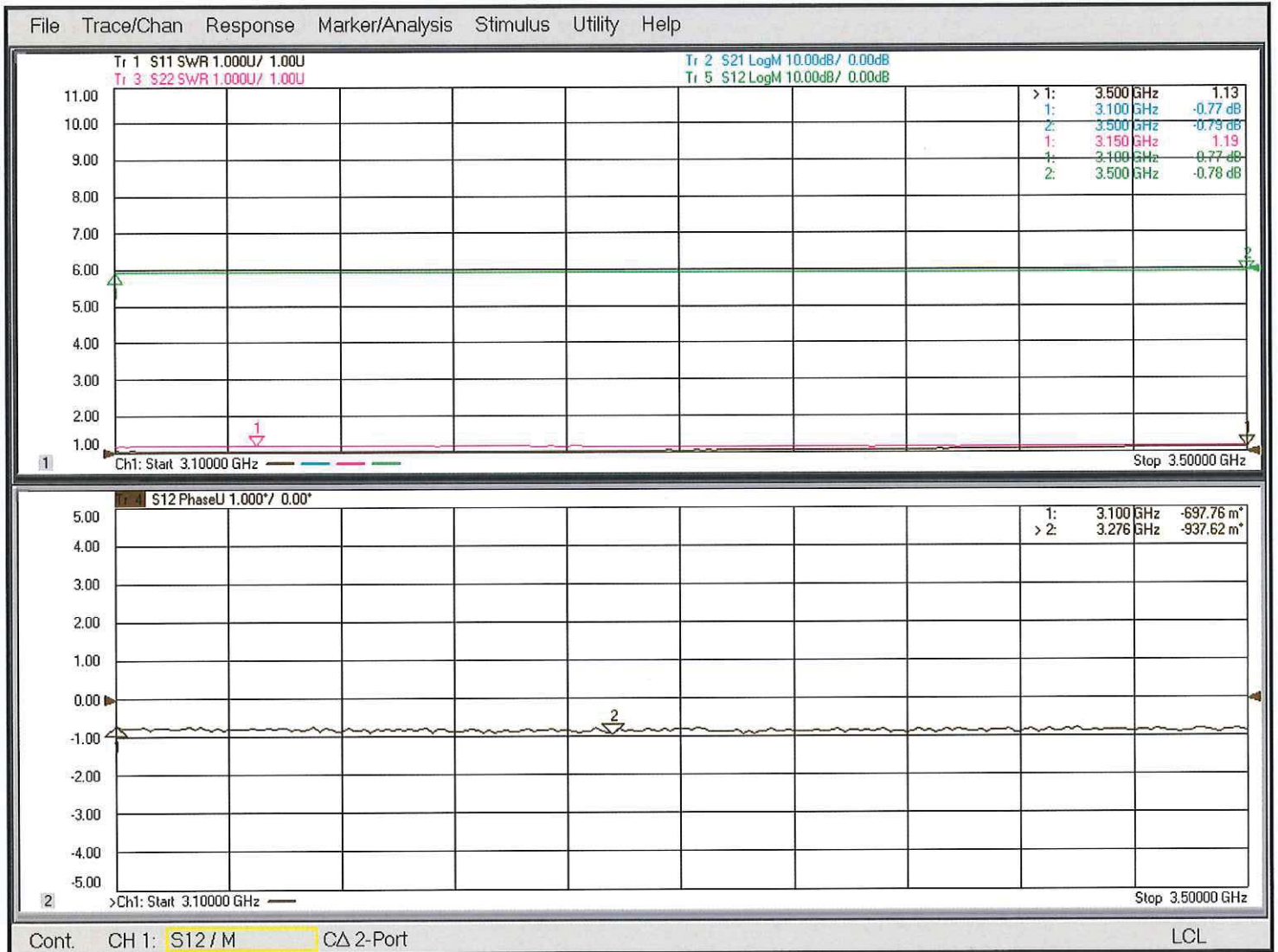
7311-F Grove Road, Frederick, MD 21704 USA
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 Email: sales@pmi-rf.com



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Low Loss State (Bi-Directional), Return Loss and Phase



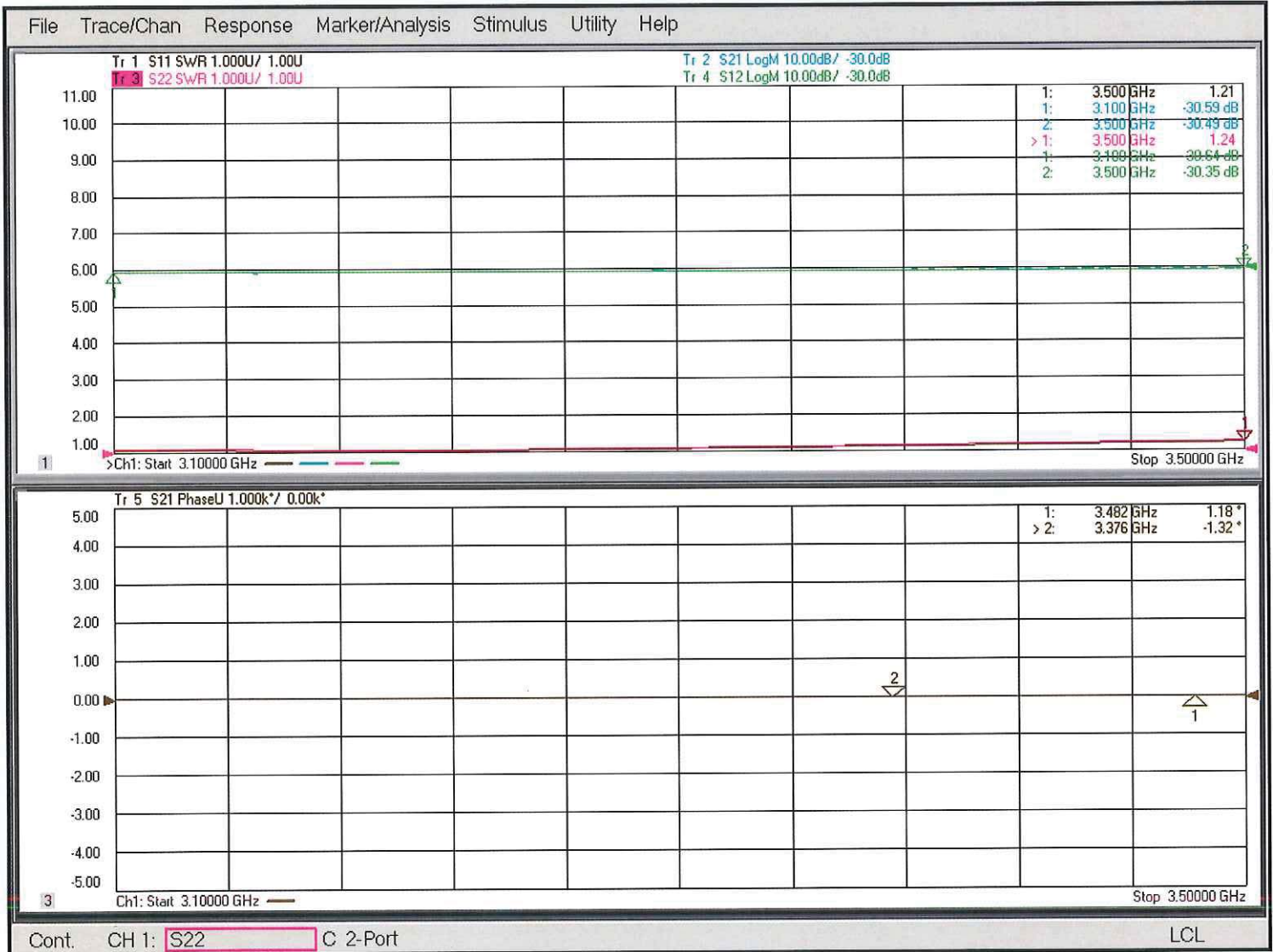
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Attenuation State (Bi-Directional), Return Loss and Phase



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