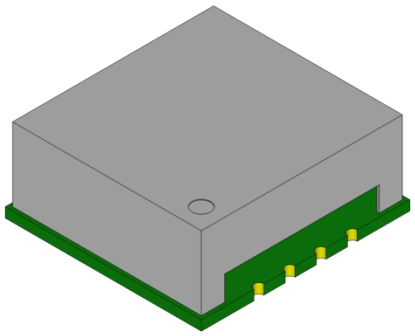
FEATURES:

- Ultra-Low Phase Noise
- Phase Locking to 10 MHz External Reference
- SMD Package
- Low Spurious
- +5V Supply Voltage
- -175 dBc/Hz Phase Noise >100 kHz Offset



ELECTRICAL SPECIFICATIONS:

Parameter	Min.	Typ.	Max.	Units	Conditions
Nominal Frequency (F)		100		MHz	
Frequency Stability (ΔF/F) (with Internal Reference)		± 0.28		ppm	vs. Temp
RF Output		+10		dBm	
Output Load		50		ohms	+/-10%
Phase Noise (Under Static Conditions) (with Internal Reference)		-85		dBc/Hz	10 Hz Offset
		-125		dBc/Hz	100 Hz Offset
		-150		dBc/Hz	1 kHz Offset
		-170		dBc/Hz	10 kHz Offset
		-175		dBc/Hz	100 kHz Offset
		-175		dBc/Hz	1 MHz Offset
Harmonics		-30		dBc	
Spurious			-80	dBc	
Internal/External Reference Selection (REF SEL)	TTL / Floating High 0V-0.35V: Low, 2.2V-3.3V: High REF SEL:Low / External Reference REF SEL:High / Internal Reference				
External Reference (EXT REF) Frequency		10		MHz	
External Reference (EXT REF) Power	0	2	5	dBm	
Operating DC Voltage	+4.75	+5	+5.25	Volts	
Operating DC Current		28		mA	

*Electrical specifications are given at 25C.

ENVIRONMENTAL SPECIFICATIONS:

Parameter	Min.	Typ.	Max.	Units
Operating Temperature Range	-40		+85	°C
Storage Temperature	-40		+85	°C

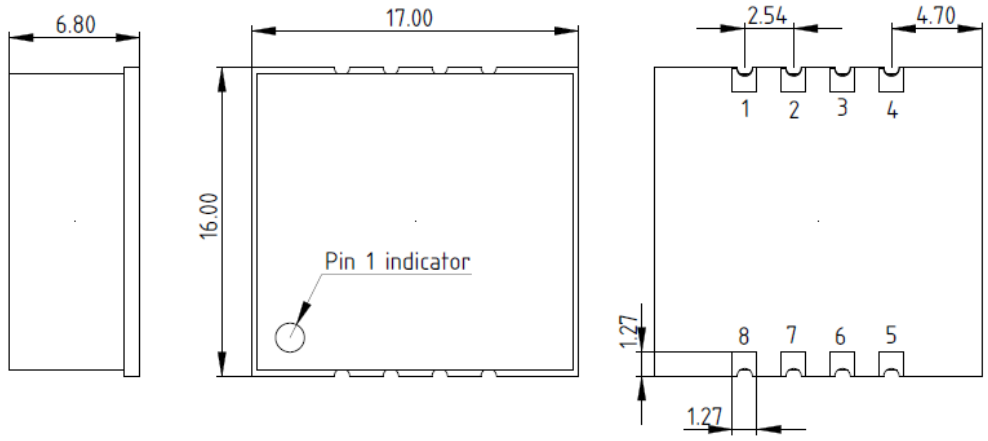
MECHANICAL SPECIFICATIONS:

Parameter	Value	Units
Dimensions (L x W x H)	17 x 16 x 6.8	mm
Weight	TBD	gr

MECHANICAL OUTLINE:

- All dimensions are in mm

- Tolerances: ± 0.3 mm



Pin #	Function
1	VCC
2	N/C
3	GND
4	GND
5	RF OUT
6	GND
7	EXT REF
8	REF SEL

REVISION HISTORY:

Rev. No.	Date	Description
P1	19-Aug-2026	Preliminary Release