

**SUMMARY TEST DATA
ON
PS-500M2G-10B-SFF-MIL-SM**

Customer: _____
Job No: _____
Model No: PS-500M2G-10B-SFF-MIL-SM
Serial No: PL52436/2515

Tested By: K. Mansfield
Date: Friday, April 11, 2025
Temp: +25°C
Drawing No: 27645660 Rev: A1

Data Taken at -10 dBm Input Power

Test Item No.	Parameters	Specified Value	Measured Value	QA QC
1	Frequency Range:	0.5 to 2 GHz	0.5 to 2 GHz	PMI QA4
2	Control:	10 BIT TTL	Pass	
3	Insertion Loss:	13 dB Max.	8.1 dB See Plot	
4	Phase Shift:	360° Typ.	360°	
5	Amplitude Error:	±1.0 dB Max.	±0.05 dB See Plot	
6	Phase Shift Error:	±5° Max.	+0° -0.4° See Plot	
7	Switching Speed:	500 ns Max.	See Typical Characteristics	
8	Spurious:	-60 dBc Max.	See Typical Characteristics	
8	Power Supply:	+15 V @ 250 mA Typ. -15 V @ 20 mA Typ.	+15 V @ 173 mA -15 V @ 29 mA	PMI QA4

** Device uses external latch/strb. Latch is triggered on the rising edge and reset on the falling edge before able to process next digital word. Reference state is all bits low "0". Control lines have an internal pull-down resistor.

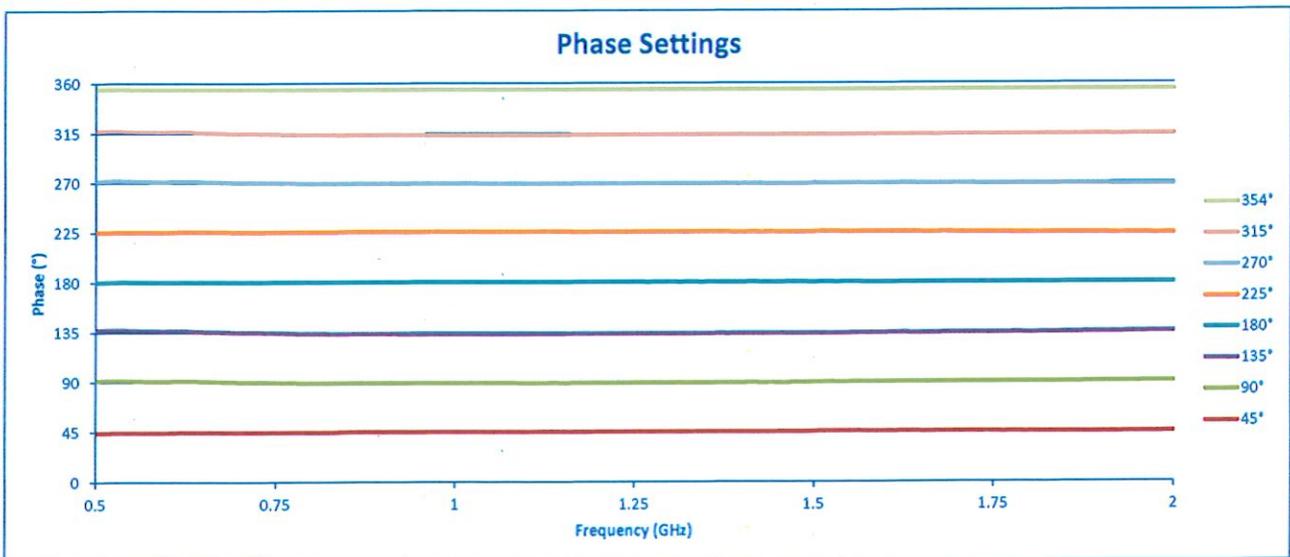
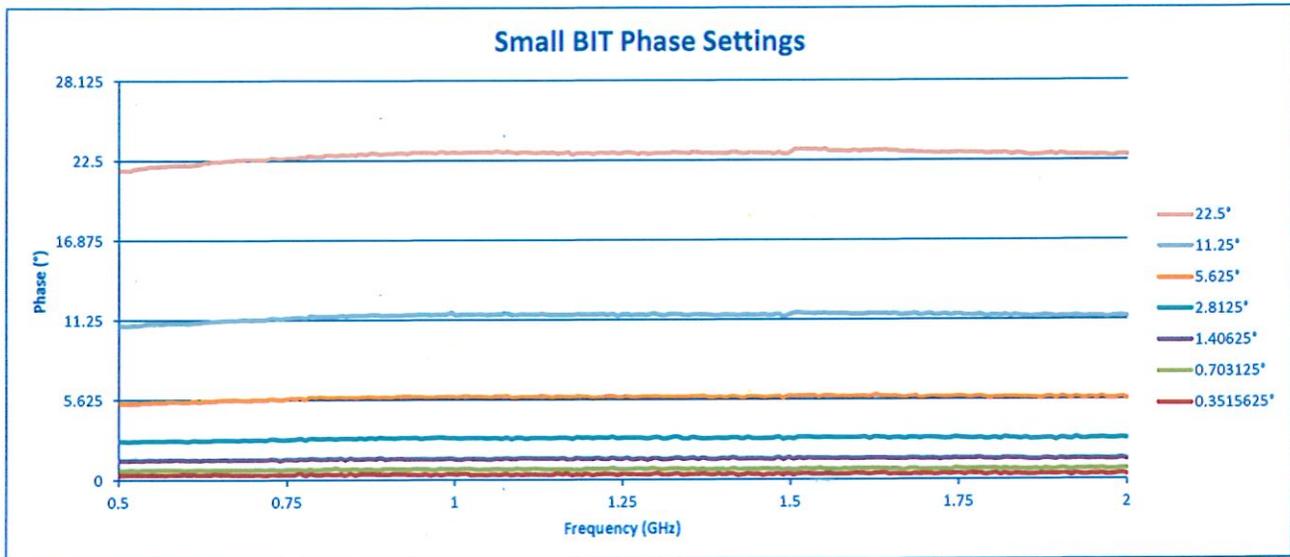
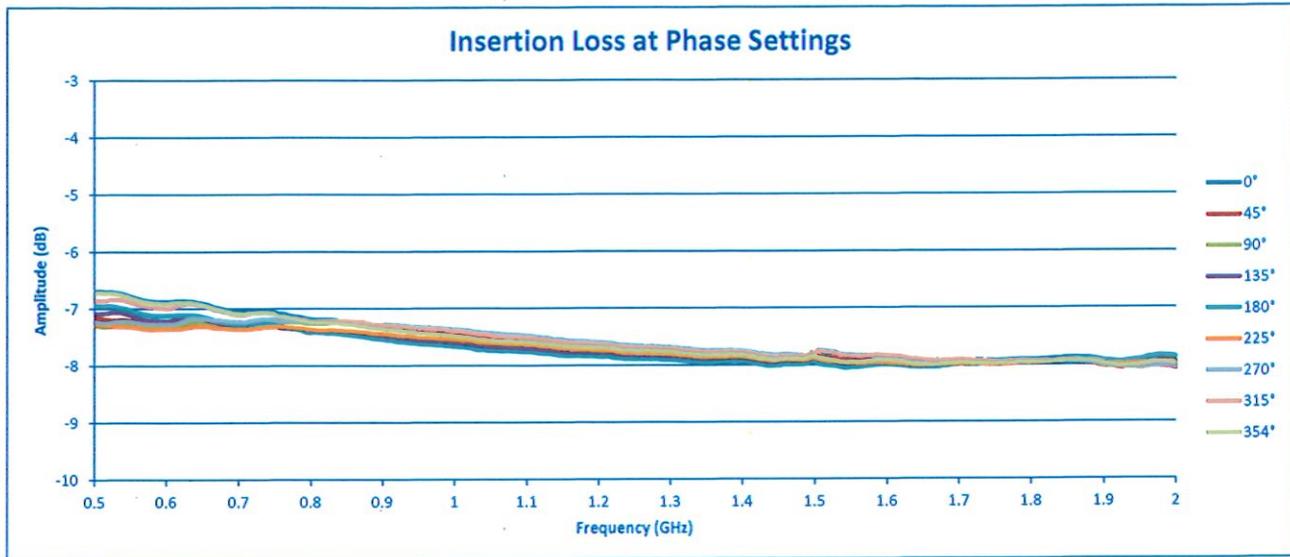
Programed Phase Shift	Phase Shift	Accuracy	Flatness
DEG	DEG	DEG	±DEG
0.3515625	0.40	-0.05	0.08
0.703125	0.73	-0.03	0.09
1.40625	1.45	-0.05	0.11
2.8125	2.85	-0.03	0.17
5.625	5.66	-0.04	0.29
11.25	11.29	-0.04	0.47
22.50	22.55	-0.05	0.76
45.00	45.20	-0.20	0.75
90.00	90.25	-0.25	1.31
180.00	180.19	-0.19	0.29

Programed Phase Shift	Phase Shift	Accuracy	Flatness
DEG	DEG	DEG	±DEG
33.75	33.87	-0.12	0.85
67.50	67.73	-0.23	0.55
101.25	101.51	-0.26	1.65
135.00	135.37	-0.37	1.92
168.75	168.98	-0.23	0.63
202.50	202.71	-0.21	0.66
236.25	236.52	-0.27	1.06
270.00	270.29	-0.29	1.69
303.75	304.13	-0.38	2.02
337.50	337.78	-0.28	1.13

QA/QC Approval: _____

Camurong Velazquez

Date: 4/15/25



SUMMARY TEST DATA
ON
PS-500M2G-10B-SFF-MIL-SM

