



Features

Standard 7x5x1.5mm ceramic 6-pad
Low phase jitter (0.7 ps max.)
Output frequency up to 320 MHz
Complementary Outputs

Picture of Part



Typical Applications

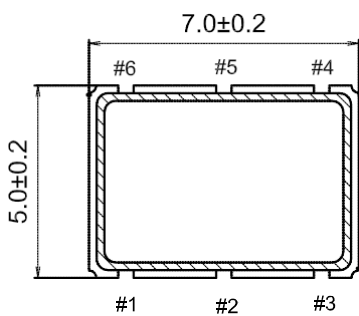
WLAN / WiMAX / WIFI
SONET / SDH / ATM
10 Gigabit Ethernet, Fiber Channel
xDSL

Description

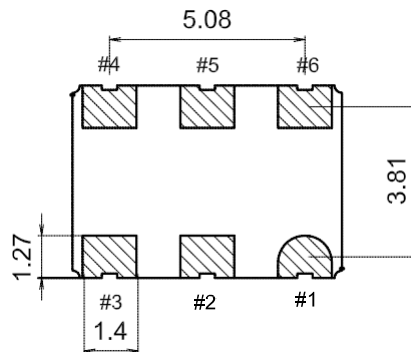
The XO3001 family offers ultra-low jitter PECL or LVDS outputs required of ever demanding data communications protocols around the world.

Mechanical Package and Test Circuit

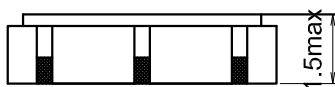
[TOP VIEW]



[BOTTOM VIEW]



[SIDE VIEW]



Pad	Function
#1	NC
#2	Tri-State
#3	GND
#4	Output
#5	Comp.Output
#6	Vcc



Specifications

XO Specification	Sym.	Condition	Value			Unit	Note
			Min.	Typ.	Max.		
Operational Frequency Range	f_0			125.00		MHz	
LVDS	H - level voltage	V_H			1.6	V	
	L - level voltage	V_L	0.9			V	
	Rise & Fall time		10% to 90%		1.0	ns	
Power supply							
Voltage	V_{cc}		2.375	2.5	2.625	V	
Current consumption	I_{cc}				50	mA	
Tri-state Enable / Disable		Outputs Active	0.8 Vcc				The customer chooses either Pin 1 or Pin 2 for Tri-state
Pin 1 or Pin 2		Outputs NOT Active			0.2 Vcc		
Frequency stability							
vs. temperature		-40 °C to +85 °C, ref 25 °C	-20.0		+20.0	ppm	
						ppm	
OVERALL Frequency Stability		Including Aging, Reflow, voltage,	-30.0		+30.0	ppm	Load, shock, vibration
Integrated phase jitter 12KHz to 20 MHz		For 125.00 MHz operating frequency			0.7	ps	
Aging	Per Year	Projected yearly aging after 30 days operation	-3.0		+3.0	ppm	
Environmental, mechanical conditions.							
Operating temperature range		-40 °C to +85 °C maximum range available					
Storage temperature range		-55 °C to 125 °C					