

## Dynamic Engineers Inc.

2550 Gray Falls Dr., Suite#128, Houston, TX, 77077 USA TEL: 1-281-870-8822 EMAIL:Sales@DynamicEng.com

#### **Features and Benefits**

Surface Mount Package High Stability 1.8V,2.5V,3.0V,3.3V Supply Voltage Operation CMOS Output 4-55MHz frequency range

### **Typical Applications**

Wireless, WLAN DSC, PDA, Notebook Mobile phone

#### Description

XO5300AP\_series offers high stability and a compact package to suit the different communication needs.

#### **Mechanical Drawing & Pin Connections**

Drawing No: MD18001' -1





LC) ' \$\$5 DSgYf]Yg

SMD 5x3.2mm Crystal Oscillator

PIN	CONNECTION					
1	"L"	OPEN or "H"				
·2	GND					
3	Z	OUTPUT				
4	V <sub>DD</sub>					
Z : high impedance						

Unit in mm 1mm = 0.0394 inches

Dynamic Engineers reserves the right to make changes to the company datasheet(s) along with other information contained inside; such as data tables and graphs without notification to potential customers who may have earlier revisions in their possession.



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## **Specifications**

Oscillator	Sym	Condition	Value			Unit	Note
Specification			Min.	Тур.	Max.	Onit	Note
Frequency Range	Fnom		4		55	MHz	
Standard Frequency			5.000, 10.000, 12.000, 16.000, 20.000, 24.000, 32.000, 40.000, 44.000			MHz	
RF Output	-		-				
Signal Waveform			CMOS				
Load	R∟		15			pF	
H-Level Voltage	V <sub>H</sub>		90% Vdd			V	
L- Level Voltage	VL				10%Vdd	V	
Duty Cycle		at 1/2 VDD level	45		55	%	
Rise/Fall time		10% VDD to 90% VDD level			5	ns	
Start up time					10	ms	
Power Supply							
Supply Voltage	Vs	±10%		1.8V,2.5V,3.0 V,3.3V		V	
Stand-by control voltage (Pin#1)		Internal crystal oscillation to be halted (Pin#1 = VIL)		VIH: 70% VDD min. VIL : 30% VDD max			
Input current (Pin#1 = Open or VIH)		No load			7.5	mA	
Stand-by current		Internal crystal oscillation to be halted (Pin#1 = VIL)			10	uA	Pin#1 = VIL
Frequency Adjustment Range							
Disable delay time					100	ns	
Enable delay time					10	ms	
Frequency Stability							
SSB Phase Noise		(at VDD = +1.8V & 48.000 MHz)	-135 dBc / Hz, Typical at 1 kHz offset -162 dBc / Hz, Typical at 1 MHz offset				
RMS jitter (12 kHz to 20.000 MHz band)		at VDD = +1.8V & 48.000 MHz		168		fs	
		The frequency	Option /	A Option B	Option C		
Overall conditions		stability is inclusive of frequency tolerance (+25°C), temperature stability(-40°C to 85°C ),input voltage change, load change, reflow frequency shift and aging (1st year at +25°C)	±15	±10	±8	ppm	
Environmental, Mechanical Conditions	4000 10 1	05%0					
Uperating temperature range -40°C to 85°C   Perform condition -20°C + 10°C for 10 cocondor + 170°C + 10°C for 1 to 2 minutes (probability)							
				$\sim 101 + 102 \text{ m}$	ues intenéat	1111111	
Absolute Max Patings	+250°C±1		70 C±10		ites (preneu	ling/	
Absolute Max. Ratings	-0.3V to +	4 0V DC	70 C±10 (			ling)	