



Dynamic Engineers Inc.

2550 Gray Falls Dr., Suite#128, Houston, TX, 77077 USA
TEL: 1-281-870-8822 EMAIL: Sales@DynamicEng.com

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Features and Benefits

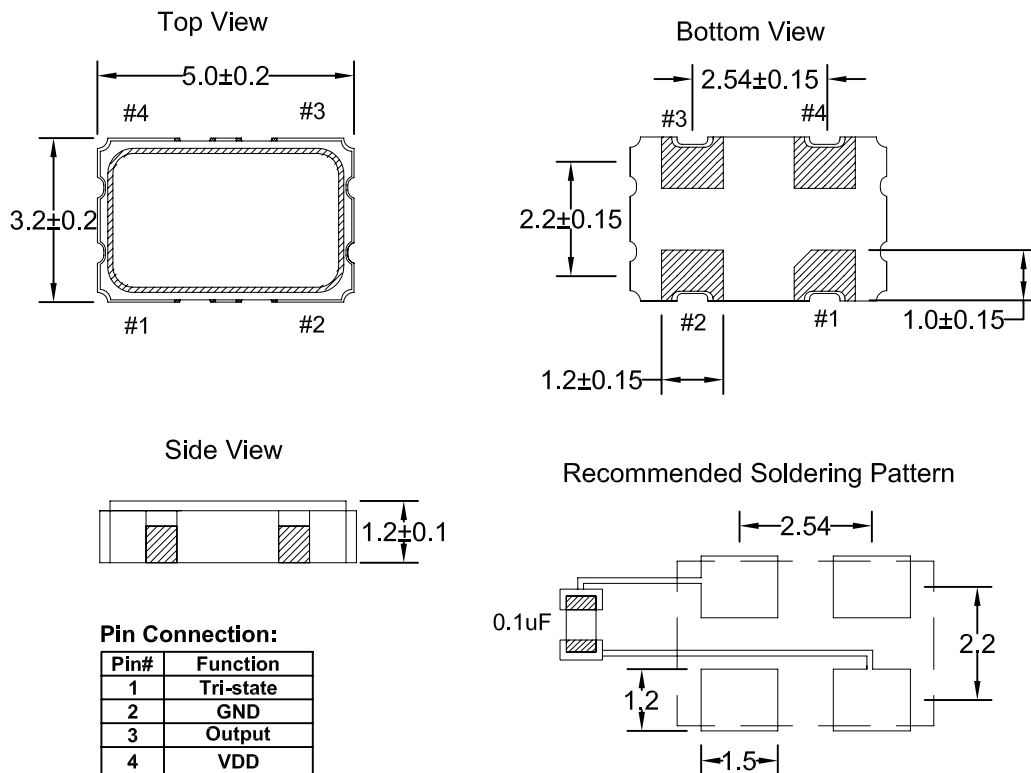
Typical 5.0 x 3.2 x 1.2 mm ceramic SMD package
Tight symmetry (45 to 55%) available
Realize the standby function with Tri-state

Typical Applications

GPS, mobile phone
WLAN, wireless, Fiber/10Gbit Ethernet
Notebook, PDA, DSC

Mechanical Drawing & Pin Connections

Drawing No: MD170028-1



Unit in mm
1mm = 0.0394 inches



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Specifications

| Oscillator Specification | | Sym | Condition | Value | | | Unit | Note |
|---|-----------------------------------|------------------|--|-------|--------|------|------|--|
| | | | | Min. | Typ. | Max. | | |
| Nominal Frequency | | F _{nom} | | | 25.000 | | MHz | |
| Supply Voltage | | V _{DD} | ±10% | | 3.3 | | V | |
| Supply Current | | | | | | 10 | mA | |
| Output Level (CMOS) | Output High (Logic "1") | | | 2.97 | | | V | |
| | Output Low (Logic "0") | | | | | 0.33 | | |
| Transition Time: Rise / Fall Time | | | Measured between 10% and 90% of VDD, output load of 15pF | | | 5 | nsec | |
| Start Time | | | | | | 5 | msec | |
| Output Drive Capability (CL) | | | | | | 15 | pF | |
| Tri-State (Input to Pin 1) | Enable (High Voltage or floating) | | | 2.31 | | | V | |
| | Disable (Low Voltage or GND) | | | | | 0.99 | | |
| Period Jitter | | | Pk-Pk | | | 40 | pSec | |
| RMS Phase Jitter | | | Integrated 12 kHz ~ 20MHz | | | 1 | pSec | |
| Standby Current | | | | | | 10 | µA | |
| Aging | | | @+25°C, 1 st year | | | ±3 | ppm | |
| Frequency Stability over -40°C to +85°C | | | at +25°C | | ±20 | | ppm | Inclusive of input voltage variation, load variation, aging (1 st year), shock, and vibration |
| Environmental Conditions | | | | | | | | |
| Operating temperature range | | -40°C to +85°C | | | | | | |
| Storage temperature range | | -55°C to +125°C | | | | | | |