



**SUMMARY TEST DATA  
ON  
TD-30T-SHS-218**

PL15395

Customer: _____	Tested By: <u>E Benson</u>
SO No: _____	Temperature: <u>+25°C</u>
Model No: <u>TD-30T-SHS-218</u>	Date: <u>01/19/15</u>
Serial No: <u>PL16395/1503</u>	Drawing No: <u>27609529</u> Rev: <u>A1</u>

TEST. ITEM NO	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA QC
1	Frequency Range:	2 TO 18 GHz	2 TO 18 GHz	
2	VSWR:	3.0: 1 Max @ -20dBm	2.90 : 1 (See Plot)	
3	Minimum Signal Level for Logic 1:	-20 dBm @ 2.0 GHz	- 21.3 dBm @ 2.0 GHz	
		-17 dBm @ 18.0 GHz	- 22.1 dBm @ 18.0 GHz	
4	Threshold Variation over Frequency (Any 1 GHz Window)	± 0.5 dB	± 0.48 dB	
5	Minimum Signal Level for Logic 0	-25 dBm	-24.8 dBm	
6	Dynamic Range:	+10 dBm Max	+10.0 dBm	
7	Propagation Delay from 50% of an input of -20 dBm	15 ns Max	See Typical Characteristics	
8	Propagation Delay from 50% of an input of +10 dBm	15 ns Max	See Typical Characteristics	
9	DC Supply:	+15VDC @ 100 mA Max	+15VDC @ 56.7 mA	
		-15VDC @ 100 mA Max	-15VDC @ 48.7 mA	

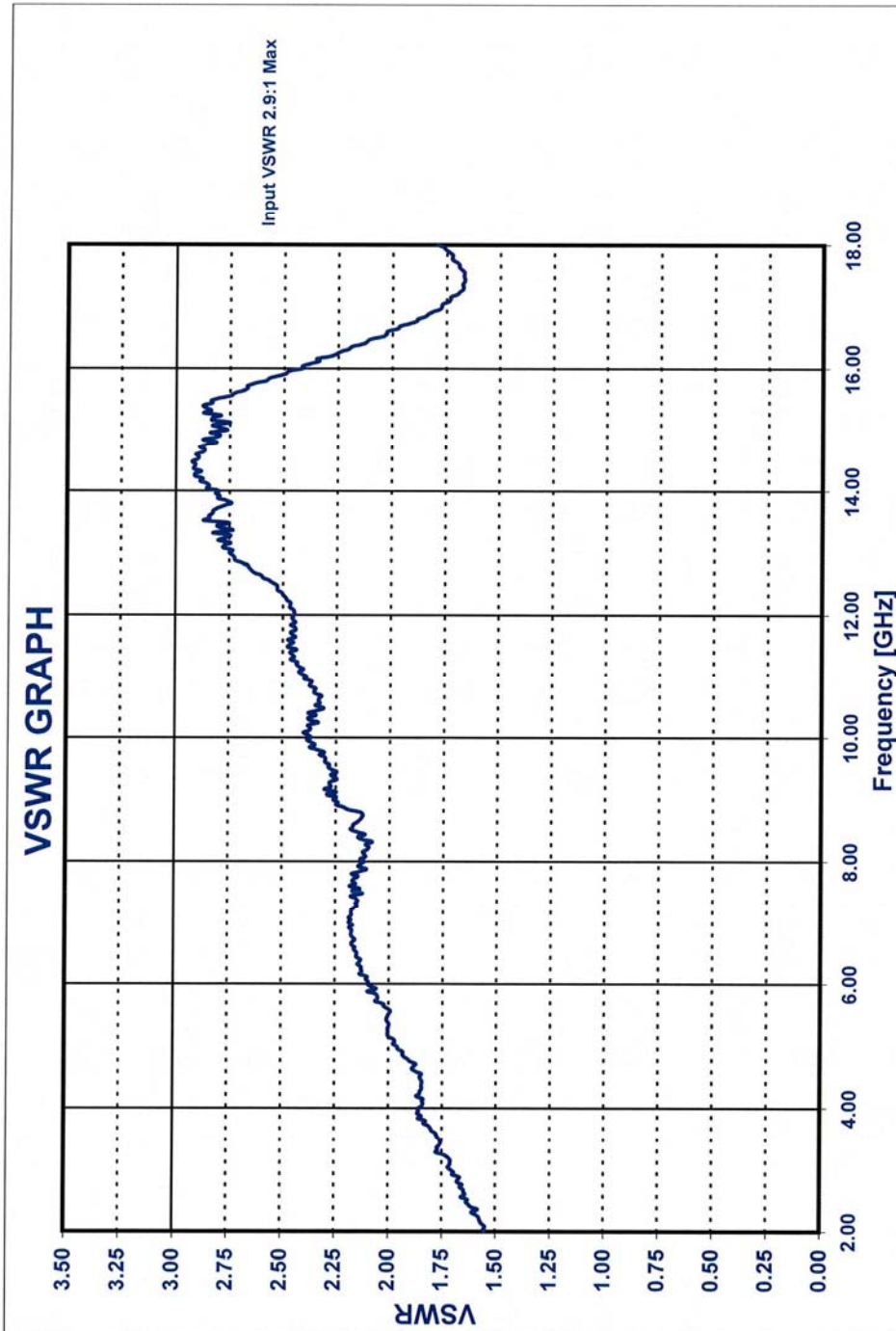
QA/QC Approval: \_\_\_\_\_ Date: \_\_\_\_\_



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**INPUT VSWR**





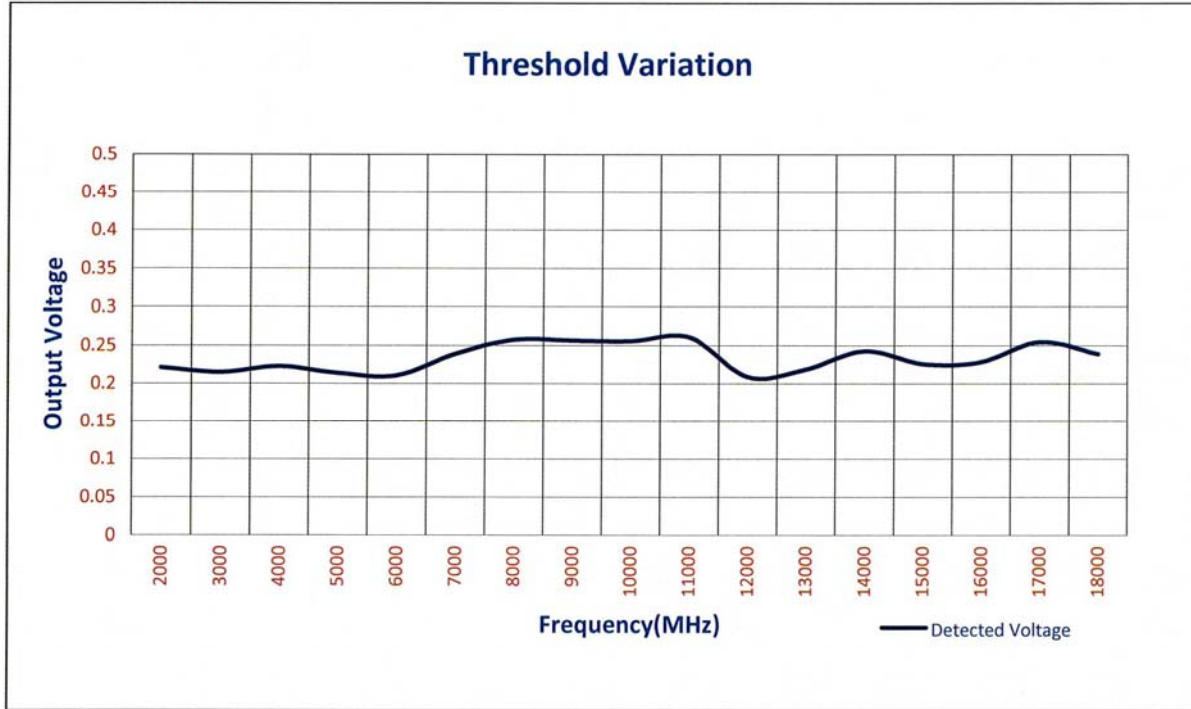
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Input Power= -15dBm

Variation= +/-0.48dB

Test Temp: +25C



Frequency	V-Out
2000MHz	0.22
3000MHz	0.214
4000MHz	0.222
5000MHz	0.213
6000MHz	0.21
7000MHz	0.238
8000MHz	0.257
9000MHz	0.256
10000MHz	0.255
11000MHz	0.26
12000MHz	0.208
13000MHz	0.218
14000MHz	0.242
15000MHz	0.225
16000MHz	0.228
17000MHz	0.254
18000MHz	0.238