



**SUMMARY TEST DATA  
ON  
APD-8-3D13D5-40W-SFF**

PL19241/1623

Customer: \_\_\_\_\_ Tested By: Jerry N.  
SO No: \_\_\_\_\_ Temperature: +25°C  
Model No: APD-8-3D13D5-40W-SFF Date: 05/23/2016  
Serial No: PL19241/1623 Drawing No: 27626081 Rev: A1

TEST. ITEM NO	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA QC
1	Frequency Range	3.1 TO 3.5 GHz	3.1 TO 3.5 GHz	
2	Insertion Loss	≤0.6 dB	0.51dB (See Plot)	
3	Isolation	≥23 dB	25.2dB (See Plot)	
4	VSWR	INPUT ≤1.30:1 OUTPUT ≤1.20:1	INPUT 1.21 :1 OUTPUT 1.15 :1 (See Plot)	
5	Amplitude Balance	≤ ±0.2 dB	±0.07dB (See Plot)	
6	Phase Balance	≤ ±2°	1.5° (See Plot)	
7	Power Handling	Forward ≤40 W Reverse ≤1 W	40W 1W	
8	Impedance	50 Ω	50 Ω	

Note: Amplitude & phase balance relative to J1 Port

QA/QC Approval: \_\_\_\_\_ Date: \_\_\_\_\_

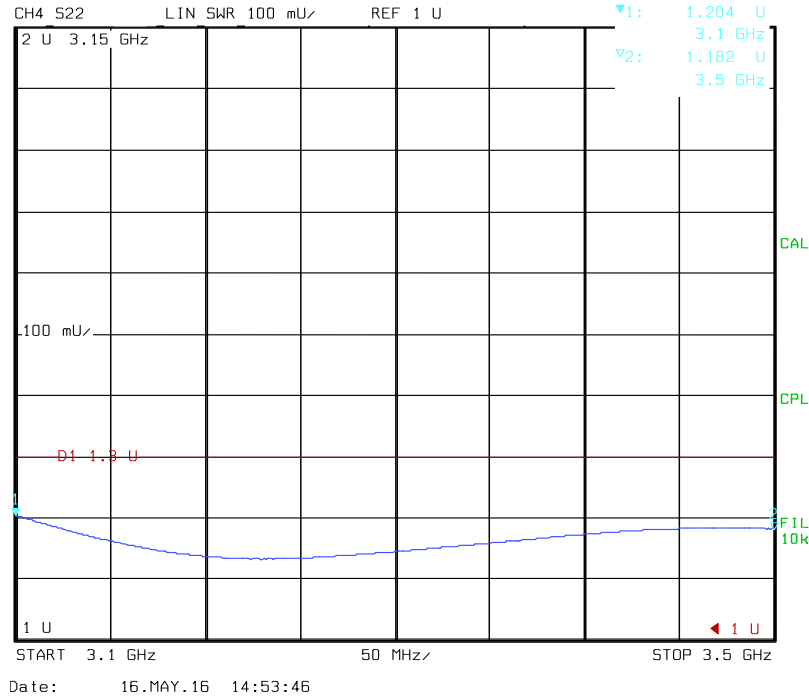
7311-F Grove Road Frederick, MD 21704 USA Phone: (301)662-5019 Fax: (301)662-1731  
Email: [sales@pmi-rf.com](mailto:sales@pmi-rf.com)



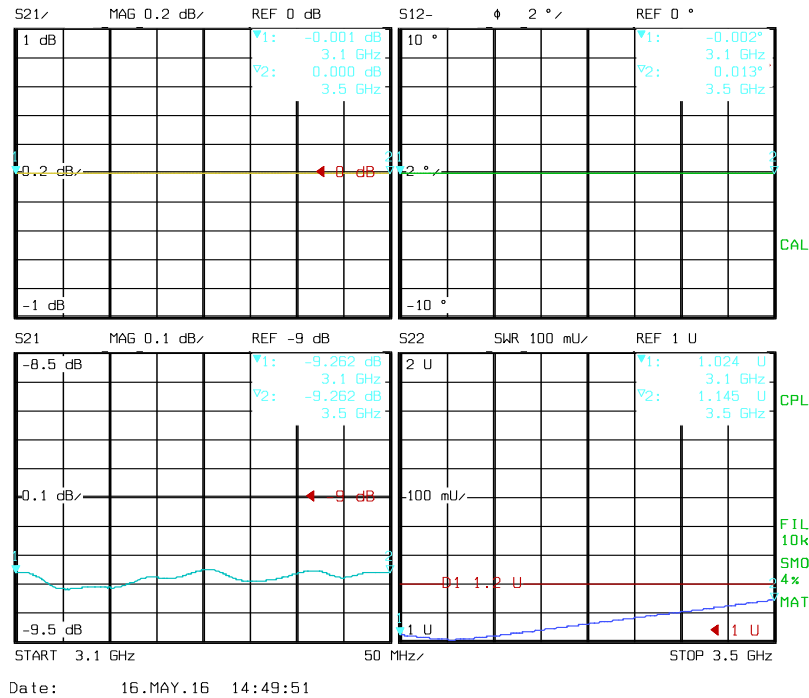
**SUMMARY TEST DATA  
ON  
APD-8-3D13D5-40W-SFF**

PL19241/1623

**J0 Input VSWR**



**J0 to J1 Amplitude Balance, Insertion Loss, Phase balance, J1 VSWR**

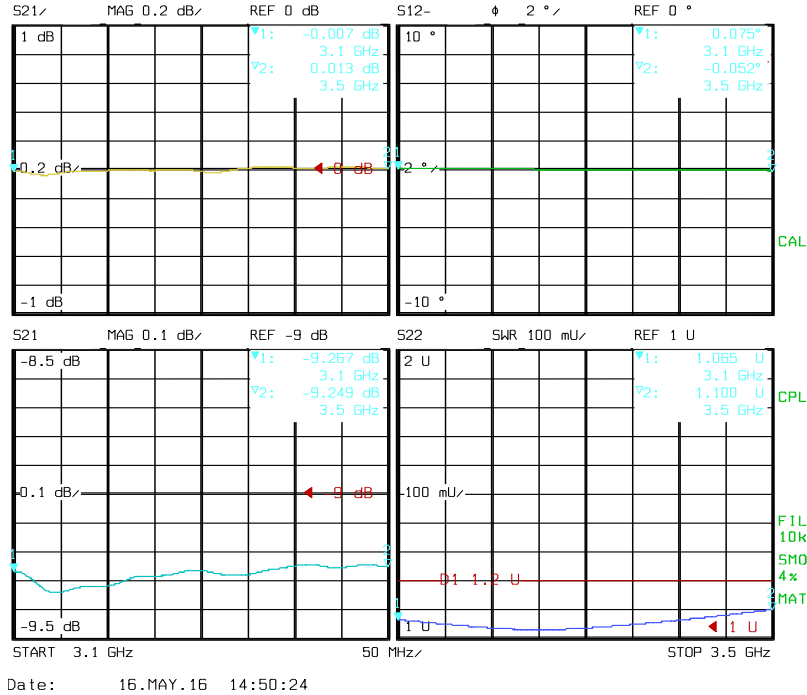




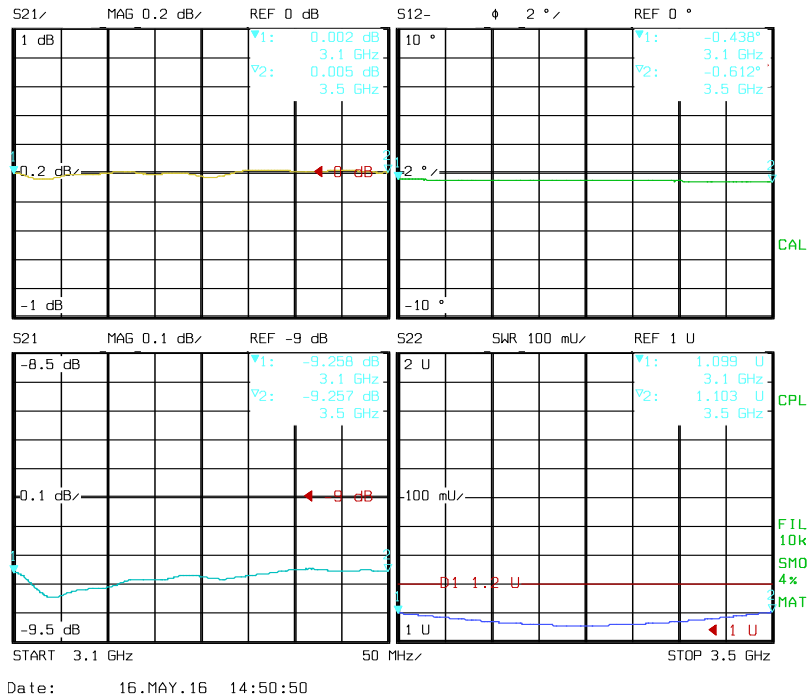
**SUMMARY TEST DATA  
ON  
APD-8-3D13D5-40W-SFF**

PL19241/1623

**J0 to J2 Amplitude Balance, Insertion Loss, Phase balance, J2 VSWR**



**J0 to J3 Amplitude Balance, Insertion Loss, Phase balance, J3 VSWR**

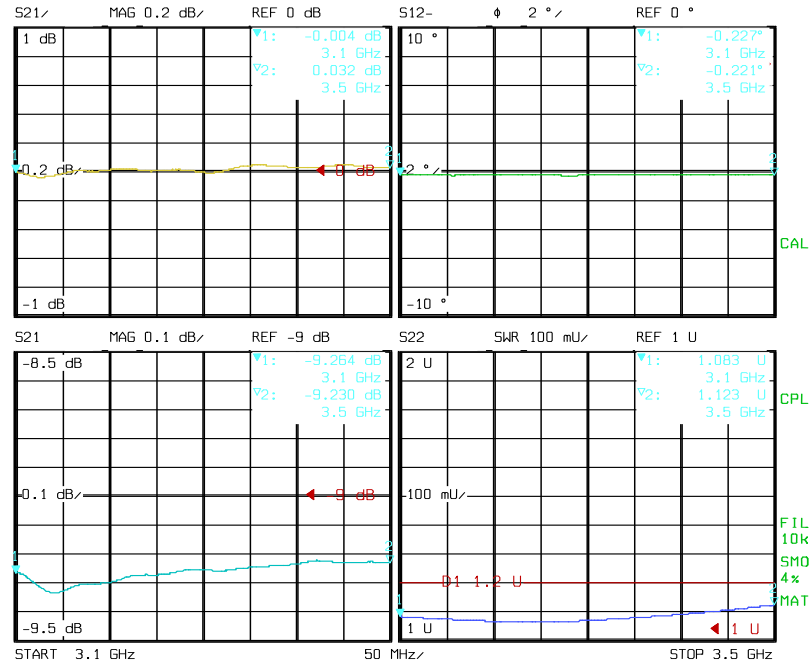




**SUMMARY TEST DATA  
ON  
APD-8-3D13D5-40W-SFF**

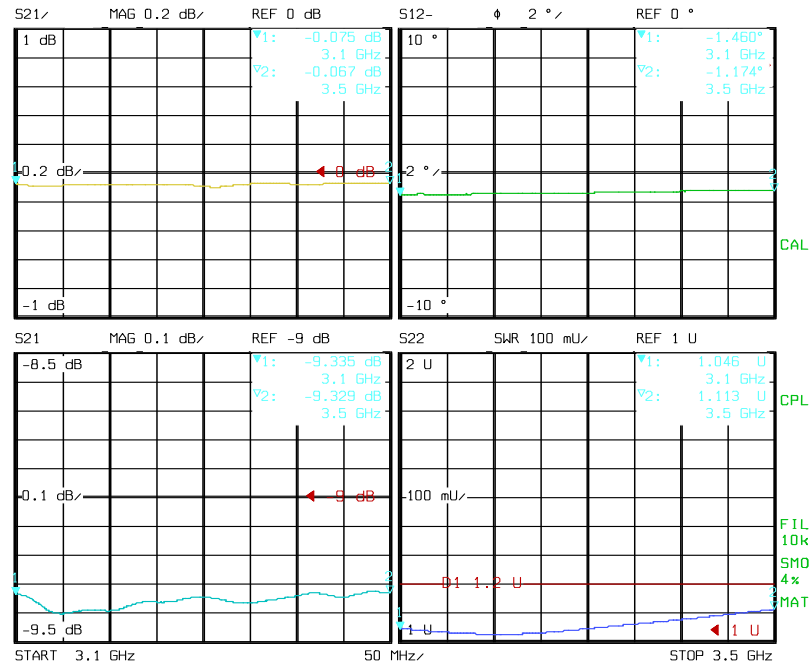
PL19241/1623

**J0 to J4 Amplitude Balance, Insertion Loss, Phase balance, J4 VSWR**



Date: 16.MAY.16 14:51:12

**J0 to J5 Amplitude Balance, Insertion Loss, Phase balance, J5 VSWR**



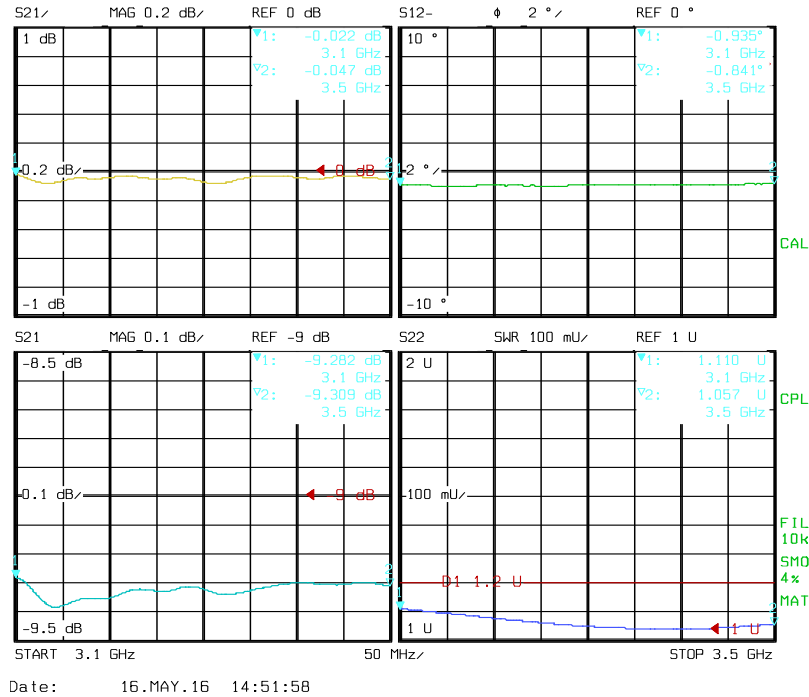
Date: 16.MAY.16 14:51:34



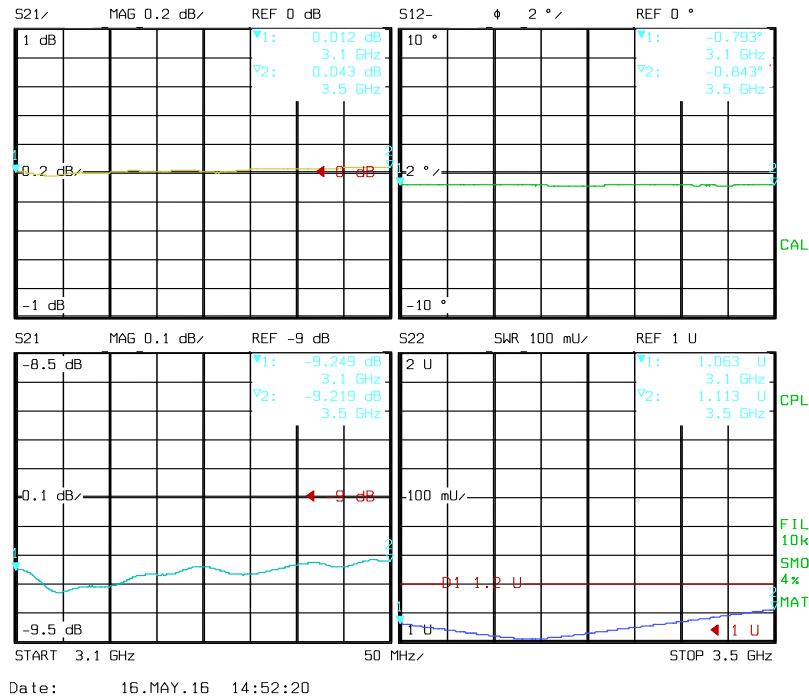
**SUMMARY TEST DATA  
ON  
APD-8-3D13D5-40W-SFF**

PL19241/1623

**J0 to J6 Amplitude Balance, Insertion Loss, Phase balance, J6 VSWR**



**J0 to J7 Amplitude Balance, Insertion Loss, Phase balance, J7 VSWR**

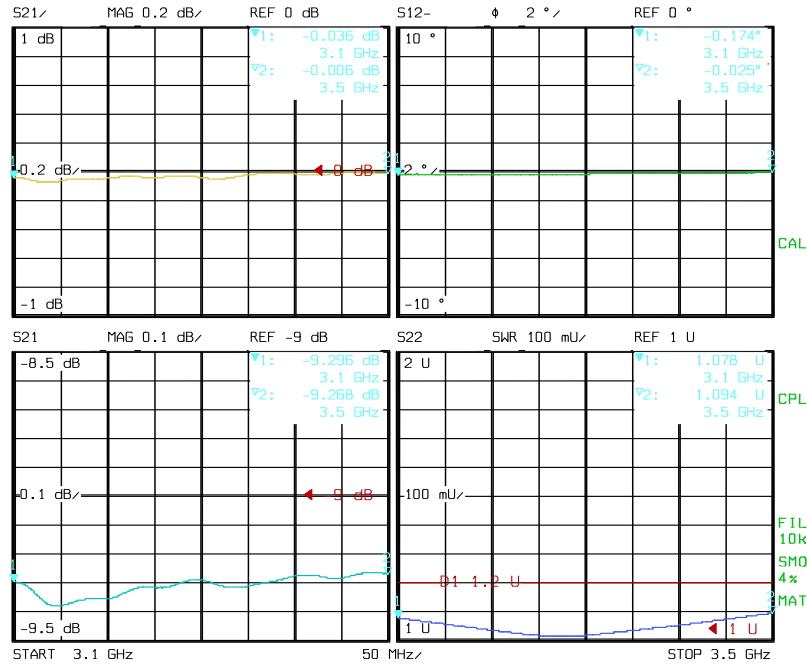




# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

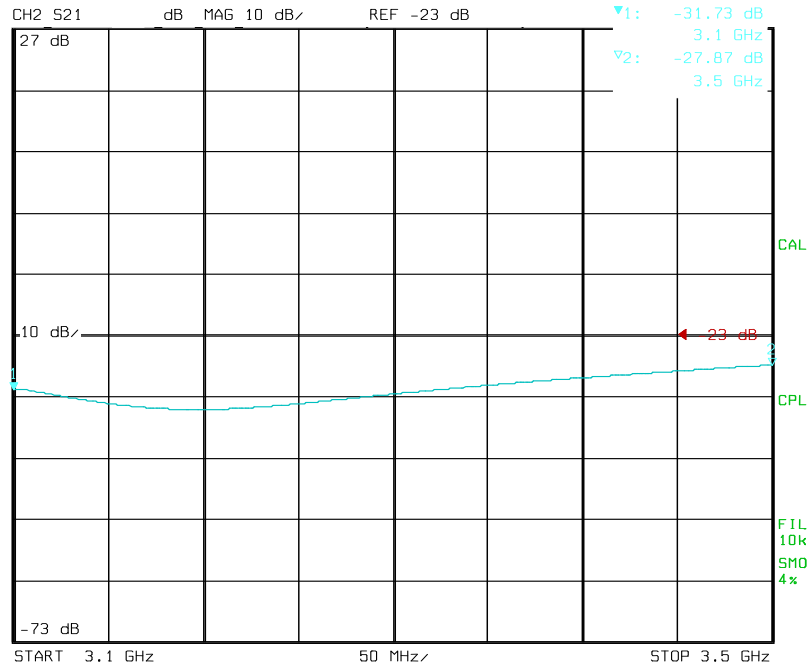
PL19241/1623

## J0 to J8 Amplitude Balance, Insertion Loss, Phase balance, J8 VSWR



Date: 16.MAY.16 14:52:46

## J1- J2 Isolation



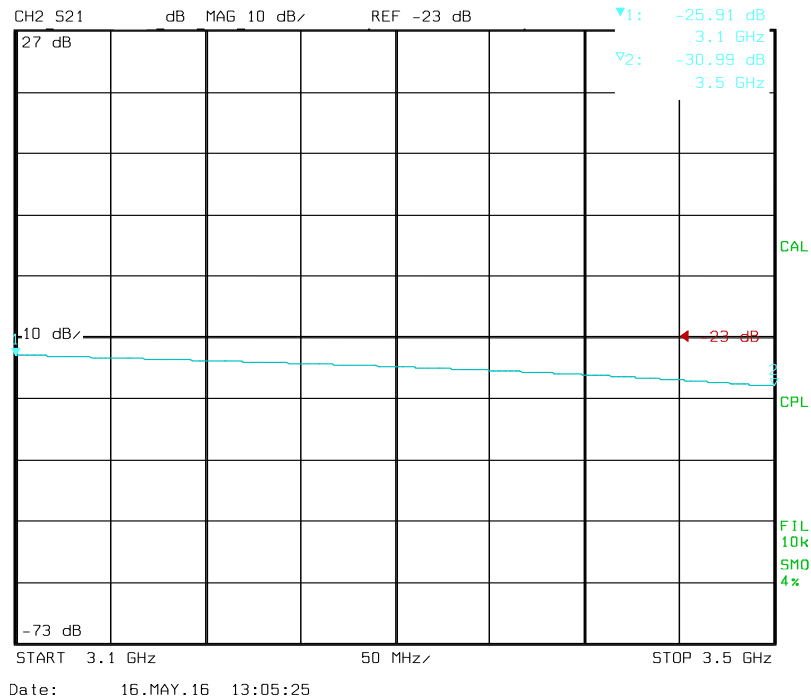
Date: 16.MAY.16 13:04:46



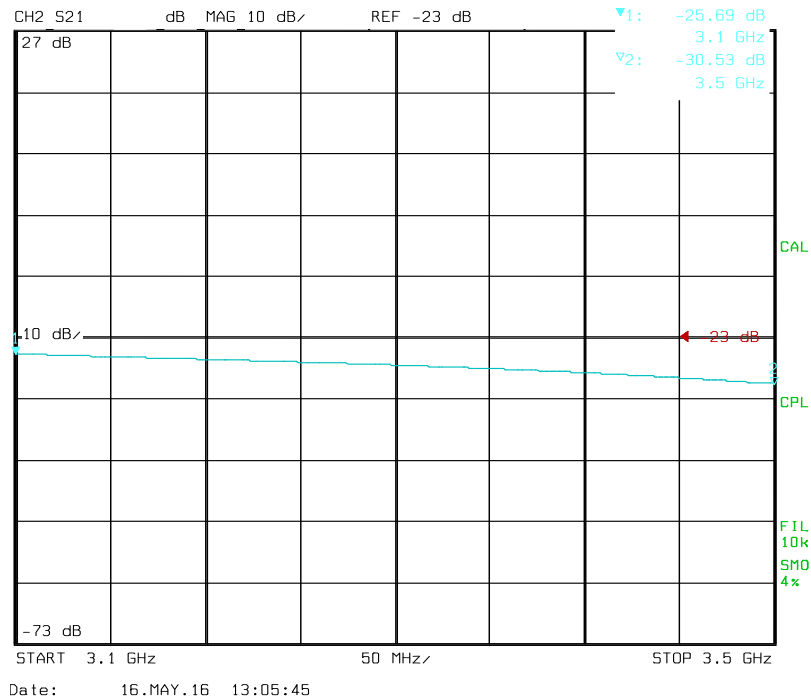
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J1- J3 Isolation



## J1- J4 Isolation

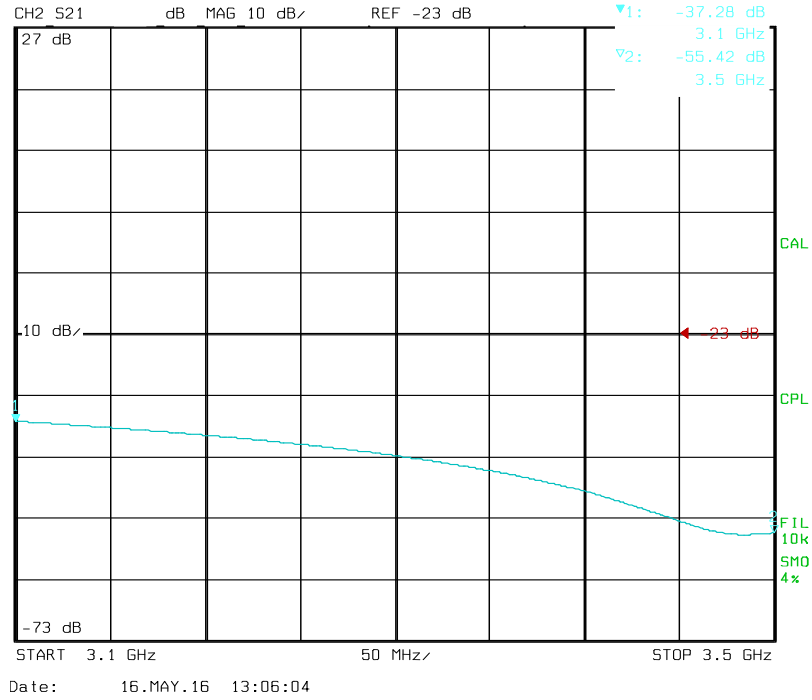




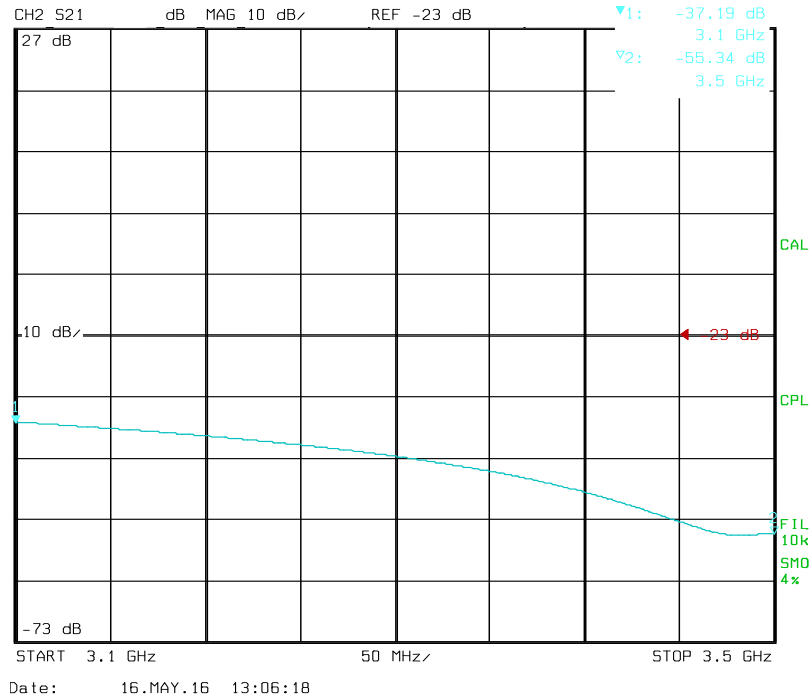
**SUMMARY TEST DATA  
ON  
APD-8-3D13D5-40W-SFF**

PL19241/1623

**J1- J5 Isolation**



**J1- J6 Isolation**



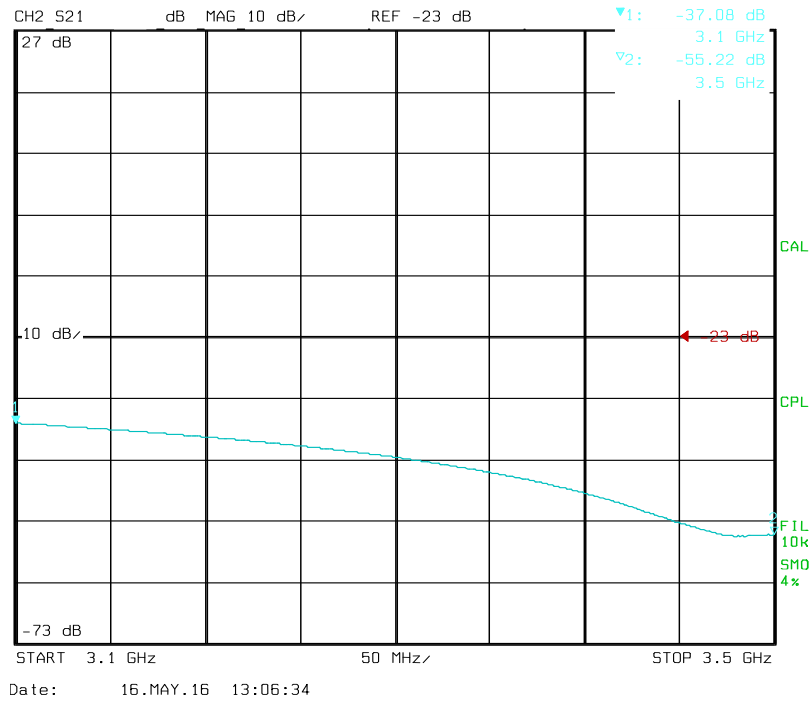




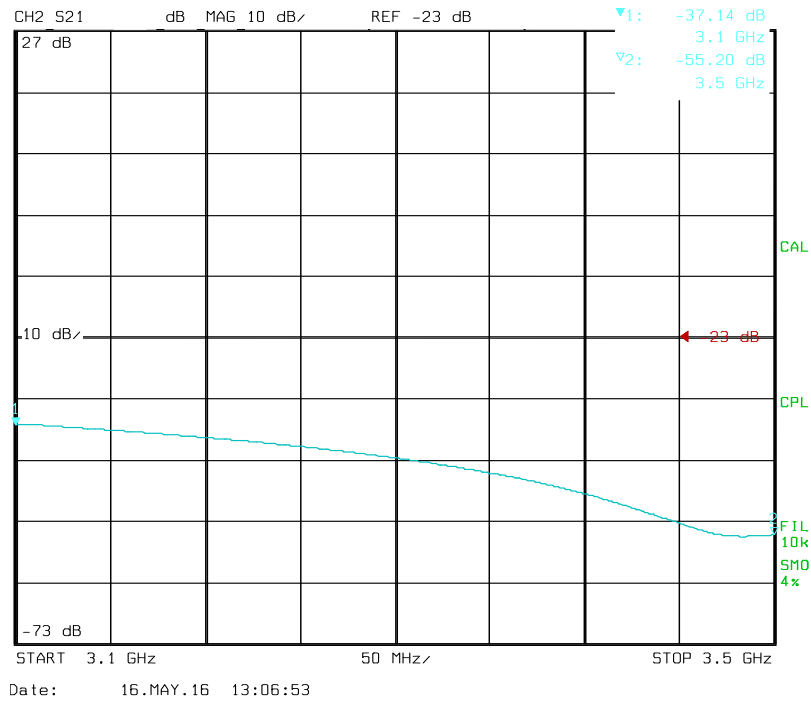
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J1- J7 Isolation



## J1- J8 Isolation

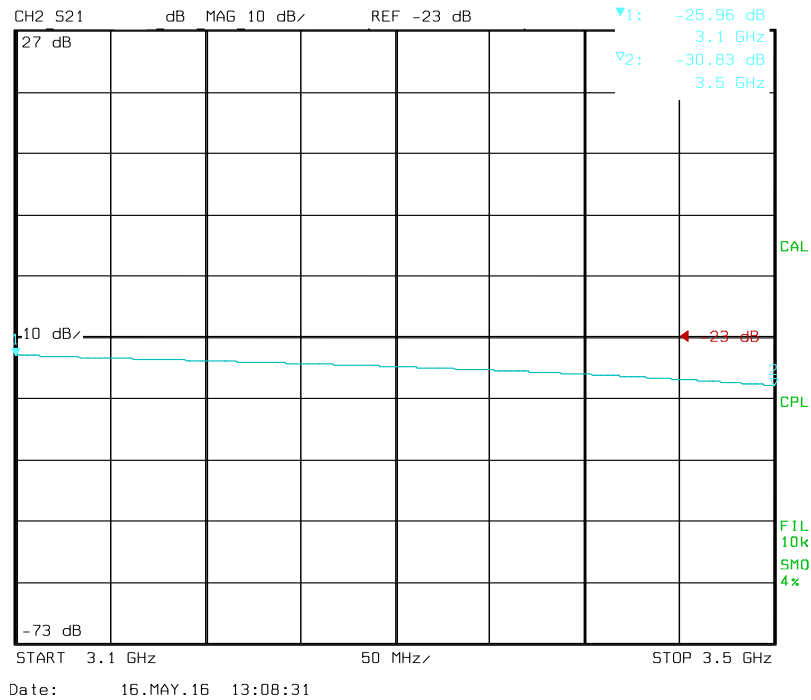




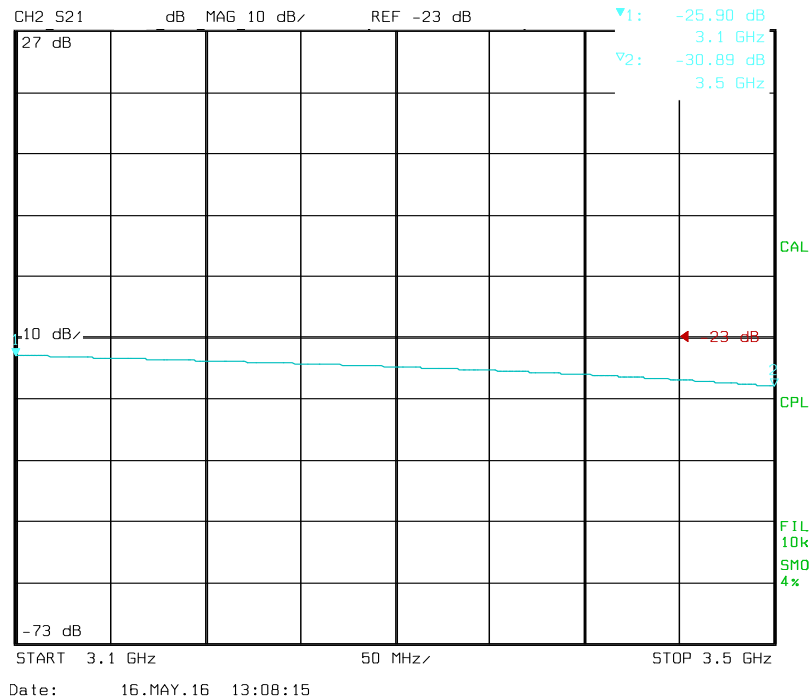
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J2- J3 Isolation



## J2- J4 Isolation

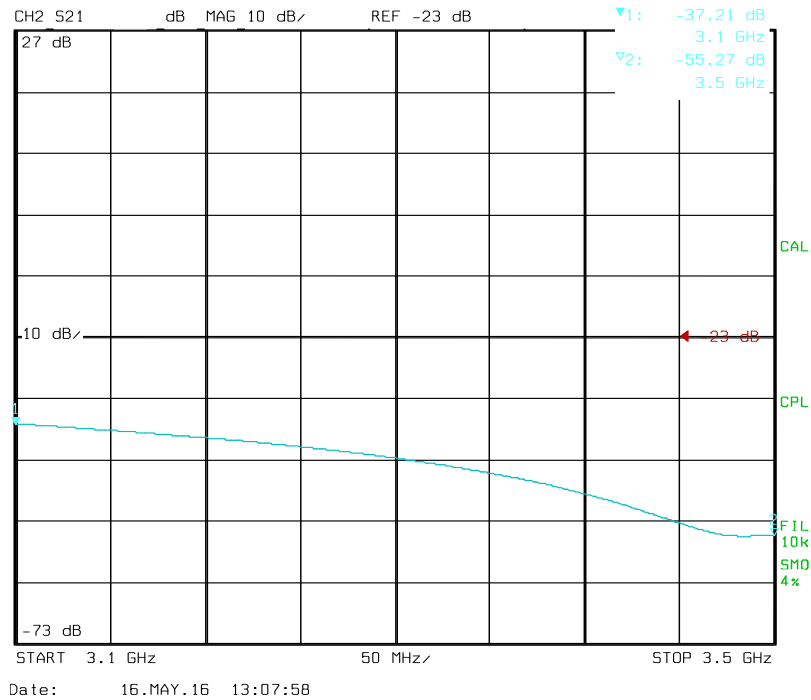




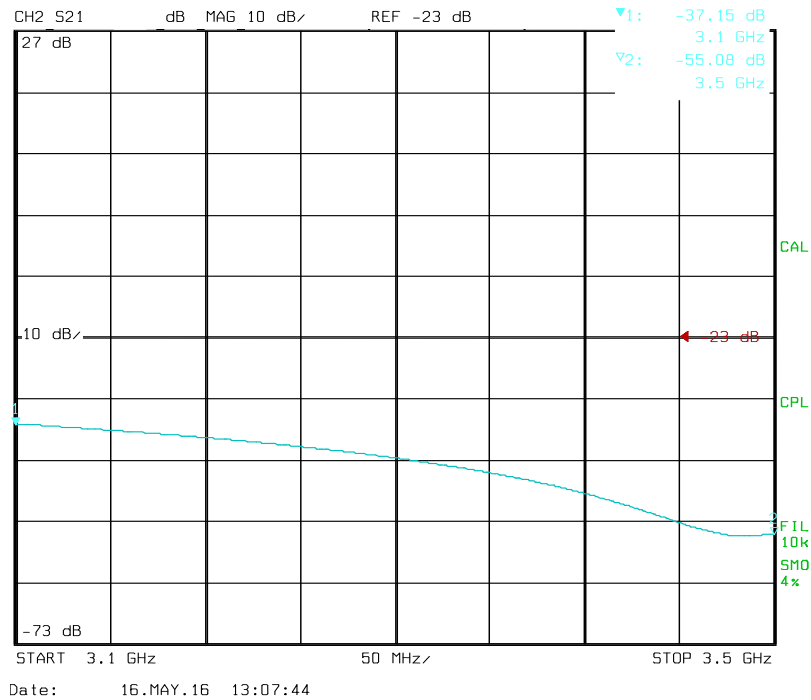
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J2- J5 Isolation



## J2- J6 Isolation

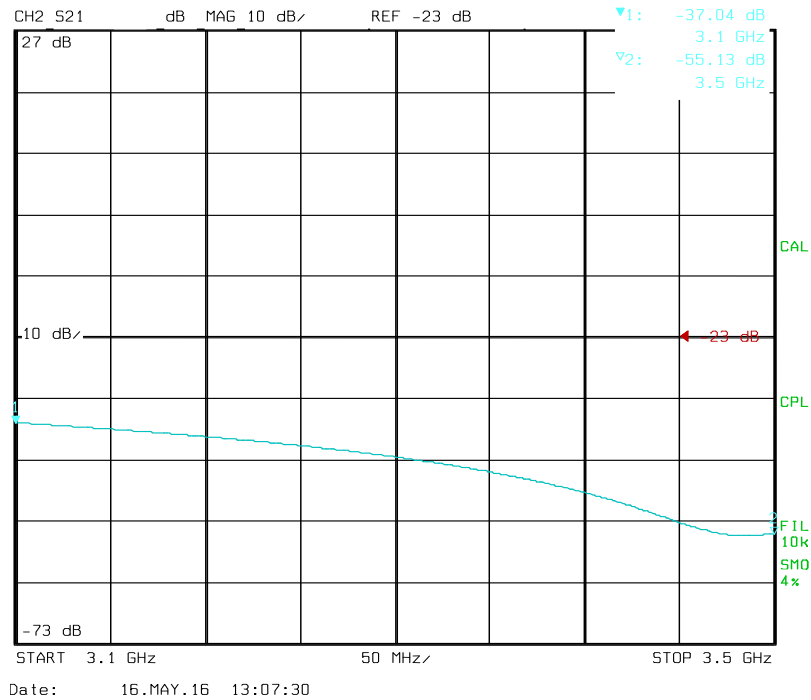




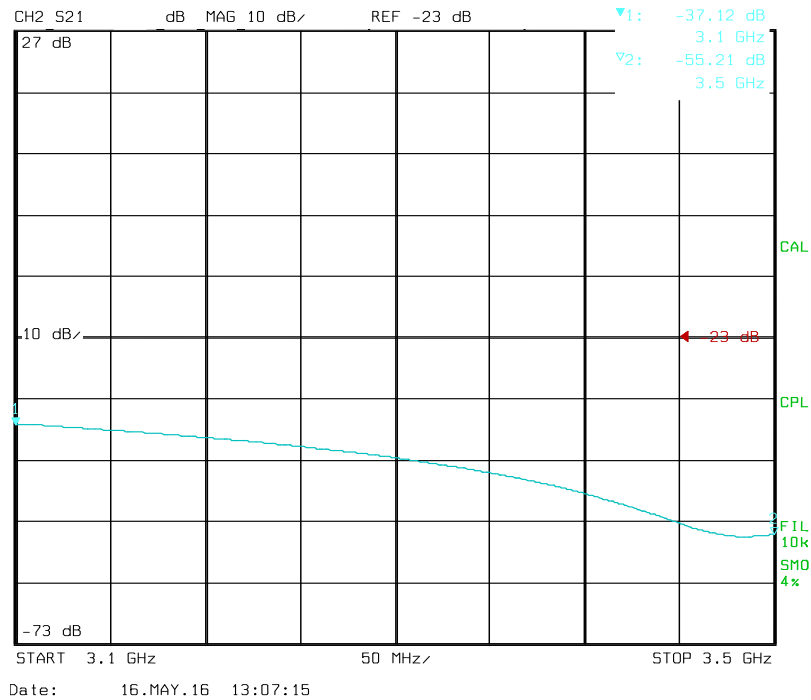
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J2- J7 Isolation



## J2- J8 Isolation

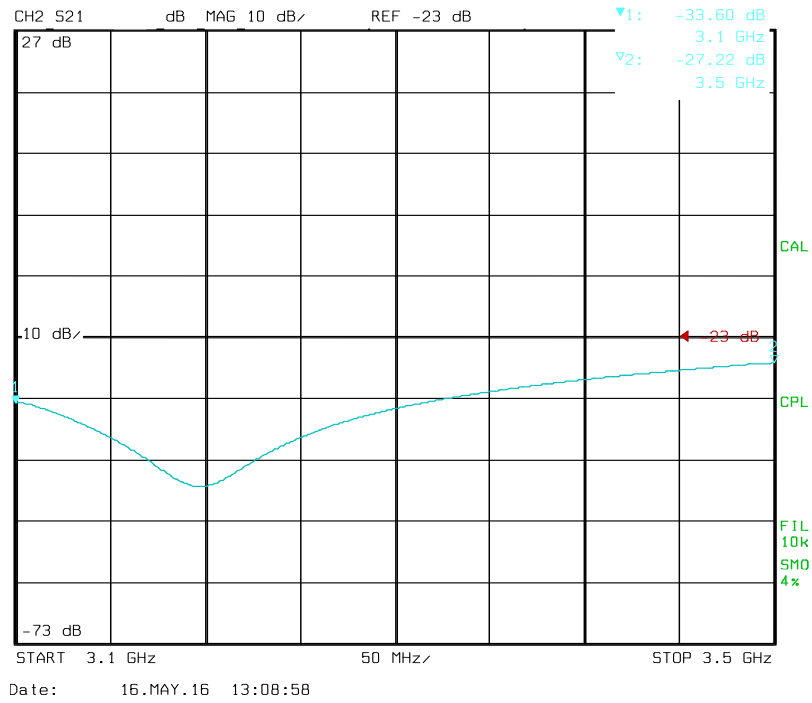




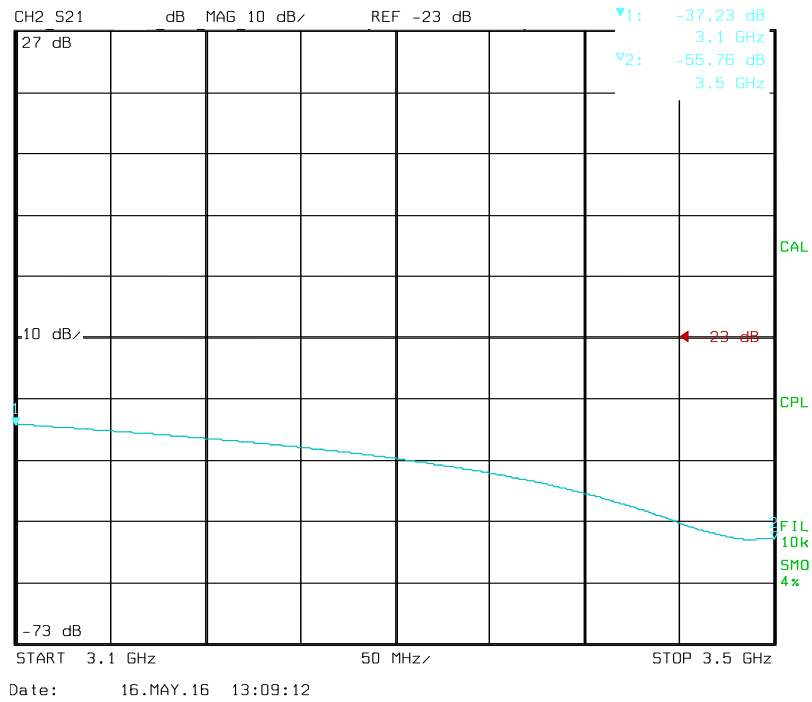
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J3- J4 Isolation



## J3- J5 Isolation

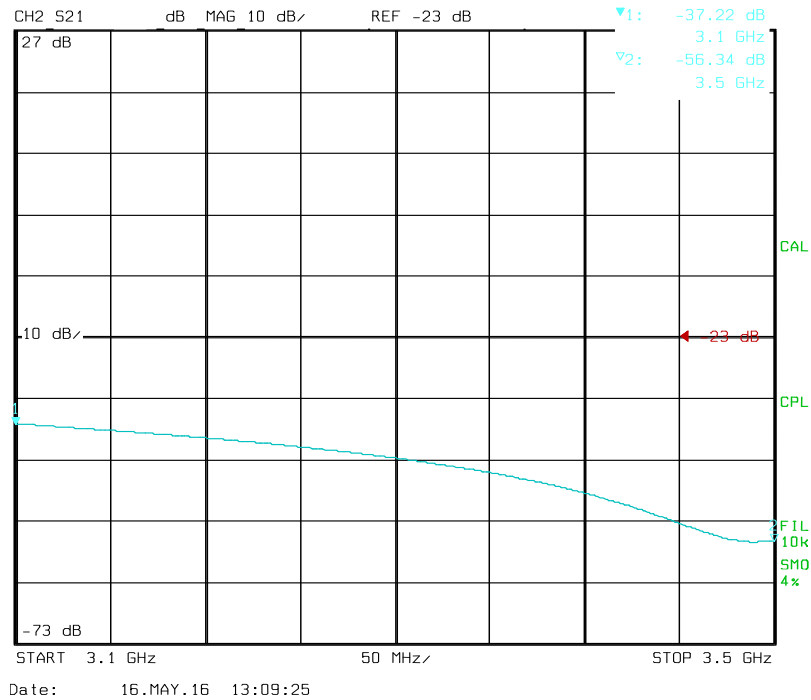




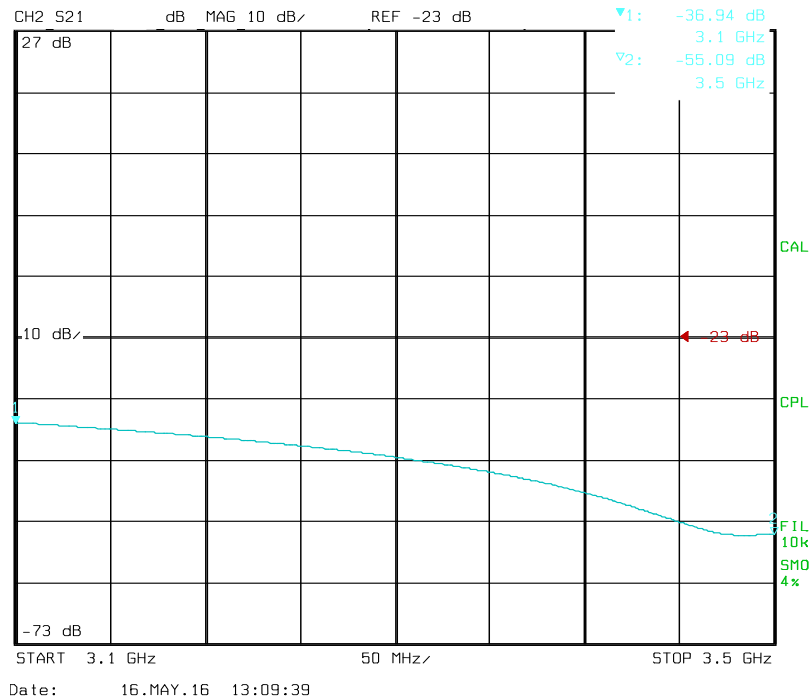
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J3- J6 Isolation



## J3- J7 Isolation

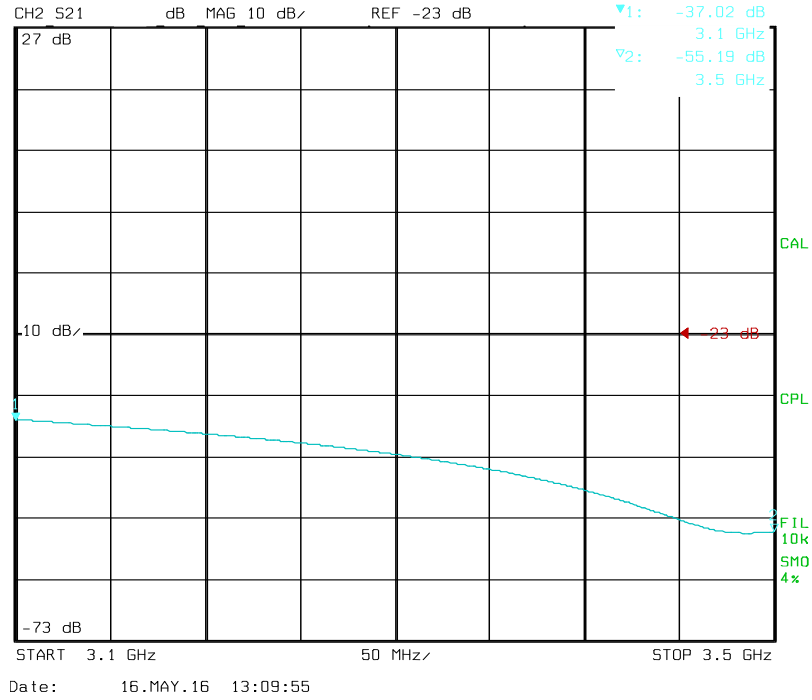




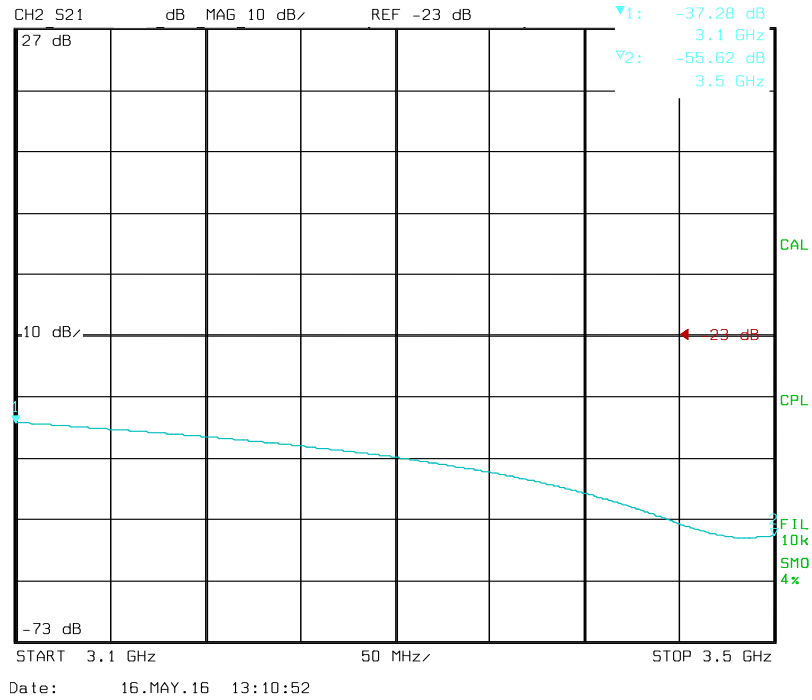
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J3- J8 Isolation



## J4- J5 Isolation

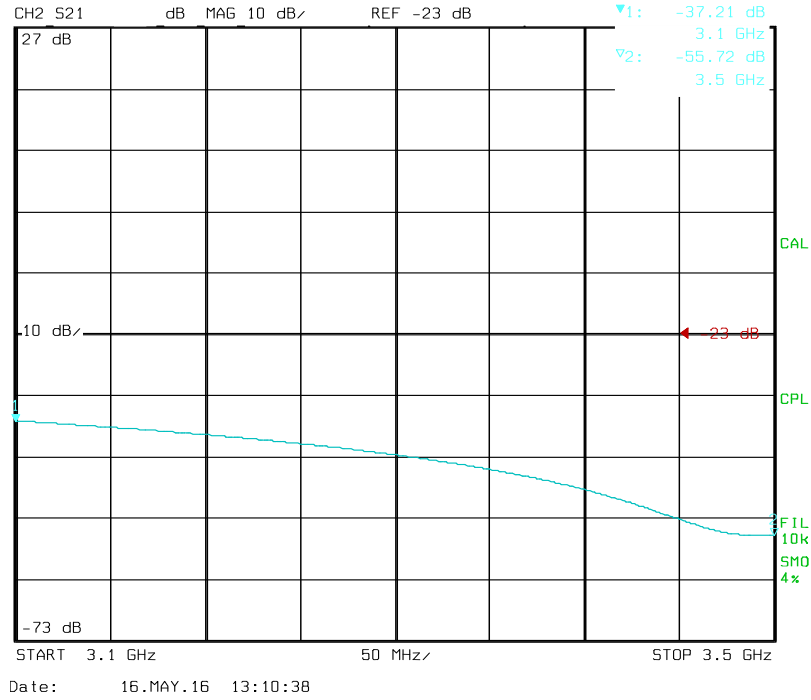




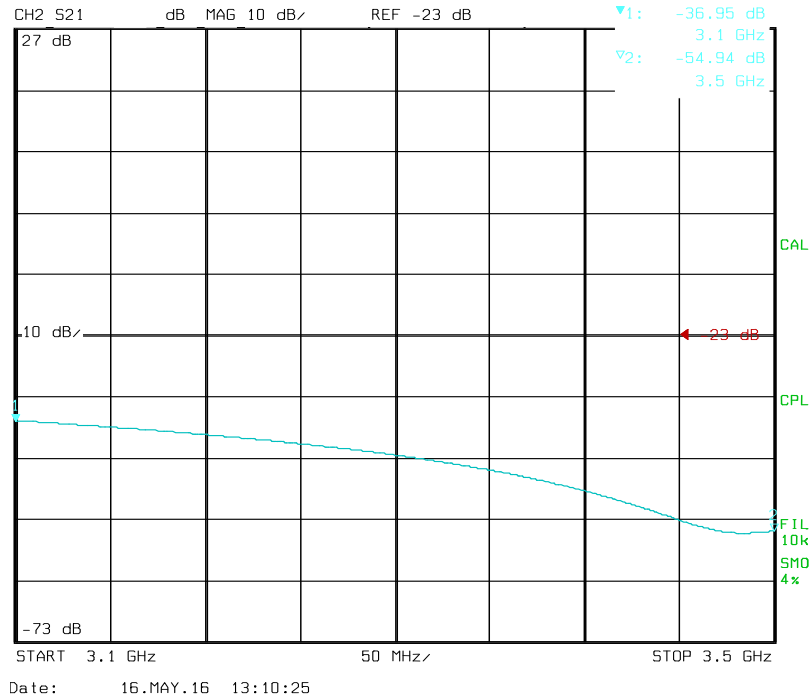
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J4- J6 Isolation



## J4- J7 Isolation



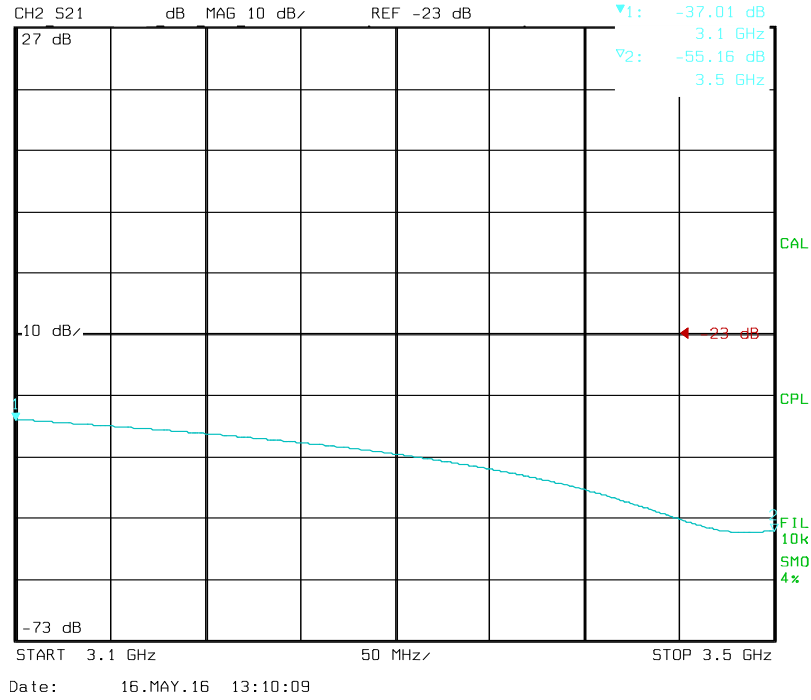




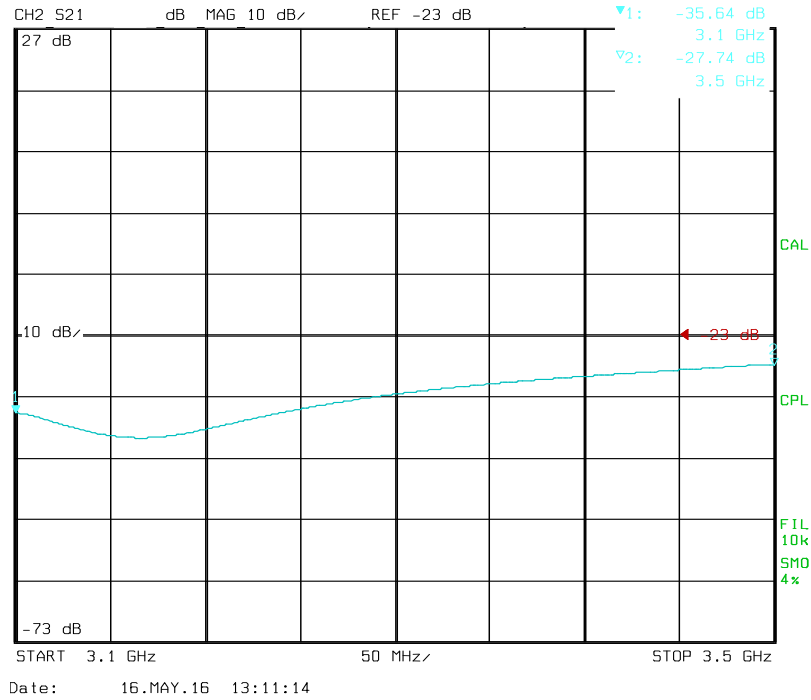
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J4- J8 Isolation



## J5- J6 Isolation

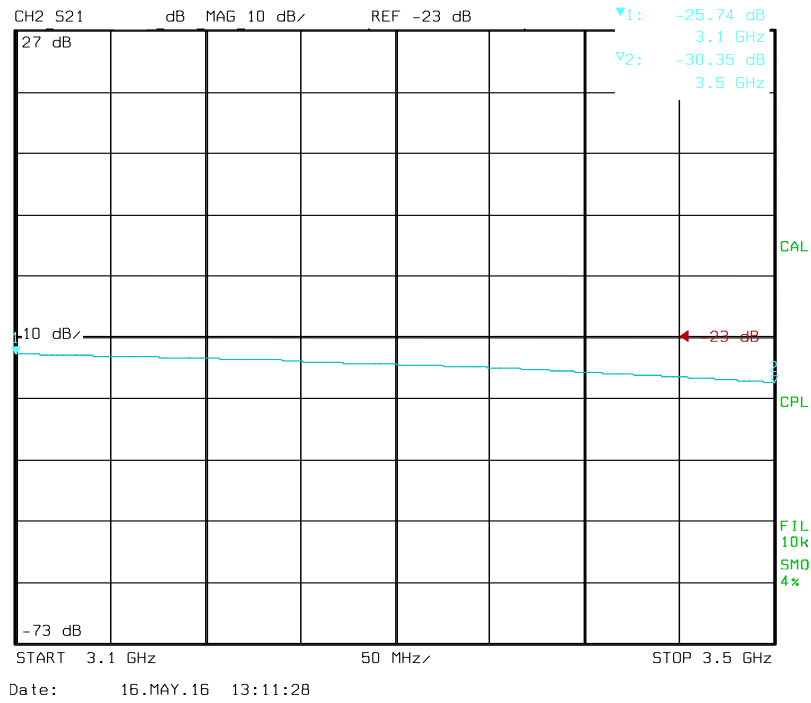




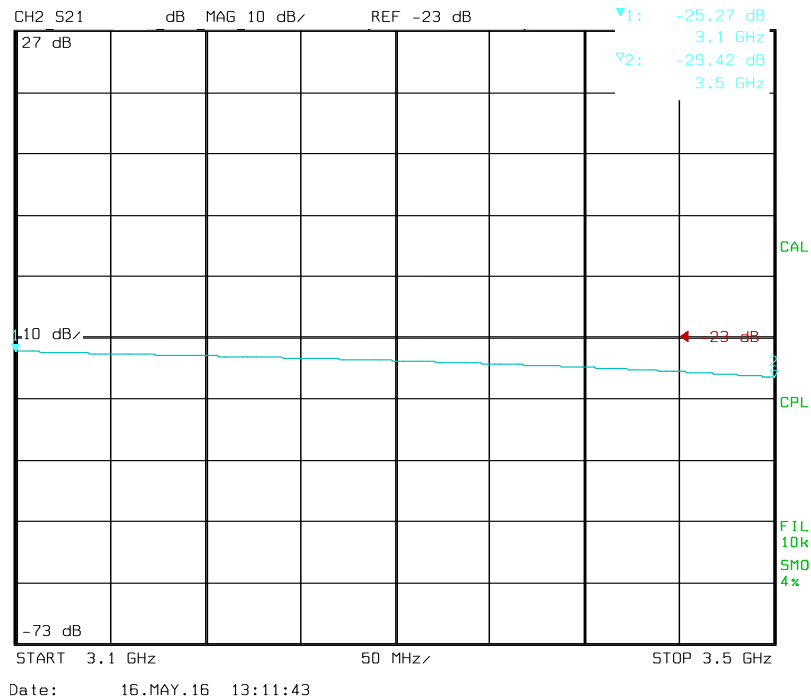
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J5- J7 Isolation



## J5- J8 Isolation

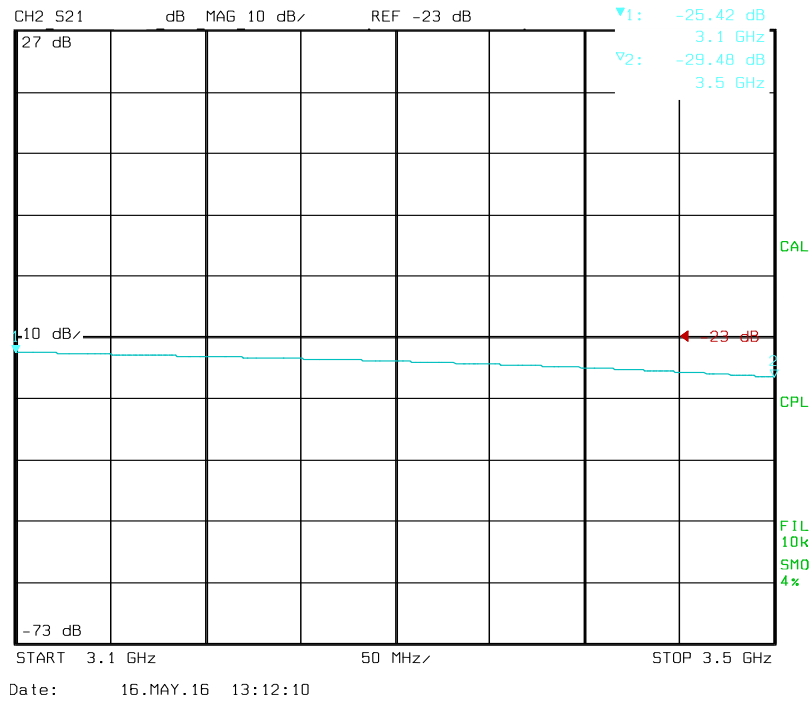




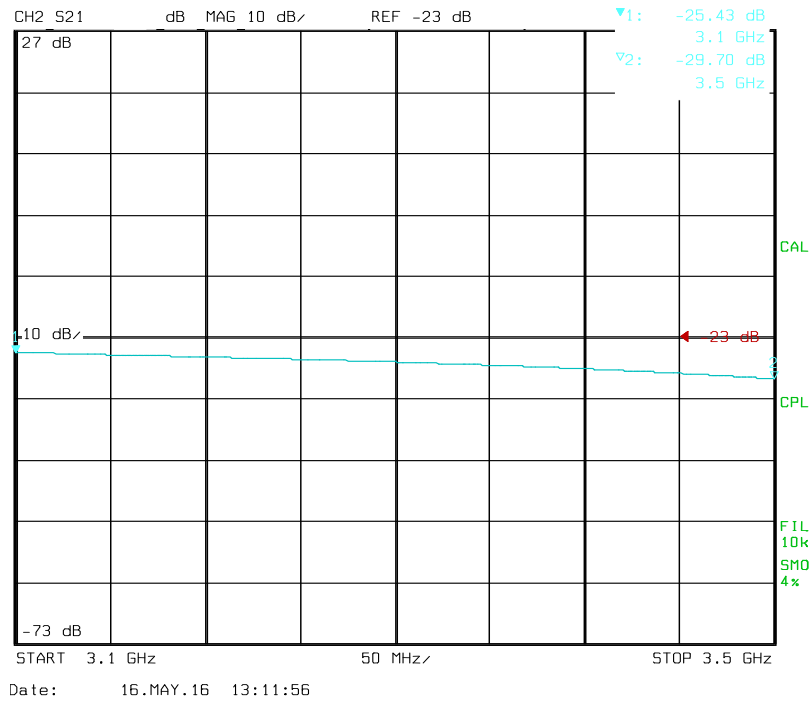
# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J6- J7 Isolation



## J6- J8 Isolation





# SUMMARY TEST DATA ON APD-8-3D13D5-40W-SFF

PL19241/1623

## J7- J8 Isolation

