



**SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH**

Summary Data for

PTRAN-100M18G-SFB-3UVPX-10HP-MAH

PL19642/1629

Recorded 10/11/2019

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TEST DATA FOR PTRAN-100M18G-SFB-3UVPX-10HP-MAH

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Customer: _____	Tested By: <u>R. Combs</u>
SO No: _____	Temperature: <u>+25°C</u>
Model No: <u>PTRAN-100M18G-SFB-3UVPX-10HP-MAH</u>	Date: <u>10/11/2019</u>
Serial No: <u>PL19639/1629</u>	Drawing No: <u>27624282</u> Rev: <u>A1</u>

TEST ITEM NO:	PARAMETERS	SPECIFIED VALUE	MEASURED VALUE	REMARKS QA/QC
J1 RF Receive Input				
1	Frequency	100MHz-18GHz	100MHz-18GHz	PMI QA 1
2	Input Power Level	-80dBm to -10dBm	-80dBm to -10dBm	
J4A IF1 Input				
3	Frequency	100MHz-4GHz	100MHz-4GHz	
4	Input Power Level	-8dBm Typical	-8dBm	
J4B IF2 Input				
5	Frequency	100MHz-4GHz	100MHz-4GHz	
6	Input Power Level	-8dBm Typical	-8dBm	
J9 LO1 Input				
7	Frequency	4GHz-20GHz	4GHz-20GHz	
8	Input Power Level	+10dBm Typ.	+10dBm	
J10 LO2 Input				
9	Input Frequency	4GHz-20GHz	4GHz-20GHz	
10	Input Power Level	+10dBm Typ.	+10dBm	PMI QA 1



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TEST ITEM NO:	PARAMETERS	SPECIFIED VALUE	MEASURED VALUE	REMARKS QA/QC	
J3A IF1 Output					
11	Output Frequency	100MHz - 4GHz	100MHz - 4GHz	PMI QA1	
12	Output Power Level	<-5dBm for Limited SDLVA channels	-5.6dBm to -11.1dBm See Plots		
J3B IF2 Output					
13	Output Frequency	100MHz - 4GHz	100MHz - 4GHz		
14	Output Power Level	<-5dBm for Limited SDLVA channels	-5.6dBm to -8.4dBm		
J6 RF Transmit Output					
15	Output Frequency	100MHz-18GHz	100MHz-18GHz		
16	Output Power Level	0dBm IF Input Level 0dBm to +10dBm Typical with use of variable attenuator in Down-Converted Channel	+0.2dBm to +7.1dBm See Plots		
Control Logic – TTL					
17	TTL 1 Transmit Path Output Select / Receive Filter Bank Input Select	0 – Output to TX Filter bank, RX- Backplane/Input (J1) 1 – Output to RX Filter bank, RX- Transceiver Input	0 – Output to TX Filter bank, RX Backplane/Input (J1) 1 – Output to RX Filter bank, RX- Transceiver Input	PMI QA1	



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TEST ITEM NO:	PARAMETERS	SPECIFIED VALUE	MEASURED VALUE	REMARKS QA/QC
18	TTL 2 RF Receive Filter Select	000 – Thru Channel 001 – Channel 1 010 – Channel 2 011 – Channel 3 100 – Channel 4 101 – Channel 5 110 Not Defined 111 – Not Defined	000 – Thru Channel 001 – Channel 1 010 – Channel 2 011 – Channel 3 100 – Channel 4 101 – Channel 5 110 Not Defined 111 – Not Defined	PMI QA1
19	TTL 3 Linear or Limited SDLVA RF Output Channel	0 - Limited RF Output Channel 1 - Linear RF Output Channel	0 – Limited RF Output Channel 1 – Linear RF Output Channel	
20	TTL 4 RF Receive Thru Channel Or Down-Converted Channel.	0 - .Thru Channel 1 - Down-Converted Channel	0 - Thru Channel 1 – Down-Converted Channel	
21	TTL 5 IF1 or IF2 Output Select	0 – IF1 Output Channel 1 – IF2 Output Channel	0 – IF1 Output Channel 1 – IF2 Output Channel	
22	TTL 6 IF1 or IF2 Input Select	0 – IF1 Input Chan. 1 – High Freq. Chan.	0 – IF1 Input Channel 1 – IF2 Input Channel	
23	TTL 7 RF Transmit Thru Channel or Down-Converted Channel	0 – Thru Channel 1 – Down-Converted Channel	0 – Thru Channel 1 – Down-Converted Channel	PMI QA1



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TEST ITEM NO:	PARAMETERS	SPECIFIED VALUE	MEASURED VALUE	REMARKS QA/QC
24	TTL 8 RF Transmit Filter Select 3 BIT Control	000 – Thru Channel 001 – Channel 1 010 – Channel 2 011 – Channel 3 100 – Channel 4 101 – Channel 5 110 Not Defined 111 – Not Defined	000 – Thru Channel 001 – Channel 1 010 – Channel 2 011 – Channel 3 100 – Channel 4 101 – Channel 5 110 Not Defined 111 – Not Defined	PMI QA1
25	TTL9 RF Transmit Down-Converted Channel 5 BIT Attenuation Control	00000 – 0 dB Attenuation 11111 – 31 dB Attenuation	00000 – 0 dB Attenuation 11111 – 31 dB Attenuation	
26	TTL10 LO1 LO2 Select 2 BIT Control	00 M1-LO1 M2-LO1 01 M1-LO1 M2-LO2 10 M1-LO2 M2-LO1 11 M1-LO2 M2-LO2	00 M1-LO1 M2-LO1 01 M1-LO1 M2-LO2 10 M1-LO2 M2-LO1 11 M1-LO2 M2-LO2	
27	TTL11 SDLVA Threshold Adjust 6 BIT Control	000000 – Lowest Threshold Level 111111 – Highest Threshold Level	000000 – Lowest Threshold Level 111111 – Highest Threshold Level	
28	TTL12 SDLVA CTL A (ENABLE)	0 – Internal Comparator Triggered SPST 1 – External Trigger Enable (SW CTL B)	0 – Internal Comparator Triggered SPST 1 – External Trigger Enable (SW CTL B)	
29	TT13 SDLVA CTL B (TRIGGER)	0 - Limited RF Channel SPST Switch ON (Insertion Loss) 1 - Limited RF Channel SPST Switch OFF (Isolation)	0 - Limited RF Channel SPST Switch ON (Insertion Loss) 1 - Limited RF Channel SPST Switch OFF (Isolation)	PMI QA1



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TEST ITEM NO:	PARAMETERS	SPECIFIED VALUE	MEASURED VALUE	REMARKS QA/QC
30	Power Supply	+12V -12V +5V +3.3V	+12V @ 897 A -12V @ 775 mA +5V @ 625 mA +3.3V @ 6 mA	PMI QA 1

QA/QC Approval:  PMI QA 1 Date: 10/14/19



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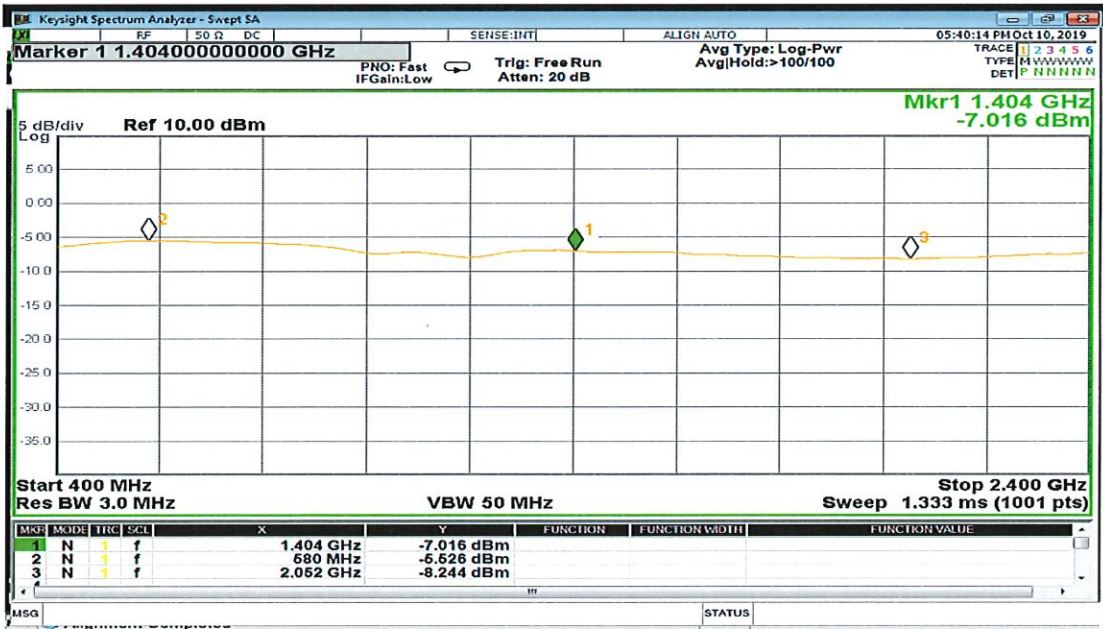
PL19642/1629

RECEIVE INPUT to IF OUTPUT PLOTS – THRU CHANNEL

RF Receive Input (J1) To IF1 Output (J3A) Thru Channel Limited (-10dBm Input)



RF Receive Input (J1) To IF2 Output (J3B) Thru Channel Limited (-10dBm Input)



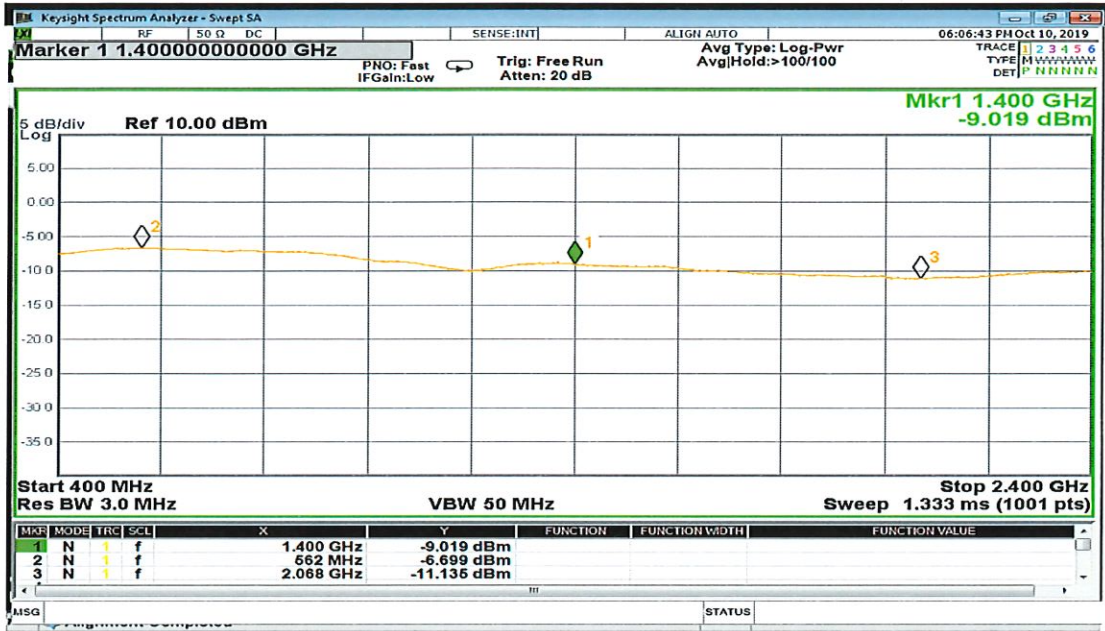


SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

PL19642/1629

RECEIVE INPUT to IF OUTPUT PLOTS – THRU CHANNEL

RF Receive Input (J1) To IF1 Output (J3A) Thru Channel Limited (-70dBm Input)



RF Receive Input (J1) To IF1 Output (J3A) Thru Channel Linear (-10dBm Input)



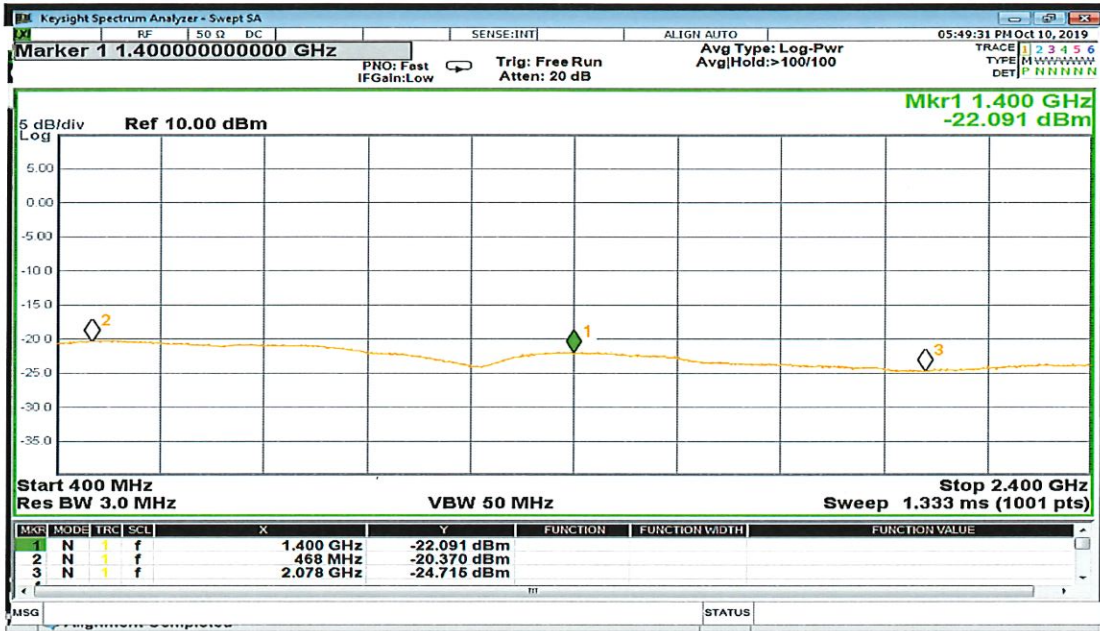


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ON
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PL19642/1629

RECEIVE INPUT to IF OUTPUT PLOTS – THRU CHANNEL

RF Receive Input (J1) To IF1 Output (J3A) Thru Channel Linear (-70dBm Input)



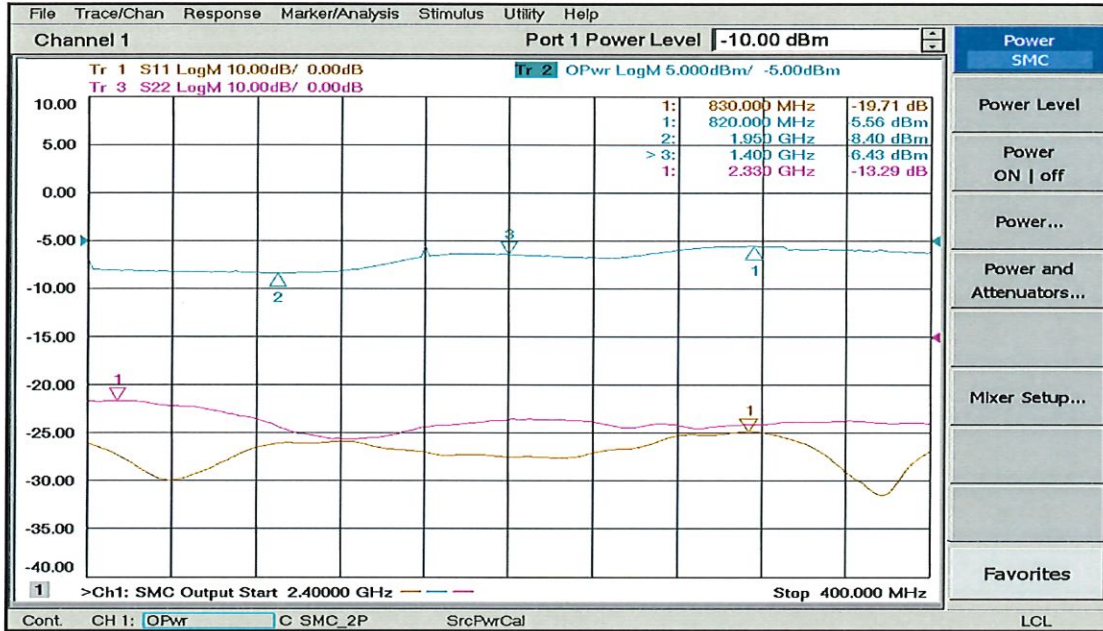


SUMMARY TEST DATA
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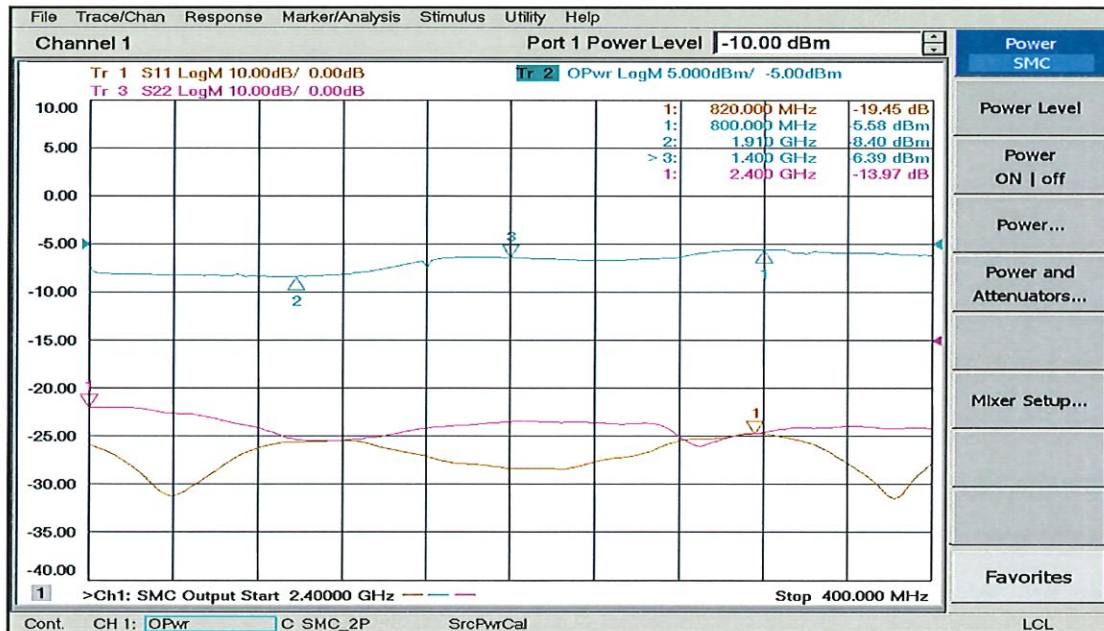
PL19642/1629

RECEIVE INPUT to IF OUTPUT PLOTS – CHANNEL 1

RF Receive Input (J1) To IF1 Output (J3A) Channel 1 Limited (-10dBm Input)



RF Receive Input (J1) To IF2 Output (J3B) Channel 1 Limited (-10dBm Input)



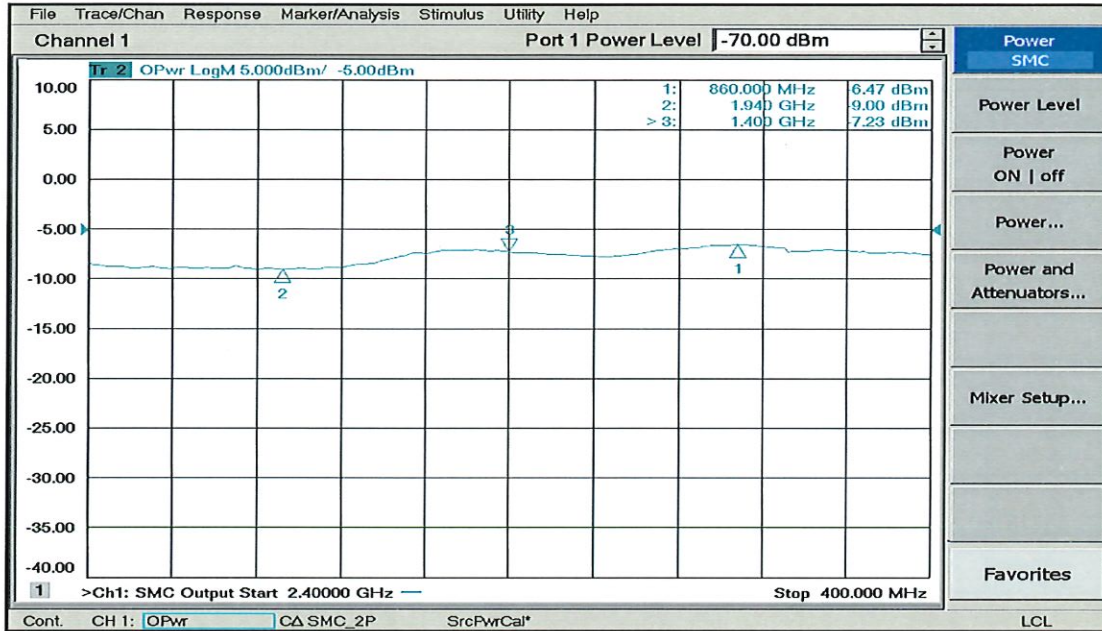


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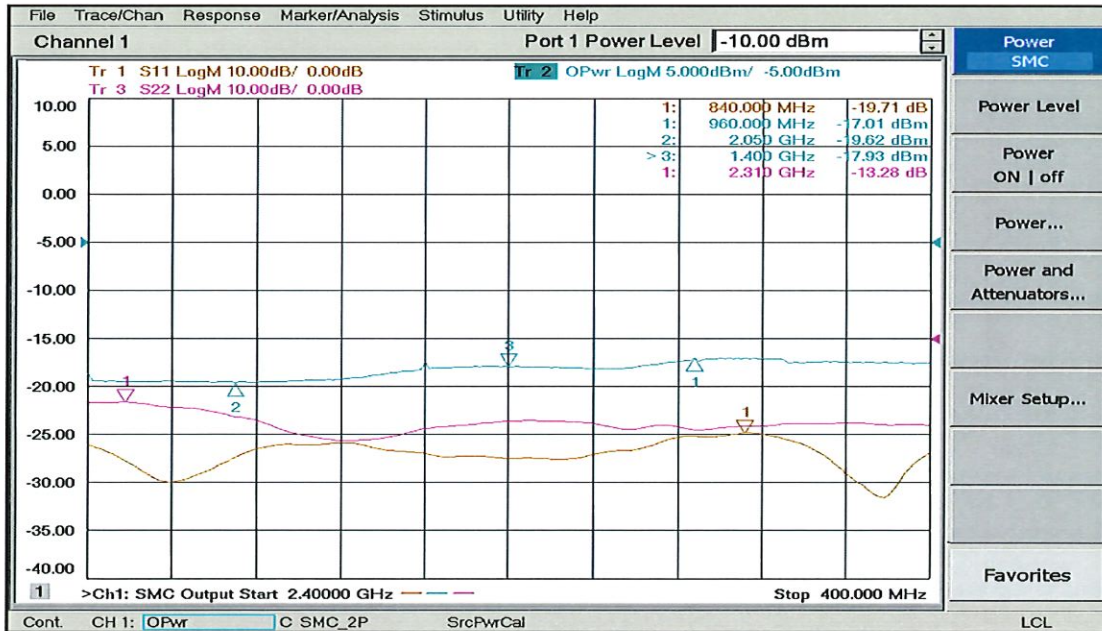
PL19642/1629

RECEIVE INPUT to IF OUTPUT PLOTS - Channel 1

RF Receive Input (J1) To IF1 Output (J3A) Channel 1 Limited (-70dBm Input)



RF Receive Input (J1) To IF1 Output (J3A) Channel 1 Linear (-10dBm Input)



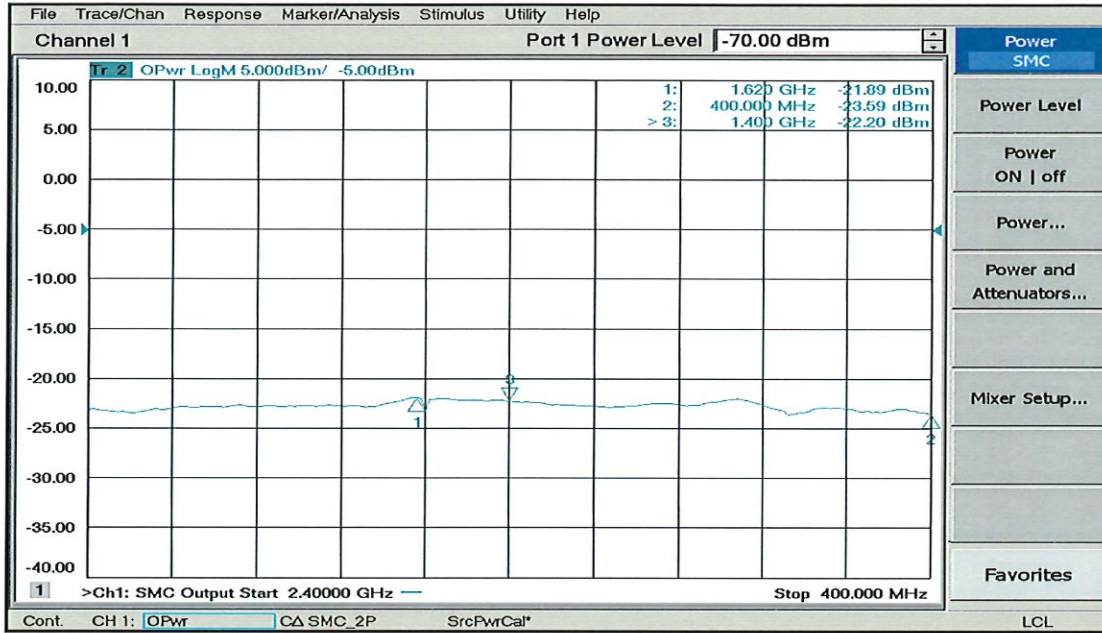


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RECEIVE INPUT to IF OUTPUT PLOTS - Channel 1

RF Receive Input (J1) To IF1 Output (J3A) Channel 1 Linear (-70dBm Input)





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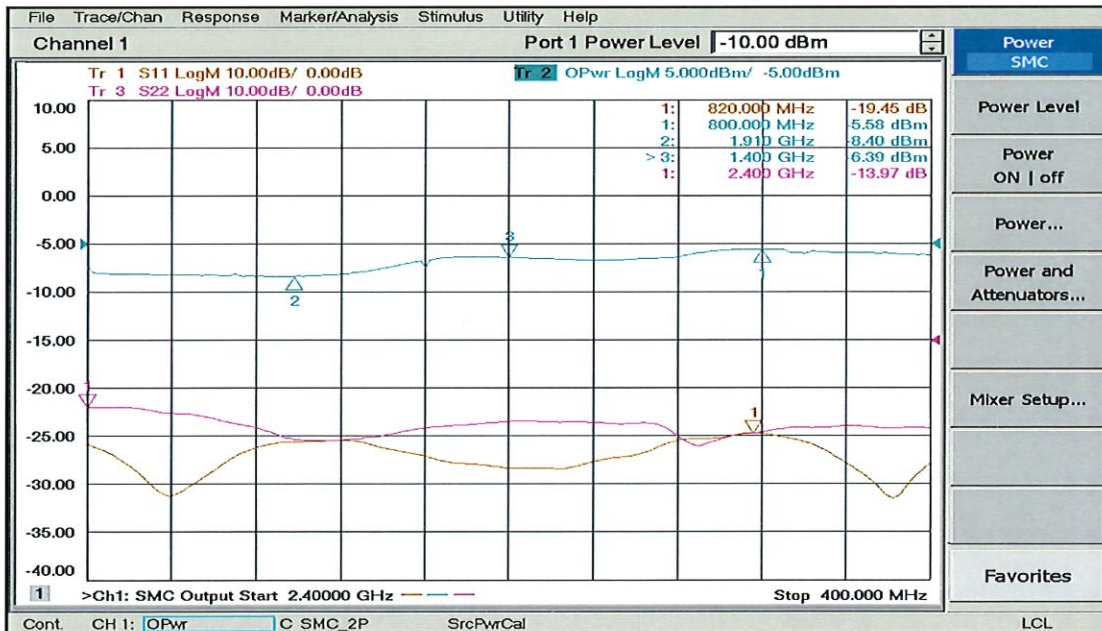
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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 2

RF Receive Input (J1) To IF1 Output (J3A) Channel 2 Limited (-10dBm Input)



RF Receive Input (J1) To IF2 Output (J3B) Channel 2 Limited (-10dBm Input)



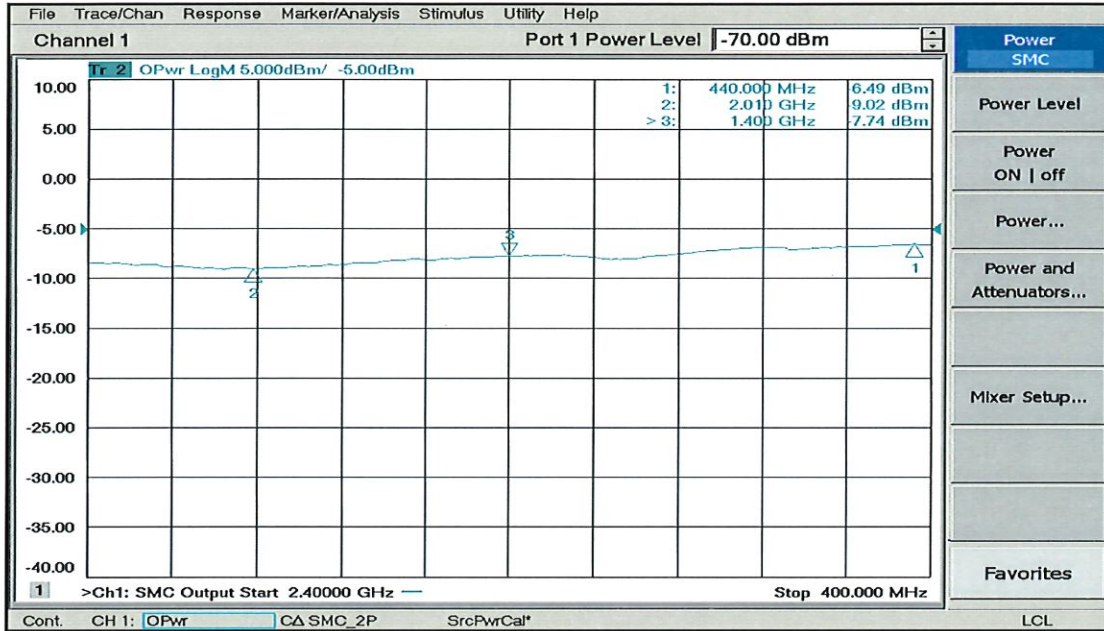


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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 2

RF Receive Input (J1) To IF1 Output (J3A) Channel 2 Limited (-70dBm Input)



RF Receive Input (J1) To IF1 Output (J3A) Channel 2 Linear (-10dBm Input)



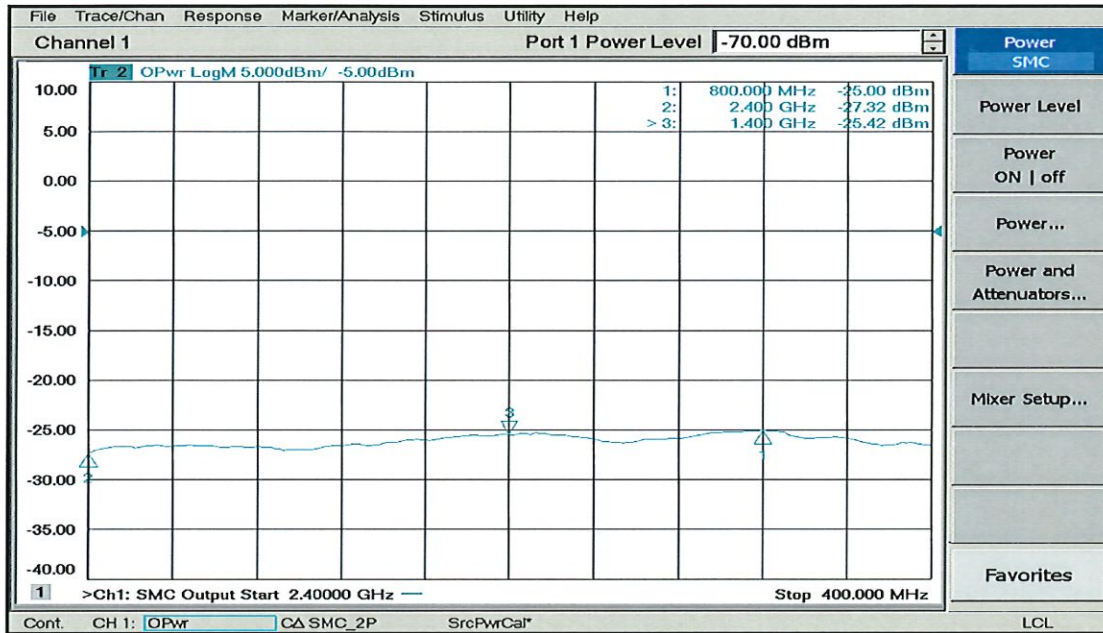


SUMMARY TEST DATA
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PTRAN-100M18G-SFB-3UVPX-10HP-MAH

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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 2

RF Receive Input (J1) To IF1 Output (J3A) Channel 2 Linear (-70dBm Input)



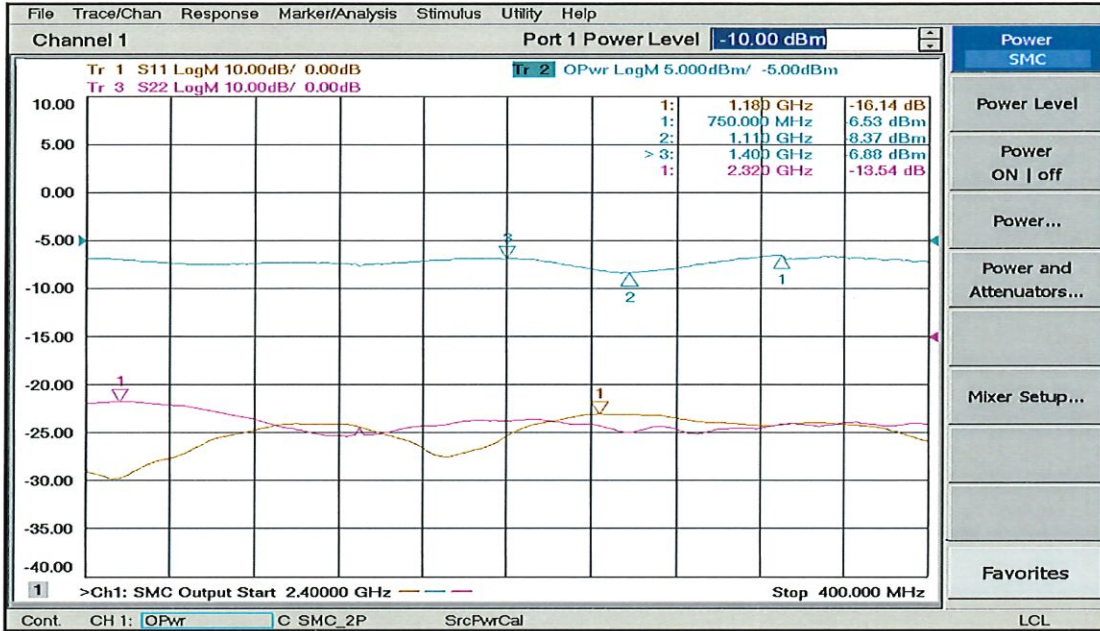


SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

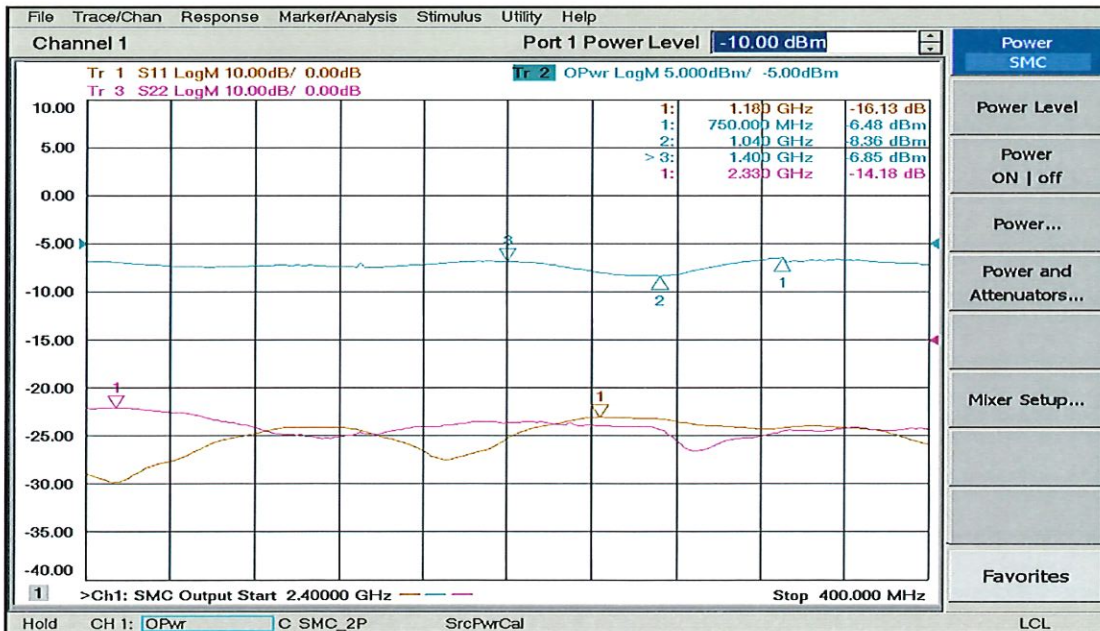
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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 3

RF Receive Input (J1) To IF1 Output (J3A) Channel 3 Limited (-10dBm Input)



RF Receive Input (J1) To IF2 Output (J3B) Channel 3 Limited (-10dBm Input)



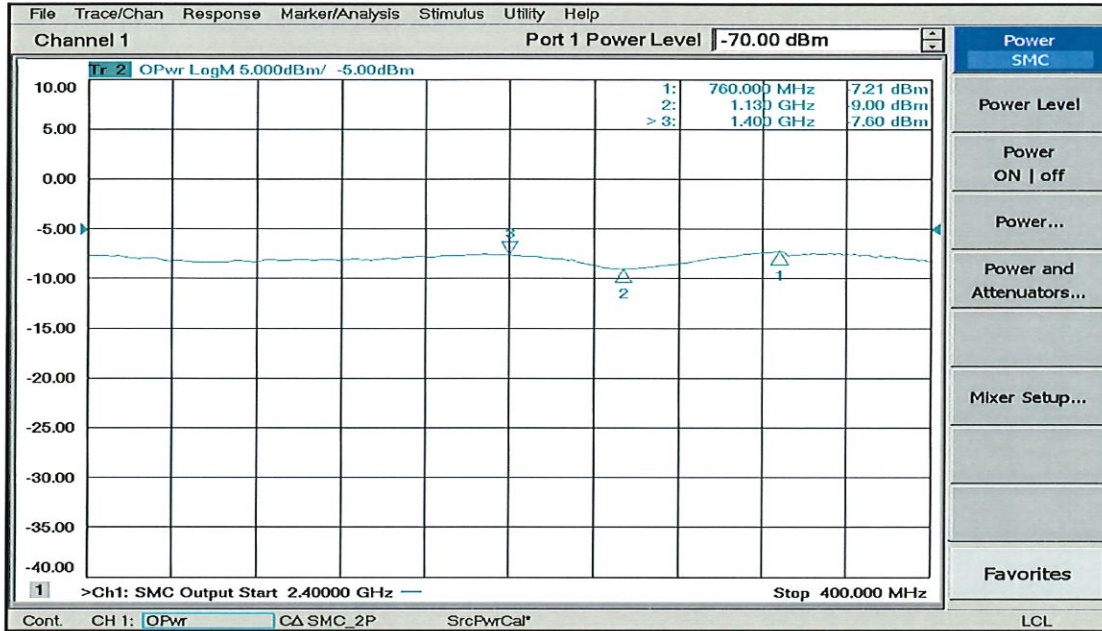


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ON
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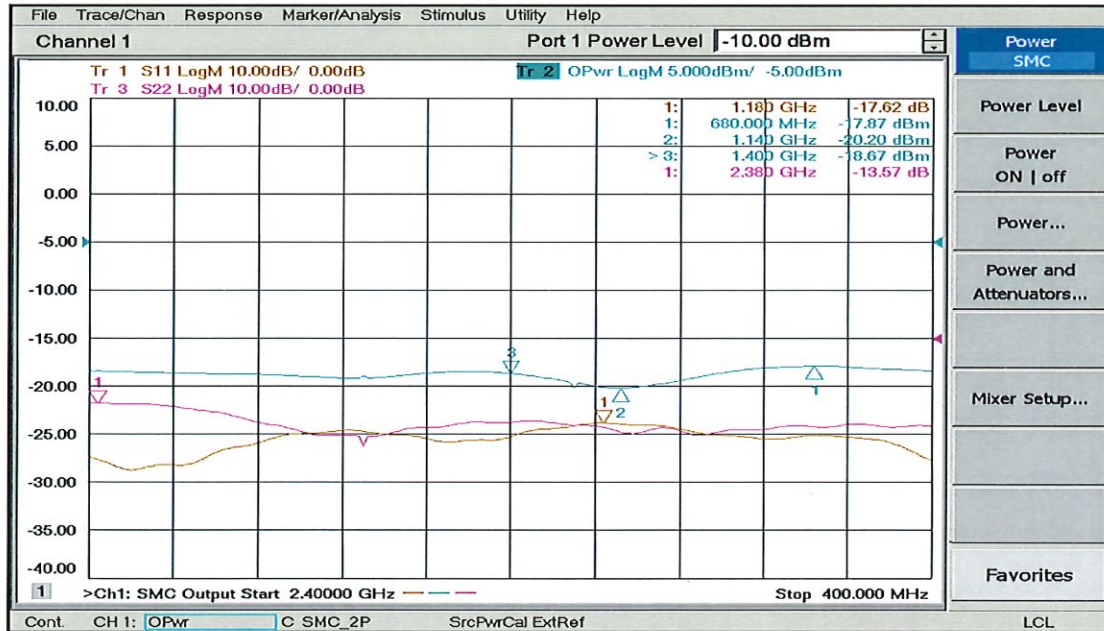
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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 3

RF Receive Input (J1) To IF1 Output (J3A) Channel 3 Limited (-70dBm Input)



RF Receive Input (J1) To IF1 Output (J3A) Channel 3 Linear (-10dBm Input)



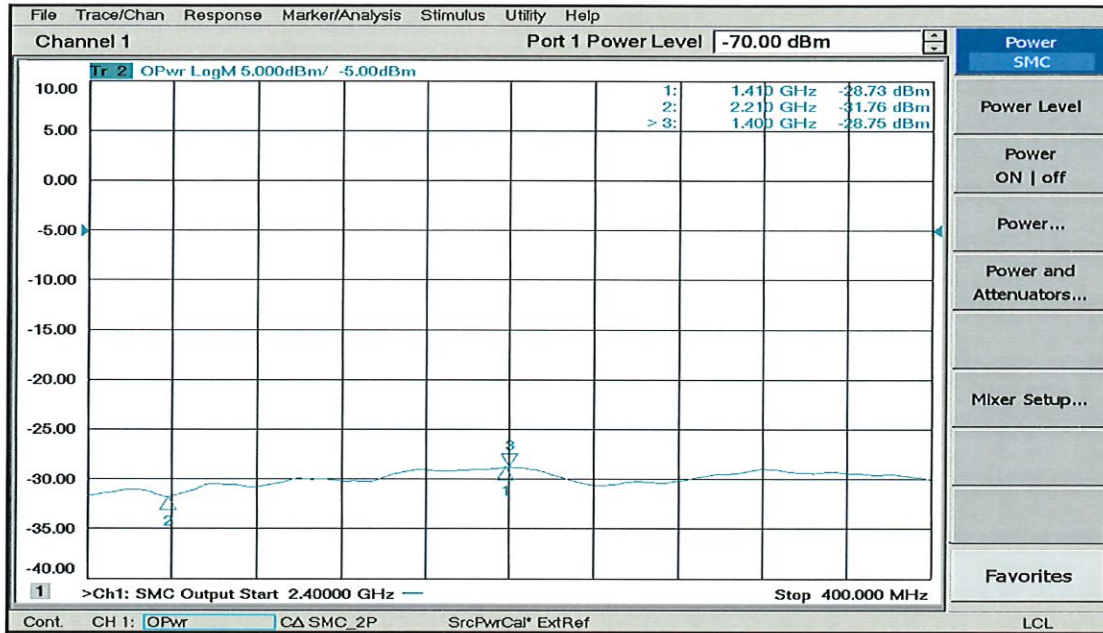


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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 3

RF Receive Input (J1) To IF1 Output (J3A) Channel 3 Linear (-70dBm Input)





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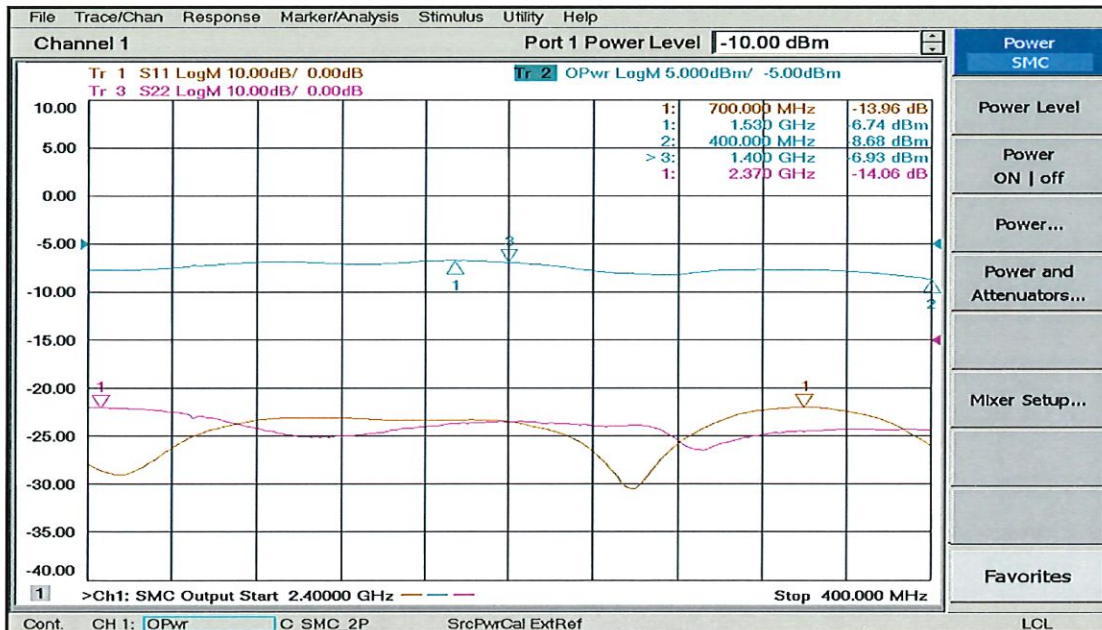
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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 4

RF Receive Input (J1) To IF1 Output (J3A) Channel 4 Limited (-10dBm Input)



RF Receive Input (J1) To IF2 Output (J3B) Channel 4 Limited (-10dBm Input)



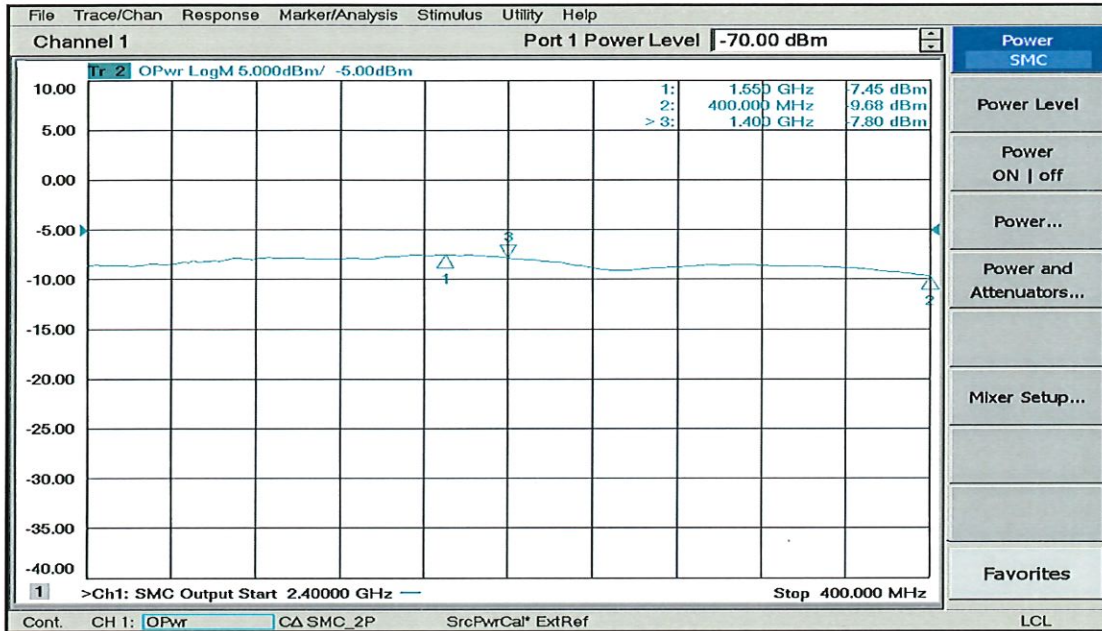


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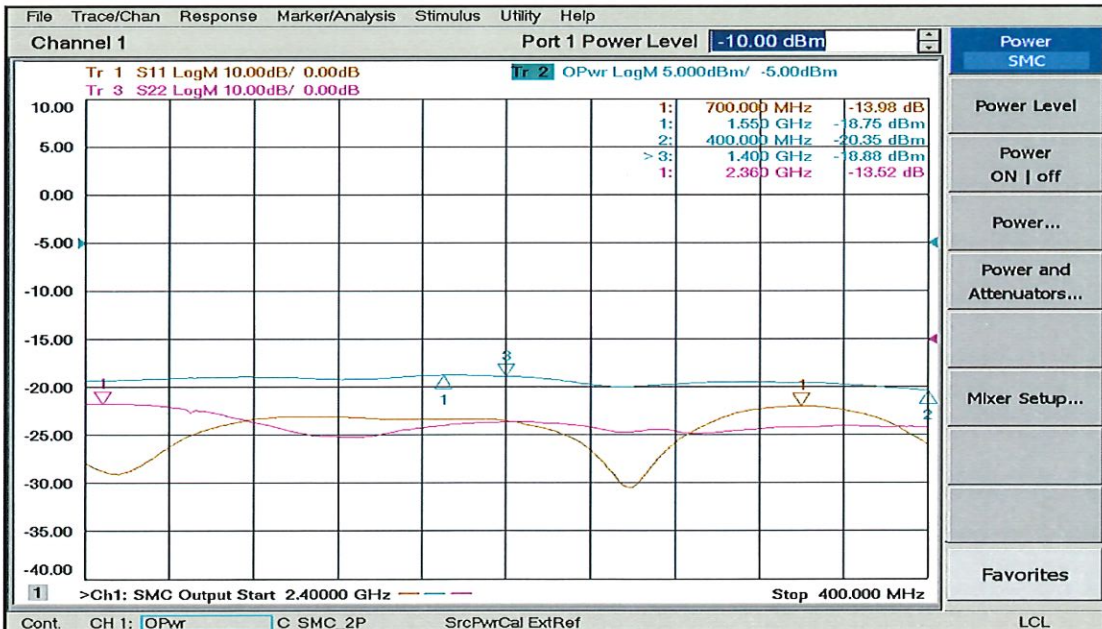
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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 4

RF Receive Input (J1) To IF1 Output (J3A) Channel 4 Limited (-70dBm Input)



RF Receive Input (J1) To IF1 Output (J3A) Channel 4 Linear (-10dBm Input)



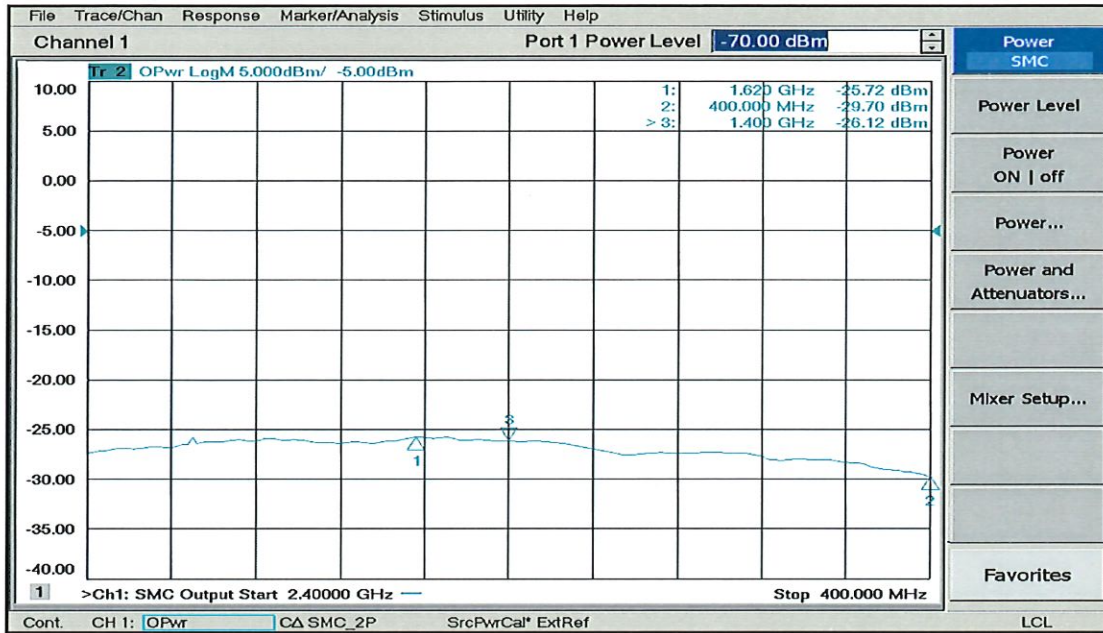


SUMMARY TEST DATA
ON
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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 4

RF Receive Input (J1) To IF1 Output (J3A) Channel 4 Linear (-70dBm Input)



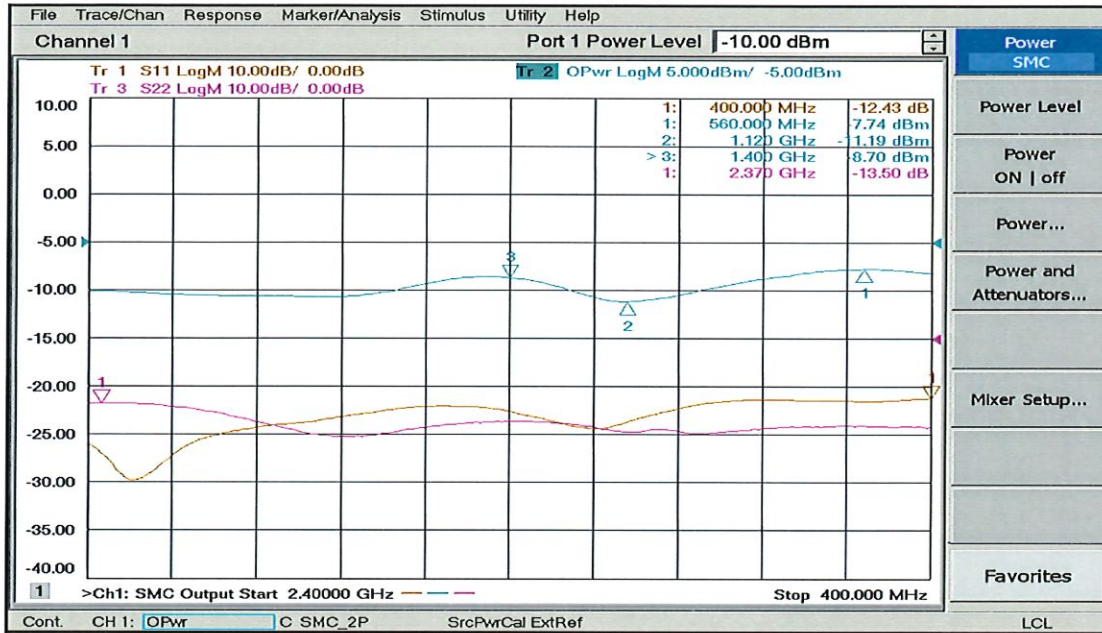


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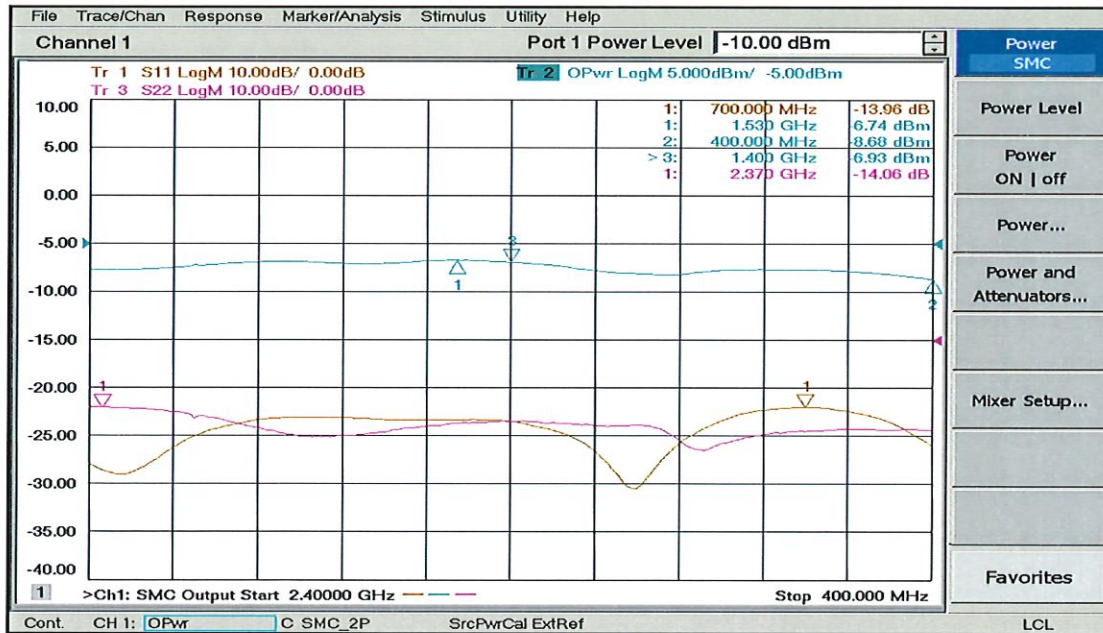
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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 5

RF Receive Input (J1) To IF1 Output (J3A) Channel 5 Limited (-10dBm Input)



RF Receive Input (J1) To IF2 Output (J3B) Channel 5 Limited (-10dBm Input)



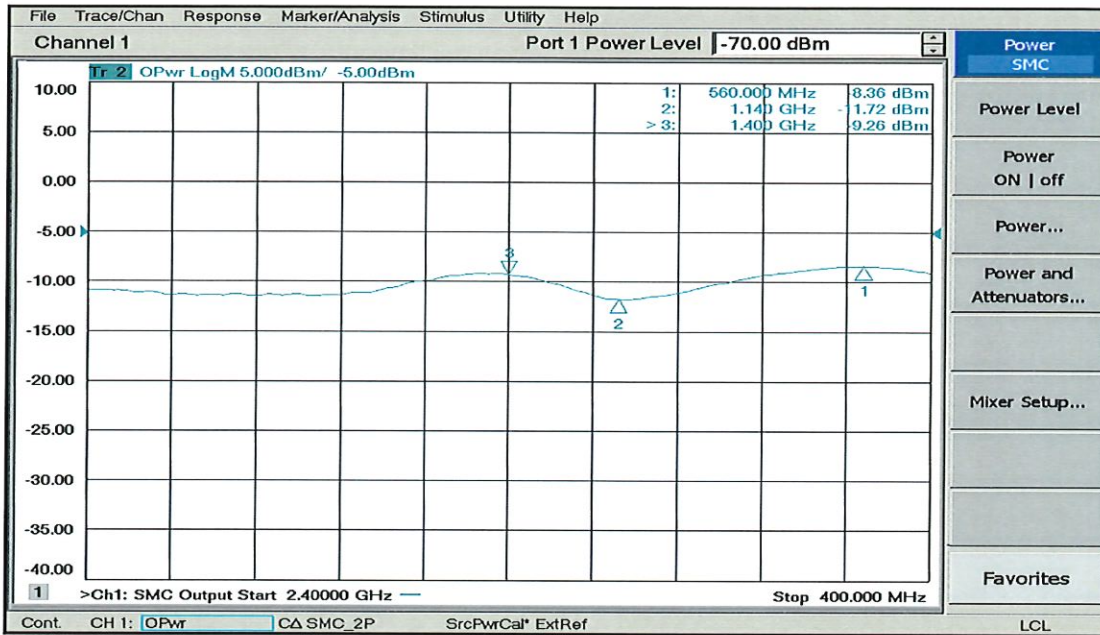


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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 5

RF Receive Input (J1) To IF1 Output (J3A) Channel 5 Limited (-70dBm Input)



RF Receive Input (J1) To IF1 Output (J3A) Channel 5 Linear (-10dBm Input)



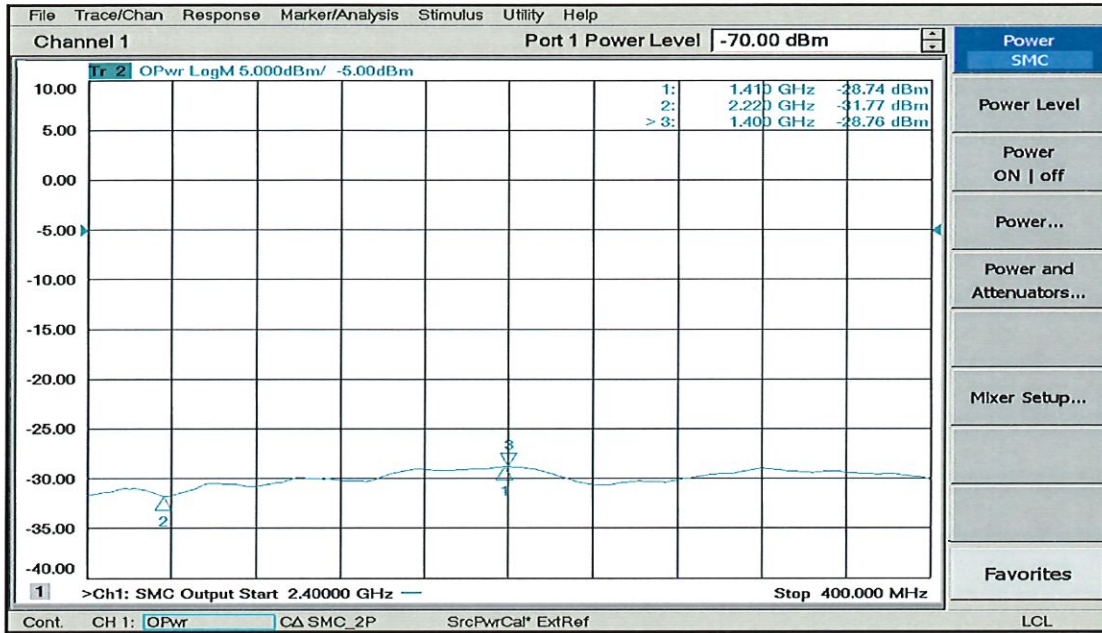


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RECEIVE INPUT to IF OUTPUT PLOTS – Channel 5

RF Receive Input (J1) To IF1 Output (J3A) Channel 5 Linear (-70dBm Input)





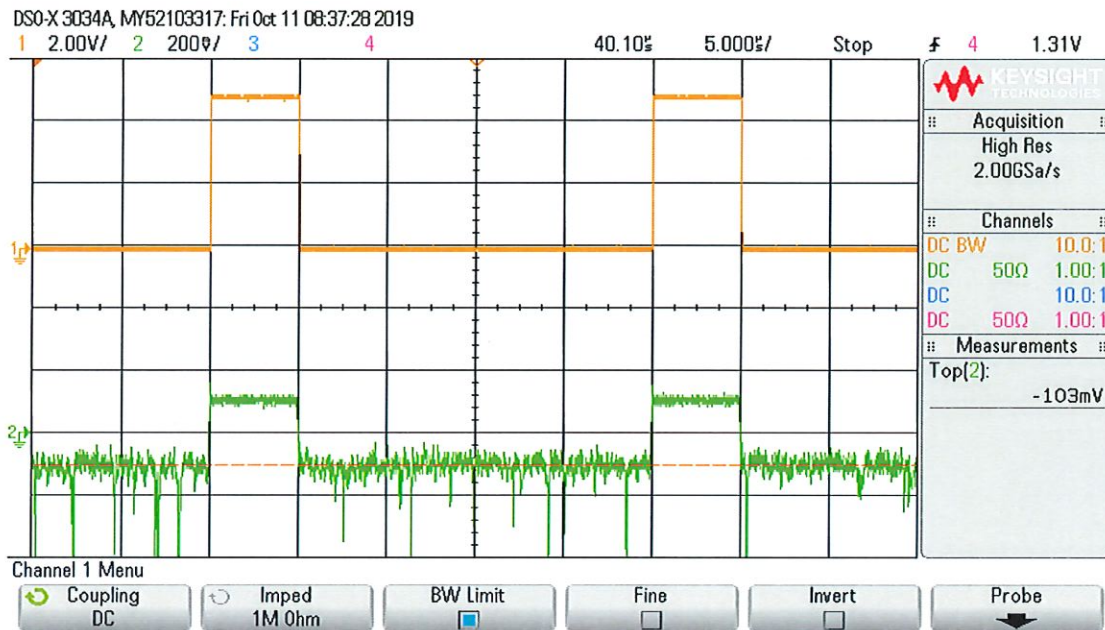
SUMMARY TEST DATA
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RECEIVE INPUT PLOTS

Log Proportional Detected Video Output (V1) and Threshold Detected Video Output (V0)

Thru Channel - 1.4GHz @ -67dBm Threshold Set to 12



Green Trace: J11 - Log Proportional Detected Video Output (V1)

Yellow Trace: J12 - Threshold Detected Video Output (V0)



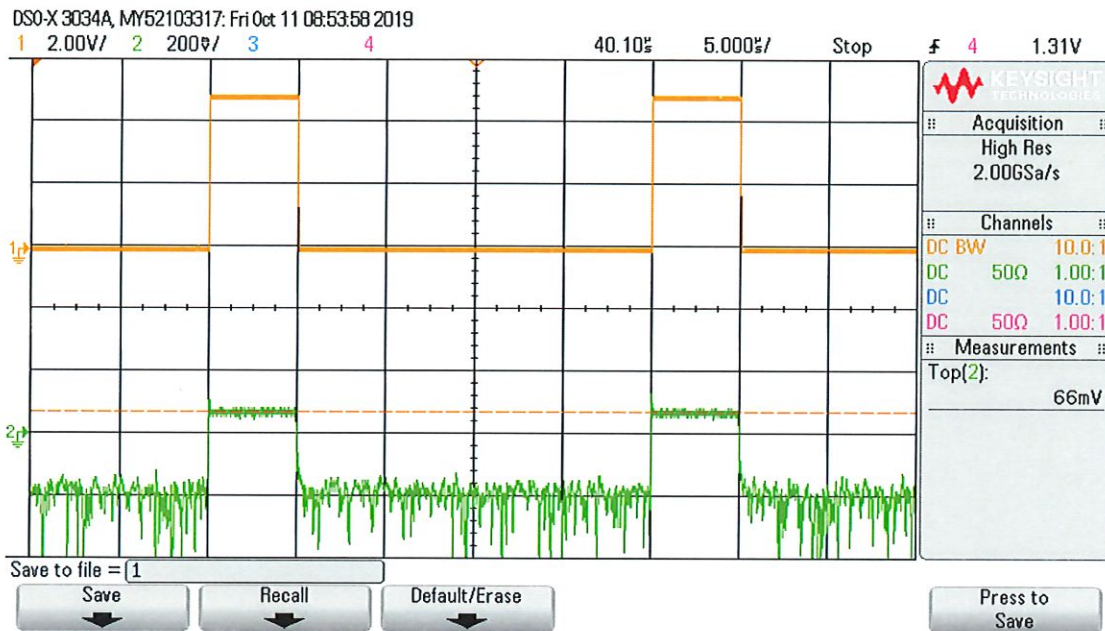
SUMMARY TEST DATA
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RECEIVE INPUT PLOTS

Log Proportional Detected Video Output (V1) And Threshold Detected Video Output (V0)

Channel 1 - 3.4GHz @ -69dBm Threshold Set to 8



Green Trace: J11 - Log Proportional Detected Video Output (V1)

Yellow Trace: J12 - Threshold Detected Video Output (V0)



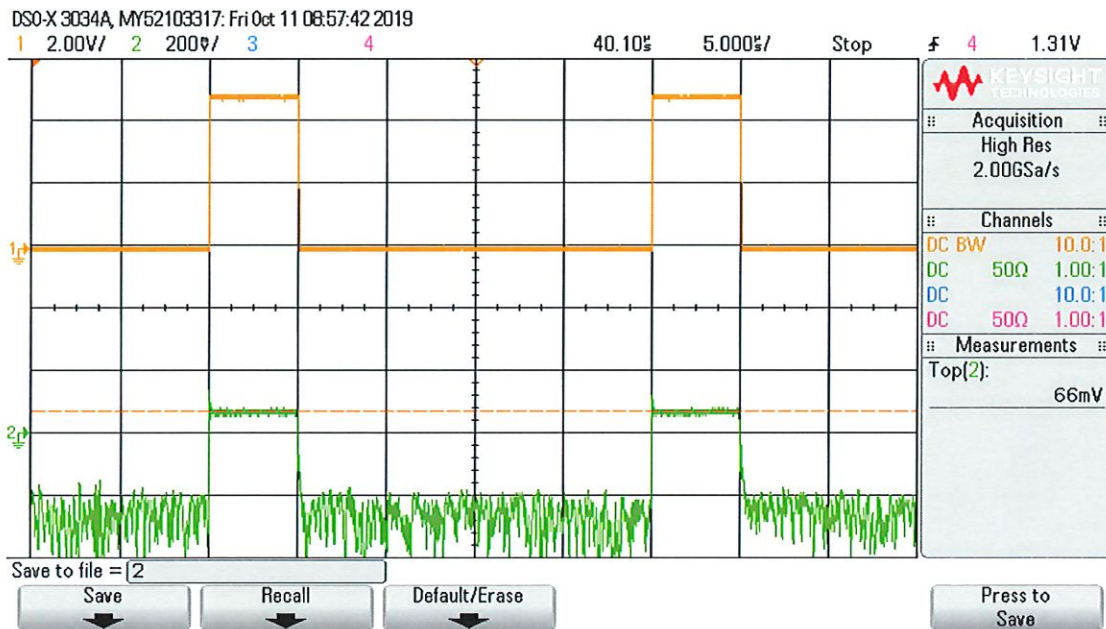
SUMMARY TEST DATA
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RECEIVE INPUT PLOTS

Log Proportional Detected Video Output (V1) And Threshold Detected Video Output (V0)

Channel 2 - 5.4GHz @ -65dBm Threshold Set to 8



Green Trace: J11 - Log Proportional Detected Video Output (V1)

Yellow Trace: J12 - Threshold Detected Video Output (V0)



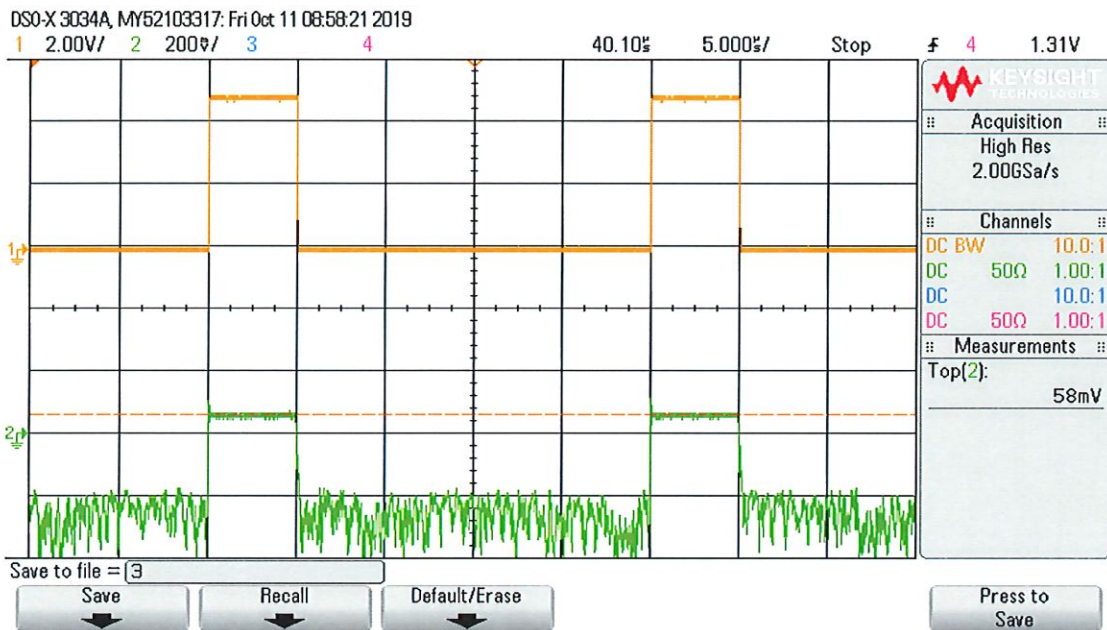
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RECEIVE INPUT PLOTS

Log Proportional Detected Video Output (V1) And Threshold Detected Video Output (V0)

Channel 3 - 7.4GHz @ -64dBm Threshold Set to 8



Green Trace: J11 - Log Proportional Detected Video Output (V1)

Yellow Trace: J12 - Threshold Detected Video Output (V0)



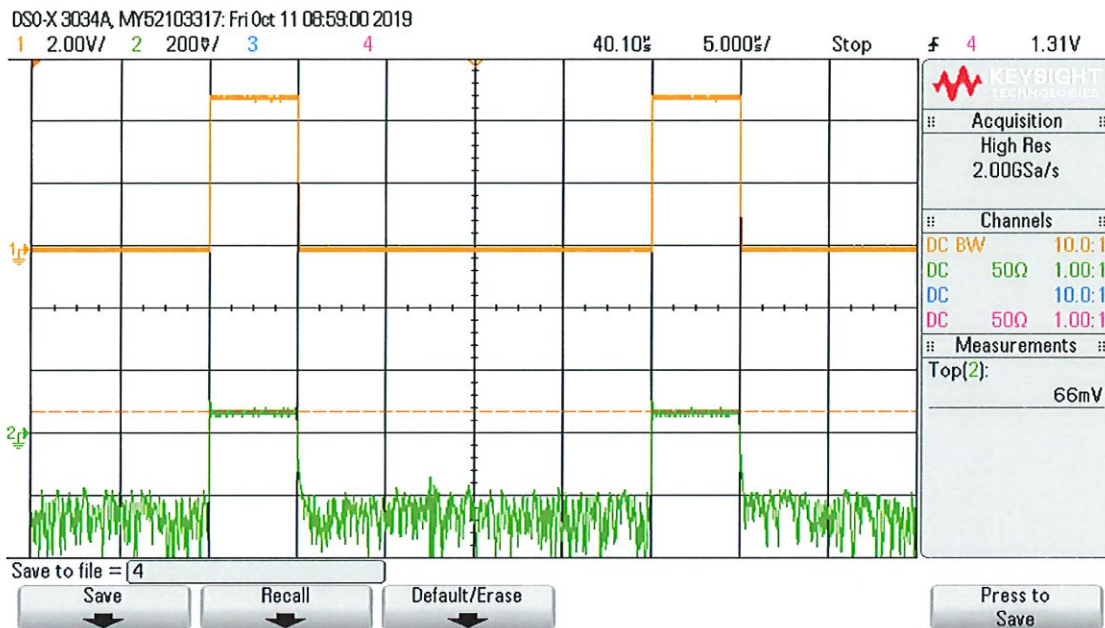
SUMMARY TEST DATA
ON
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PL19642/1629

RECEIVE INPUT PLOTS

Log Proportional Detected Video Output (V1) And Threshold Detected Video Output (V0)

Channel 4 - 9.4GHz @ -65dBm Threshold Set to 8



Green Trace: J11 - Log Proportional Detected Video Output (V1)

Yellow Trace: J12 - Threshold Detected Video Output (V0)



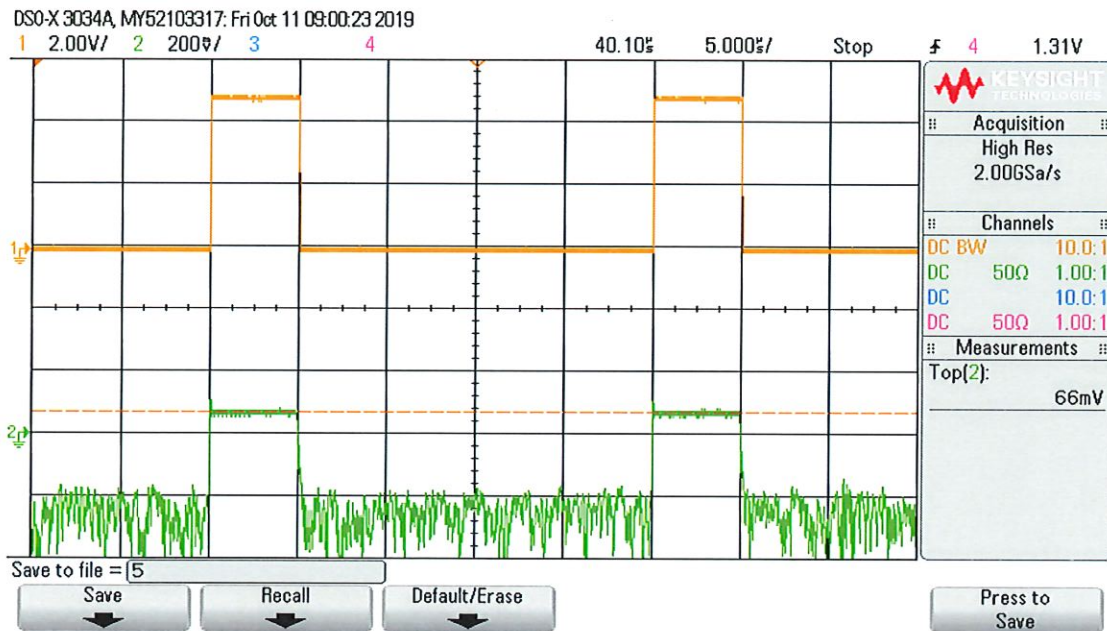
SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

PL19642/1629

RECEIVE INPUT PLOTS

Log Proportional Detected Video Output (V1) And Threshold Detected Video Output (V0)

Channel 5 - 11.4GHz @ -65dBm Threshold Set to 8



Yellow Trace: J11 - Log Proportional Detected Video Output (V1)

Green Trace: J12 - Threshold Detected Video Output (V0)



**SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH**

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TABLE 1

Typical Minimum Threshold Values for Band Centers and Band Edges

Channel	Frequency	dBm	Threshold setting	Frequency	dBm	Threshold setting	Frequency	dBm	Threshold setting
1	2.4 GHz	-67	10	3.4 GHz	-67	10	4.4 GHz	-65	10
2	4.4 GHz	-62	10	5.4 GHz	-65	10	6.4 GHz	-65	10
3	6.4 GHz	-63	10	7.4 GHz	-63	10	8.4 GHz	-61	10
4	8.4 GHz	-63	10	9.4 GHz	-64	10	10.4 GHz	-63	10
5	10.4 GHz	-63	10	11.4 GHz	-65	10	12.4 GHz	-62	10
THRU	400 MHz	-68	12	1.4 GHz	-67	12	2.4 GHz	-66	12



SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

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IF INPUT to TRANSMIT OUTPUT PLOTS

IF1 Input (J4A) To RF Transmit Output (J6) Thru Channel (-8dBm Input)



IF1 Input (J4A) To RF Transmit Output (J6) Channel 1 (-8dBm Input)



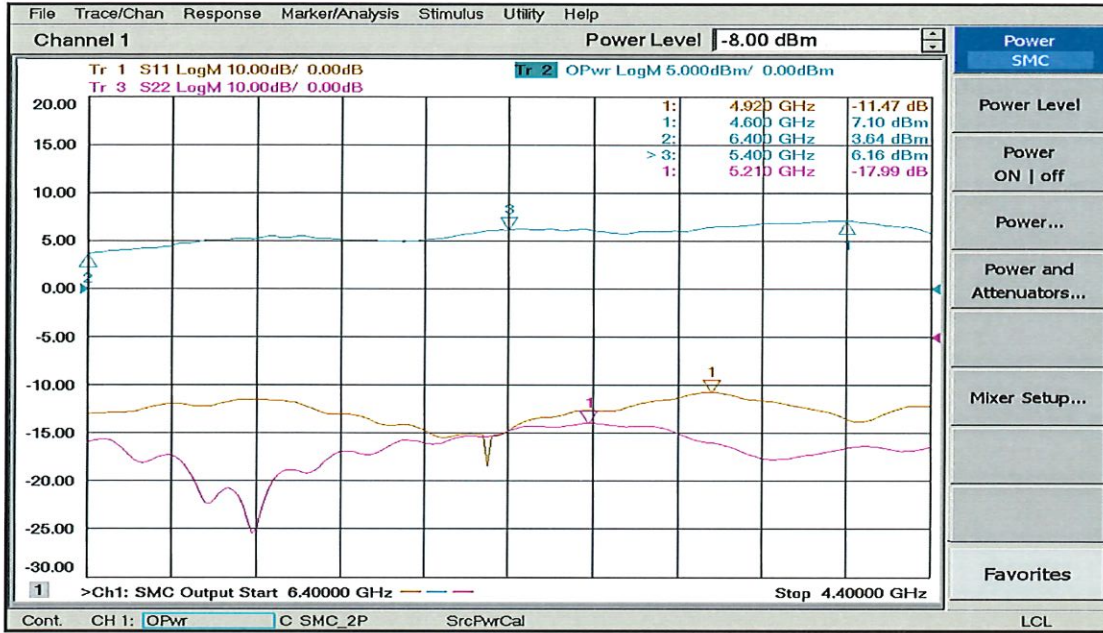


SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

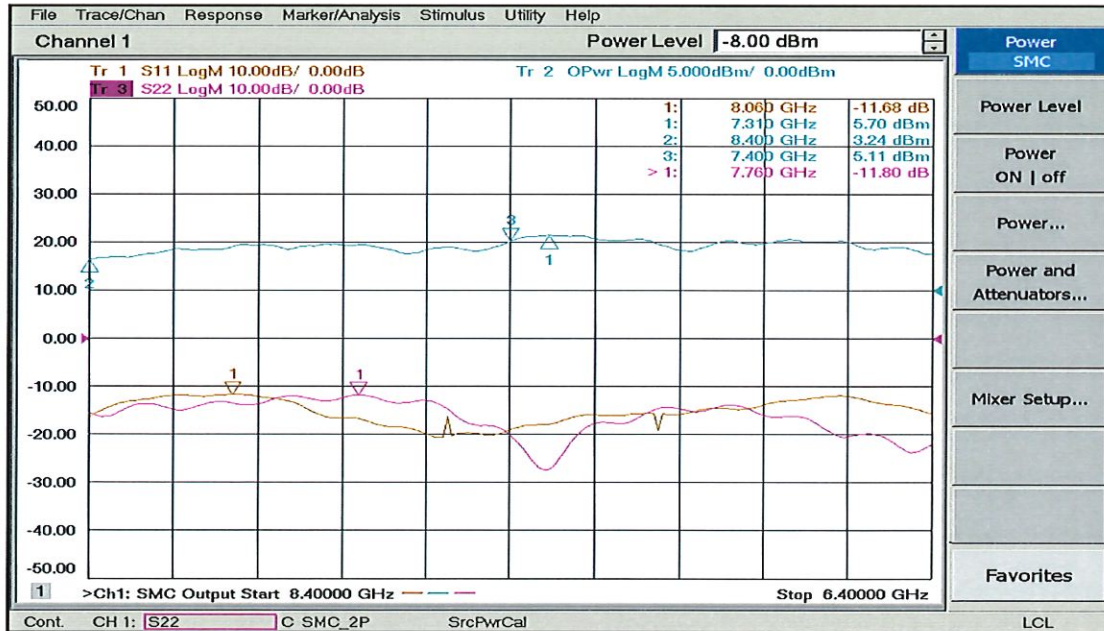
PL19642/1629

IF INPUT to TRANSMIT OUTPUT PLOTS

IF1 Input (J4A) To RF Transmit Output (J6) Channel 2 (-8dBm Input)



IF1 Input (J4A) To RF Transmit Output (J6) Channel 3 (-8dBm Input)



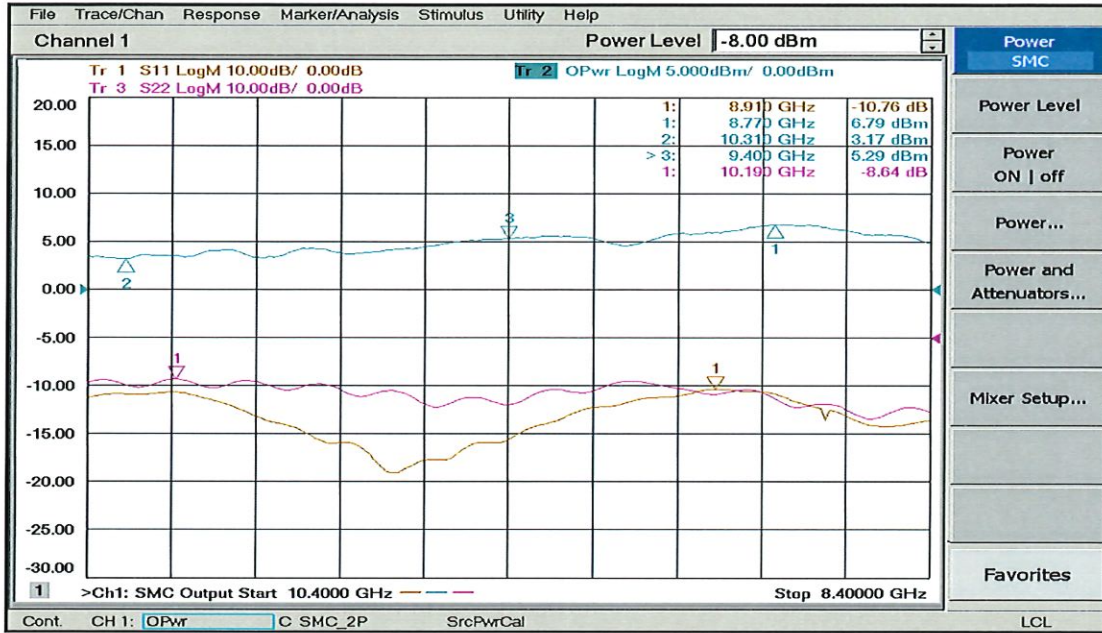


**SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH**

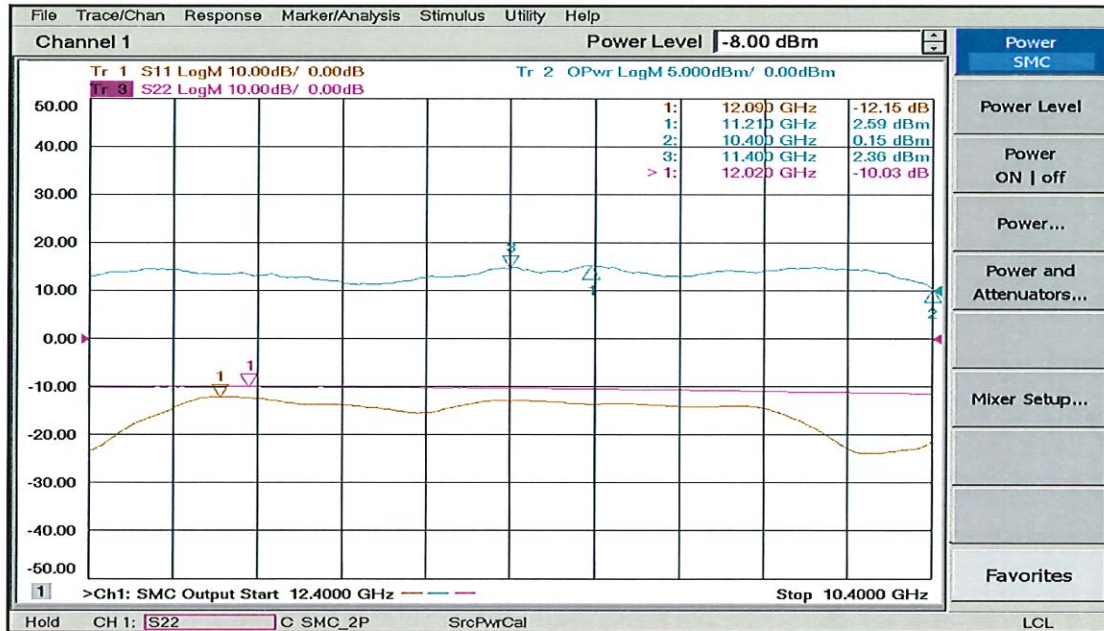
PL19642/1629

IF INPUT to TRANSMIT OUTPUT PLOTS

IF1 Input (J4A) To RF Transmit Output (J6) Channel 4 (-8dBm Input)



IF1 Input (J4A) To RF Transmit Output (J6) Channel 5 (-8dBm Input)



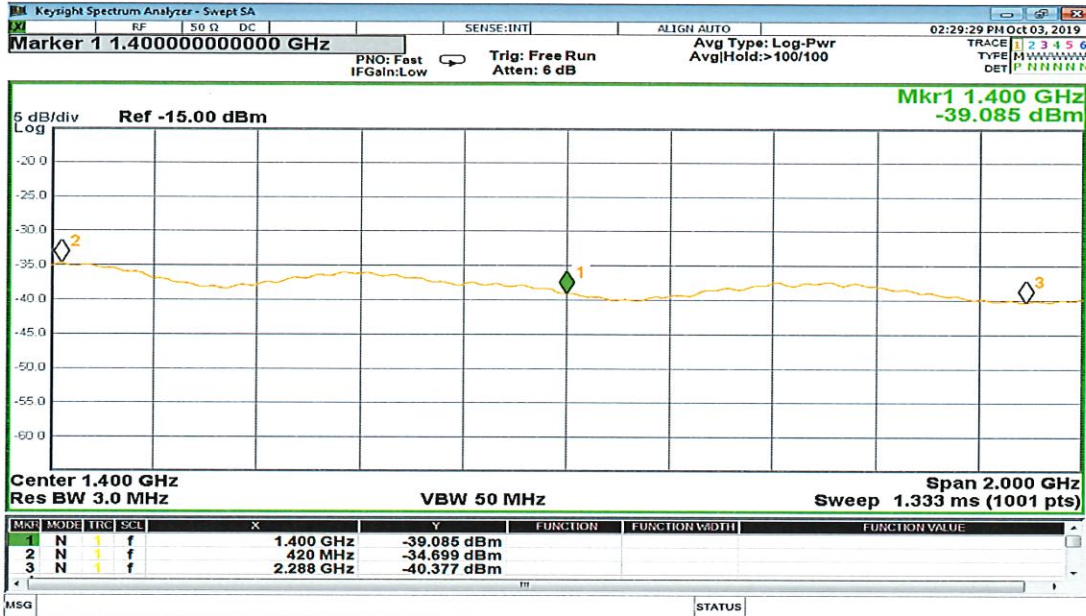


SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

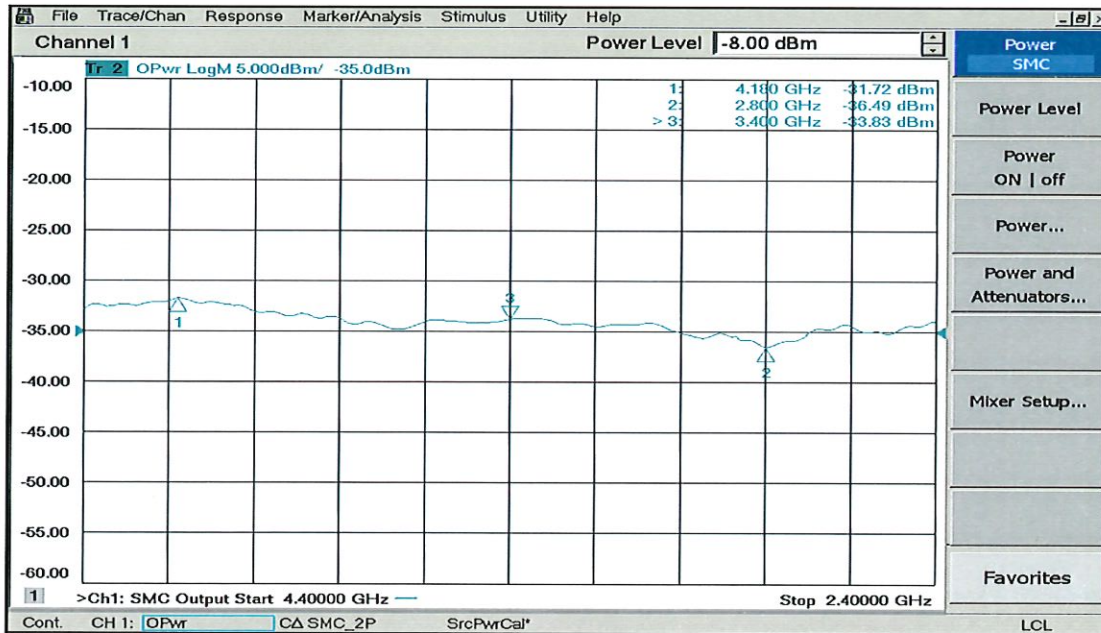
PL19642/1629

IF INPUT to RF TRANSMIT SAMPLE OUTPUT PLOTS

IF1 Input (J4A) To RF Transmit Sample Output (J8) Thru Channel (-8dBm Input)



IF1 Input (J4A) To RF Transmit Sample Output (J8) Channel 1 (-8dBm Input)



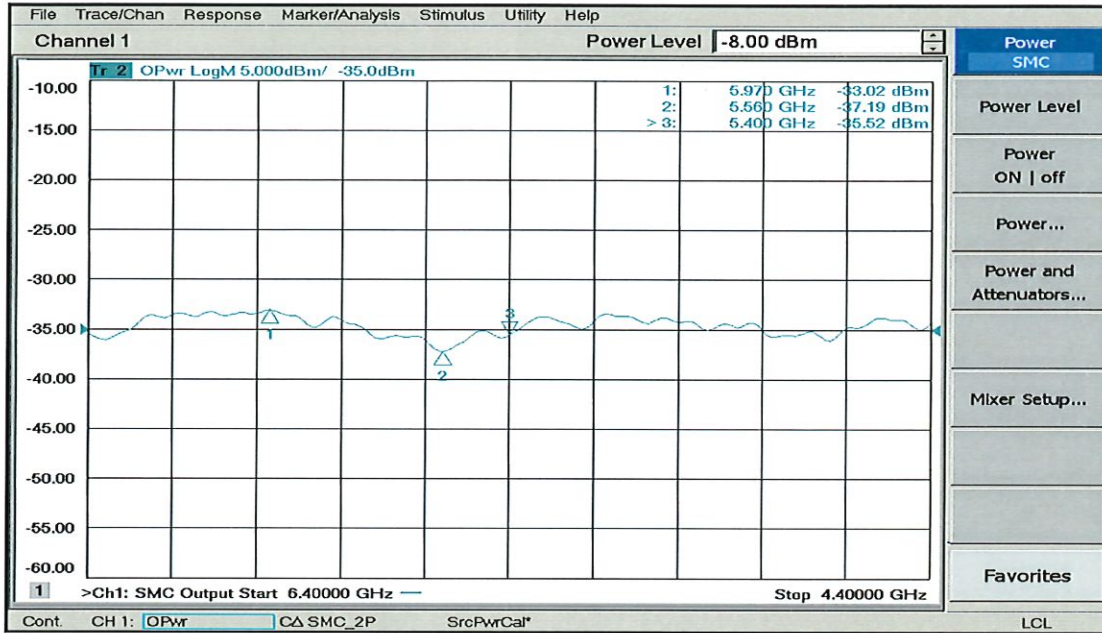


SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

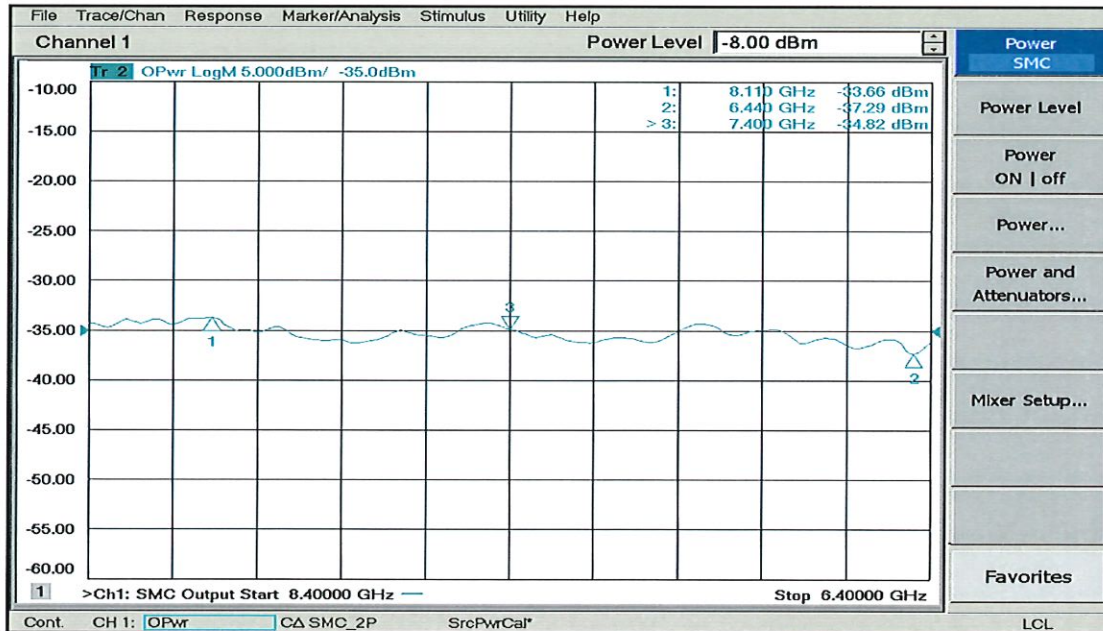
PL19642/1629

IF INPUT to RF TRANSMIT SAMPLE OUTPUT PLOTS

IF1 Input (J4A) To RF Transmit Sample Output (J8) Channel 2 (-8dBm Input)



IF1 Input (J4A) To RF Transmit Sample Output (J8) Channel 3 (-8dBm Input)



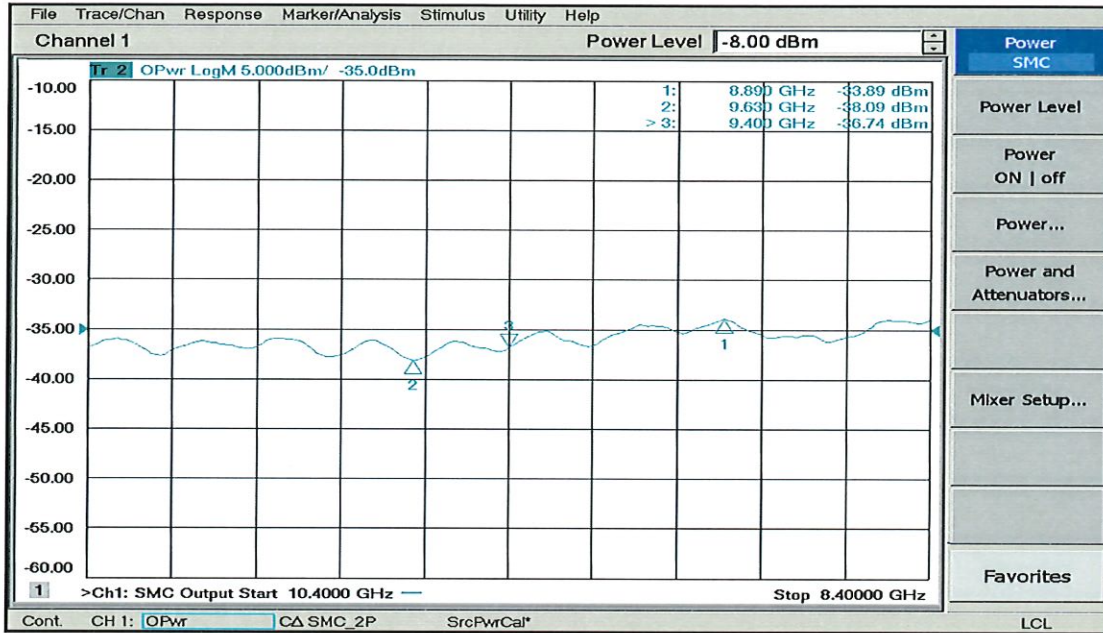


SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

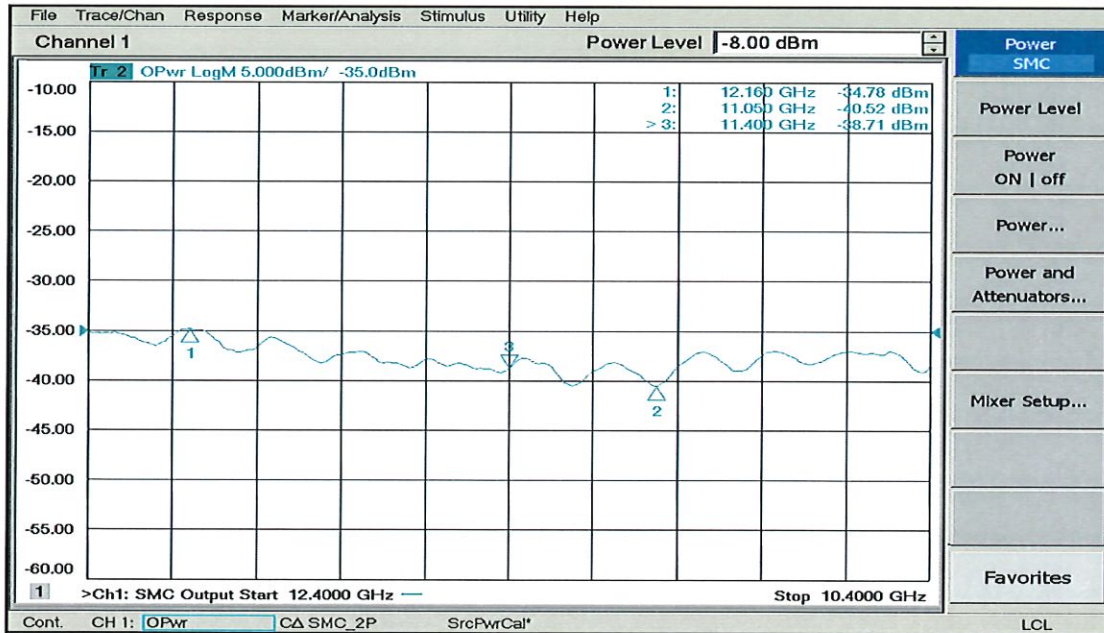
PL19642/1629

IF INPUT to RF TRANSMIT SAMPLE OUTPUT PLOTS

IF1 Input (J4A) To RF Transmit Sample Output (J8) Channel 4 (-8dBm Input)



IF1 Input (J4A) To RF Transmit Sample Output (J8) Channel 5 (-8dBm Input)



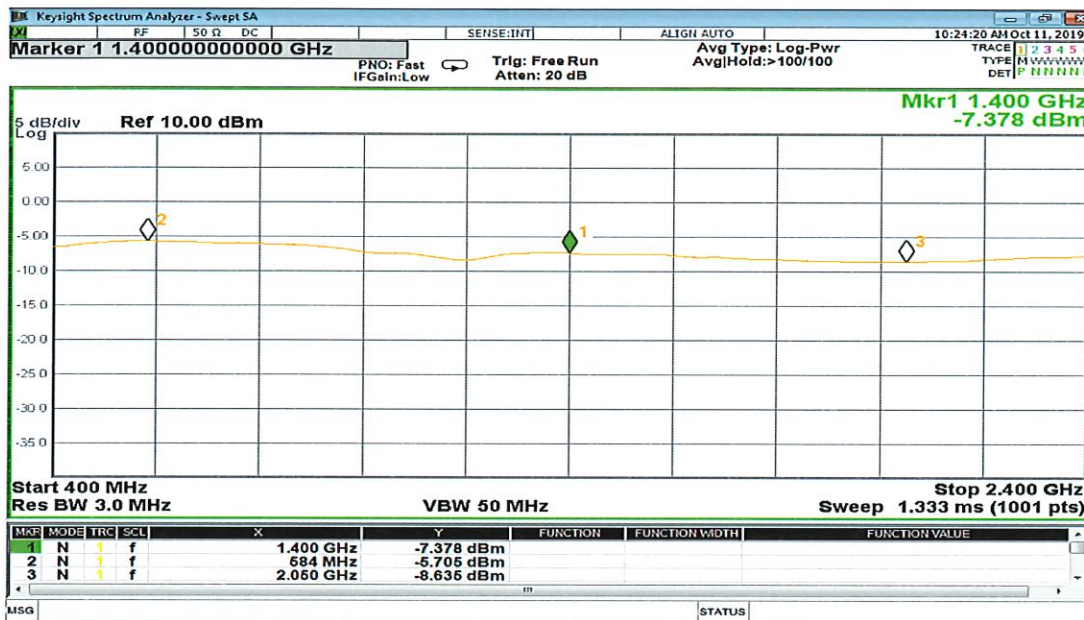


SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

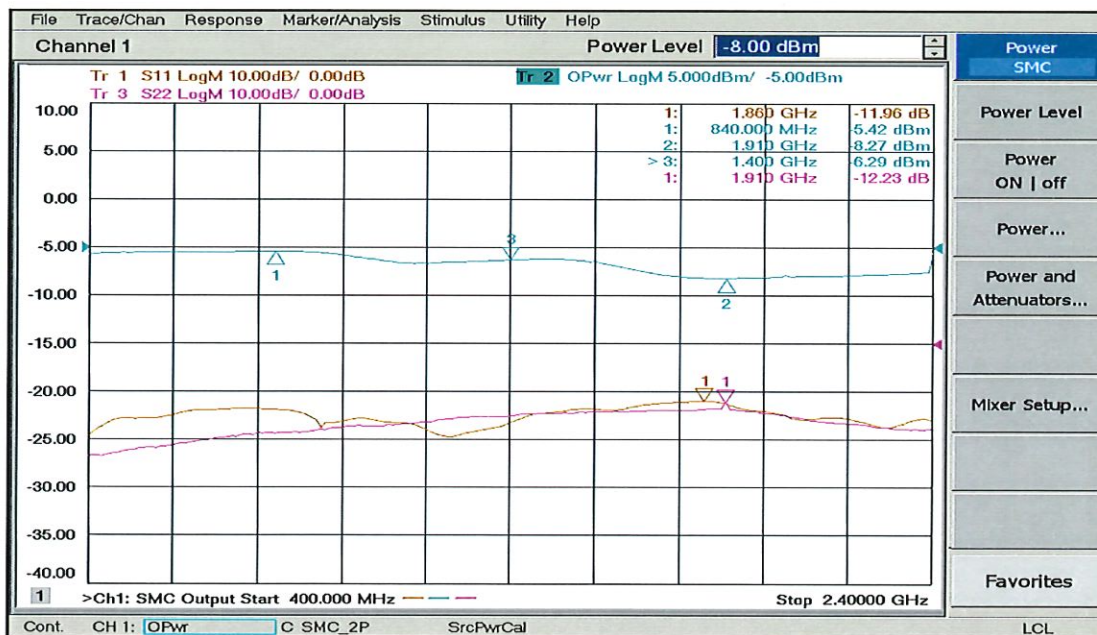
PL19642/1629

IF INPUT to RF TRANSMIT BUILT-IN-TEST OUTPUT PLOTS

IF1 Input (J4A) To IF1 Output (J3A) Thru Channel (-8dBm Input)



IF1 Input (J4A) To IF1 Output (J3A) Channel 1 (-8dBm Input)



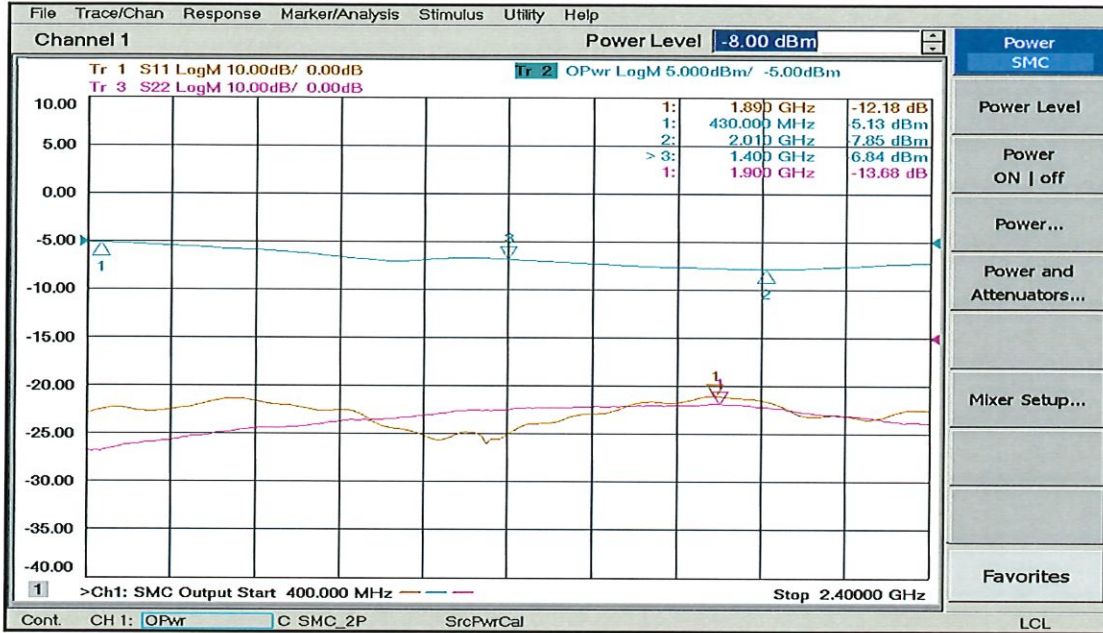


**SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH**

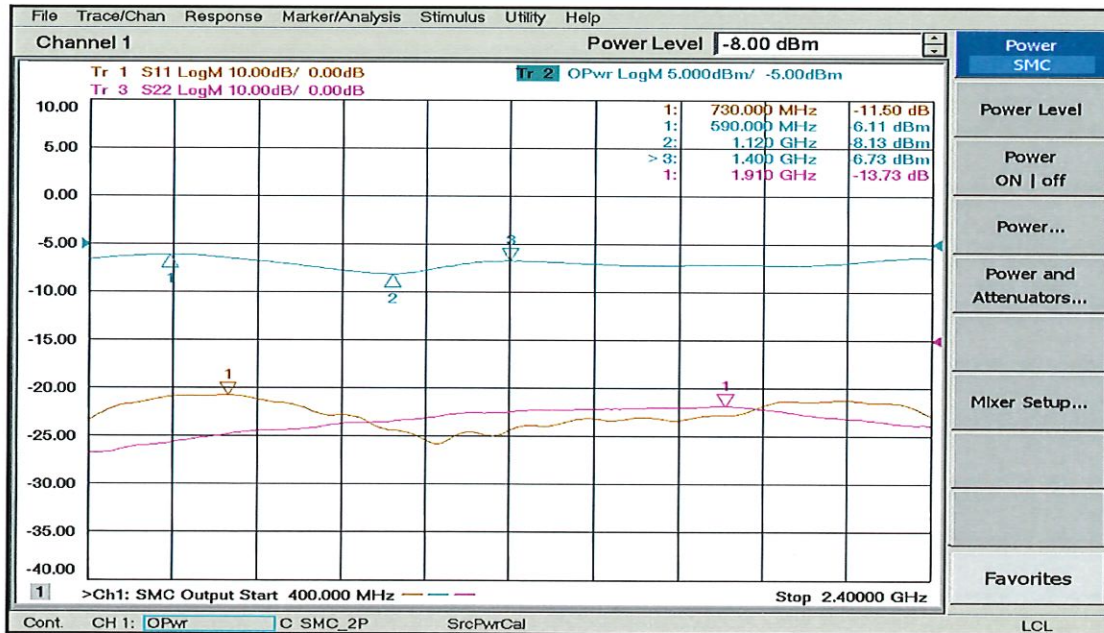
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IF INPUT to RF TRANSMIT BUILT-IN-TEST OUTPUT PLOTS

IF1 Input (J4A) To IF1 Output (J3A) Channel 2 (-8dBm Input)



IF1 Input (J4A) To IF1 Output (J3A) Channel 3 (-8dBm Input)



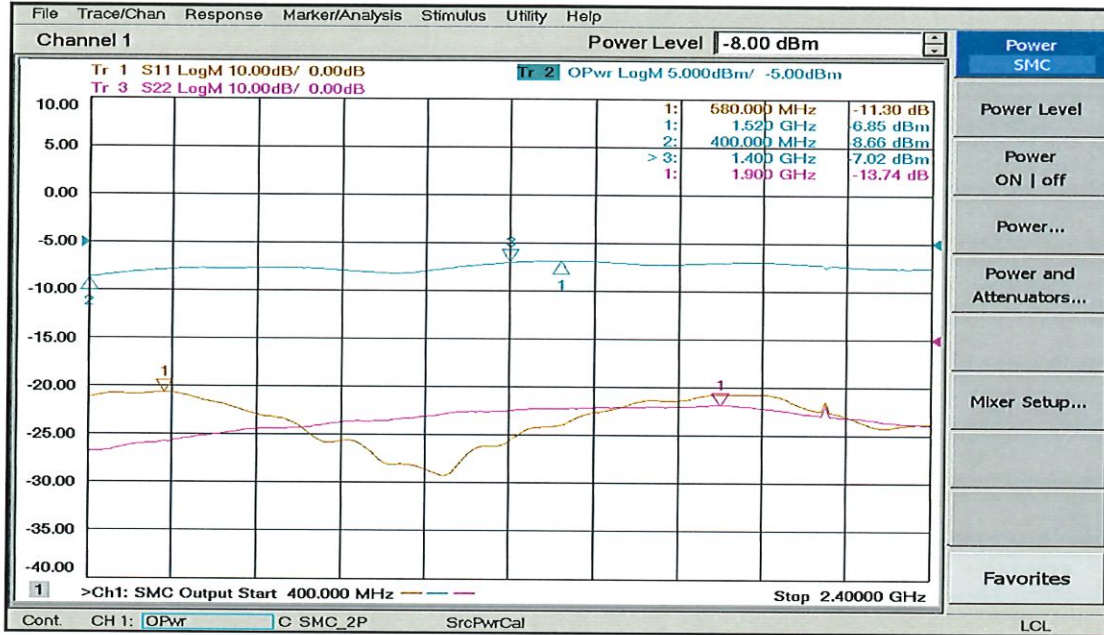


SUMMARY TEST DATA
ON
PTRAN-100M18G-SFB-3UVPX-10HP-MAH

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IF INPUT to RF TRANSMIT BUILT-IN-TEST OUTPUT PLOTS

IF1 Input (J4A) To IF1 Output (J3A) Channel 4 (-8dBm Input)



IF1 Input (J4A) To IF1 Output (J3A) Channel 5 (-8dBm Input)

