



**SUMMARY TEST DATA  
ON  
6SFB-CC-100M18G-MAH-RX-TX**

PL20385/1702

Customer: _____	Tested By: <u>RCombs</u>
SO No: _____	Temperature: <u>+25°C</u>
Model No: <u>6SFB-CC-100M18G-MAH-RX-TX</u>	Date: <u>4/14/17</u>
Serial No: <u>PL20385/1702</u>	Drawing No: <u>27624332</u> Rev: <u>A1</u>

TEST ITEM NO:	PARAMETERS	SPECIFIED VALUE	MEASURED VALUE	REMARKS QA/QC
1	J1 Input Frequency (RF RX Input)	100MHz-18.0GHz	<b>100MHz- 18.0GHz</b> See Plot	
2	J1 Input Power Level	-80dBm to -10dBm Typical	<b>-80dBm to -10dBm</b>	
3	J5 Input Frequency (RF TX Input)	100MHz-18.0GHz	<b>100MHz- 18.0GHz</b> See Plot	
4	J5 Input Power Level	-20dBm to -15dBm Typical	<b>-20dBm to -15dBm</b>	
5	J7 Input Frequency (RF BIT RX Input)	100MHz-18.0GHz	<b>100MHz- 18.0GHz</b> See Plot	
6	J7 Input Power Level	-20dBm to -15dBm Typical	<b>-20dBm to -15dBm</b>	
7	J2 Output Frequency (RF RX Output)	100MHz-18.0GHz	<b>100MHz- 18.0GHz</b> See Plot	
8	J2 Output Power Level	-62dBm to +8dBm Typical	<b>-60dBm to +15dBm</b>	
9	J6 Output Frequency (RF TX Output)	100MHz-18.0GHz	<b>100MHz- 18.0GHz</b> See Plot	
10	J6 Output Power Level	0dBm to +10dBm Typical	<b>+7dBm to +14dBm</b>	
11	J1 RX Path Gain	18dB Typical	<b>20dB to 29dB</b>	
12	J7 RX BIT Path Insertion Loss	10dB Typical	<b>-3dB to -8dB</b>	
13	(J1 to J2) to (J7 to J2) RX Isolation	100dB Typical	<b>116.51dB</b> See Plot	
14	J5 TX Path Gain	32dB Typical	<b>33dB to 40dB</b>	
15	VSWR Over 90% Passband	2 : 1 Maximum	<b>2:1</b> See Plots	



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16	Switching Speed	100ns Typical	<b>75.0ns</b> See Plots	
17	Thru Channel Passband	100MHz-18.0GHz	<b>100MHz-18.0GHz</b> See Plots	
18	Channel 1 Center Frequency	3400MHz	<b>3400MHz</b>	
19	Channel 1 3dB Bandwidth	2000MHz	<b>2000MHz</b>	
20	Channel 1 RX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-2.0GHz,	<b>-44.2dBc</b> See Plot	
		-40dBc Typical, -30dBc Minimum 4.8GHz-18.0GHz	<b>-46.3dBc</b> See Plot	
21	Channel 1 TX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-2.0GHz,	<b>-33.4dBc</b> See Plot	
		-40dBc Typical, -30dBc Minimum 4.8GHz-18.0GHz	<b>-66.5dBc</b> See Plot	
22	Channel 2 Center Frequency	5400MHz	<b>5400MHz</b>	
23	Channel 2 3dB Bandwidth	2000MHz	<b>2000MHz</b>	
24	Channel 2 RX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-4.0GHz,	<b>-62.6dBc</b> See Plot	
		-40dBc Typical, -30dBc Minimum 6.8GHz-18.0GHz	<b>-31.2dBc</b> See Plot	
25	Channel 2 TX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-4.0GHz,	<b>-65.4dBc</b> See Plot	
		-40dBc Typical, -30dBc Minimum 6.8GHz-18.0GHz	<b>-34.6dBc</b> See Plot	
26	Channel 3 Center Frequency	7400MHz	<b>7400MHz</b>	
27	Channel 3 3dB Bandwidth	2000MHz	<b>2000MHz</b>	
28	Channel 3 RX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-6.0GHz,	<b>-48.1dBc</b> See Plot	
		-40dBc Typical, -30dBc Minimum 8.8GHz-18.0GHz	<b>-30.9dBc</b> See Plot	



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29	Channel 3 TX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-6.0GHz,  -40dBc Typical, -30dBc Minimum 8.8GHz-18.0GHz	<b>-53.4dBc See Plot</b>  <b>-30.2dBc See Plot</b>	
30	Channel 4 Center Frequency	9400MHz	<b>9400MHz</b>	
31	Channel 4 3dB Bandwidth	2000MHz	<b>2000MHz</b>	
32	Channel 4 RX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-8.0GHz,  -40dBc Typical, -30dBc Minimum 10.8GHz-18.0GHz	<b>-30.3dBc See Plot</b>  <b>-35.5dBc See Plot</b>	
33	Channel 4 TX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-8.0GHz,  -40dBc Typical, -30dBc Minimum 10.8GHz-18.0GHz	<b>-30.8dBc See Plot</b>  <b>-41.5dBc See Plot</b>	
34	Channel 5 Center Frequency	11400MHz	<b>11400MHz</b>	
35	Channel 5 3dB Bandwidth	2000MHz	<b>2000MHz</b>	
36	Channel 5 RX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-10.0GHz,  -40dBc Typical, -30dBc Minimum 12.8GHz-18.0GHz	<b>-66.0dBc See Plot</b>  <b>-34.4dBc See Plot</b>	
37	Channel 5 TX Rejection	-40dBc Typical, -30dBc Minimum 100MHz-10.0GHz,  -40dBc Typical, -30dBc Minimum 12.8GHz-18.0GHz	<b>-62.2dBc See Plot</b>  <b>-33.7dBc See Plot</b>	
38	Control Logic	TTL '0': 0V to 0.8V TTL '1': 2V to 5V	<b>Pass</b>	



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QA/QC Approval:

Date:

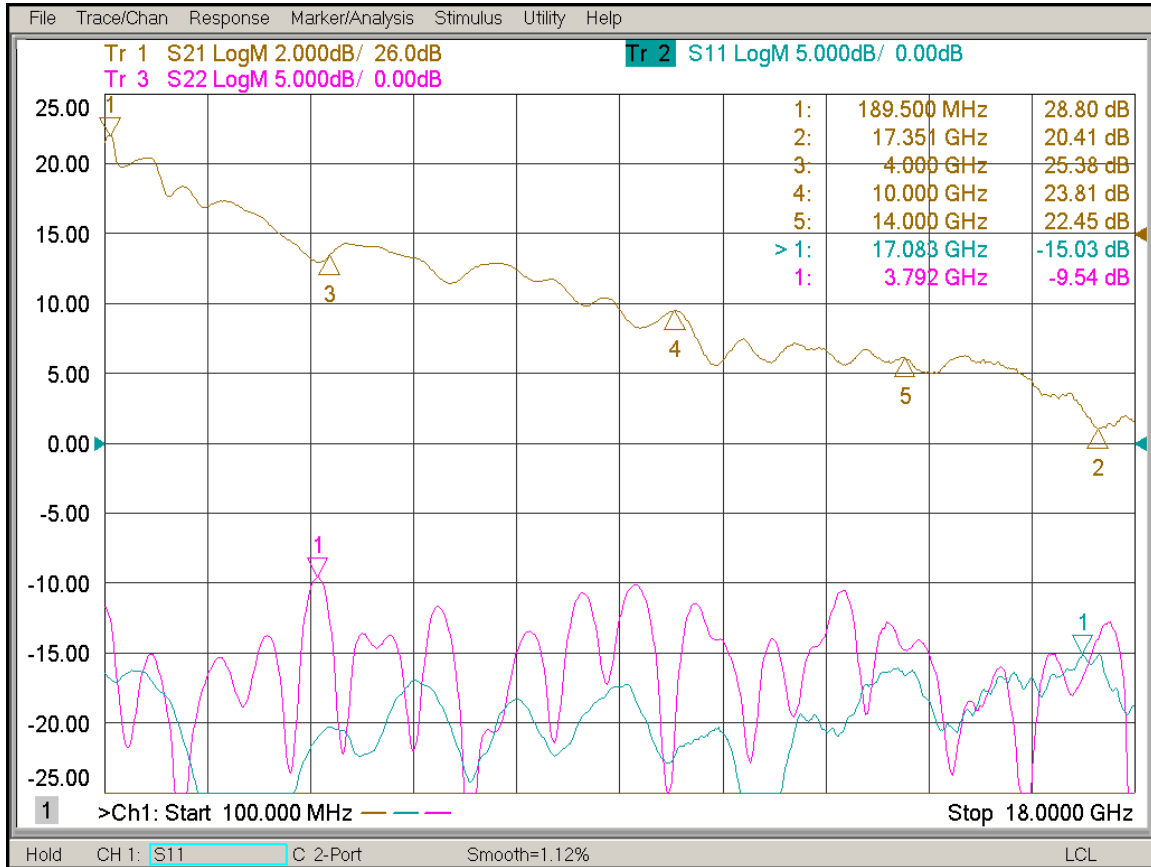
39	Power Supplies	+12V @ 600mA Max +5V @ 550mA Max -12V @ 300mA Max	+12V @ 401mA +5V @ 83mA -12V @ 133mA	
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**RX High Gain Thru Path (J1 RX IN)**

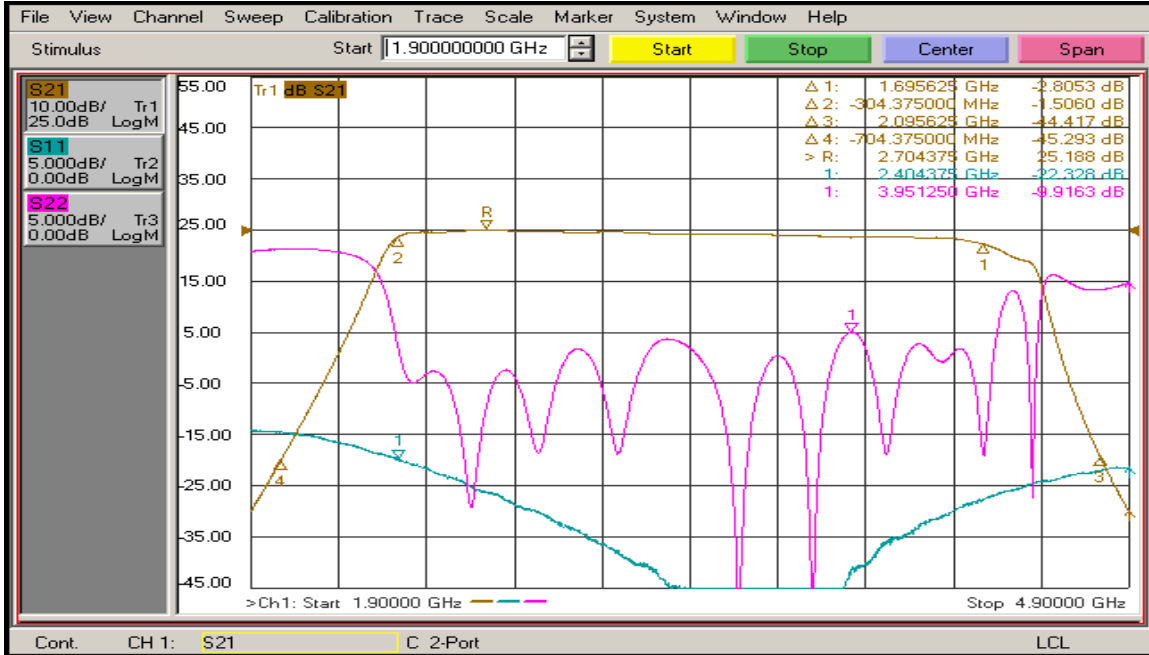




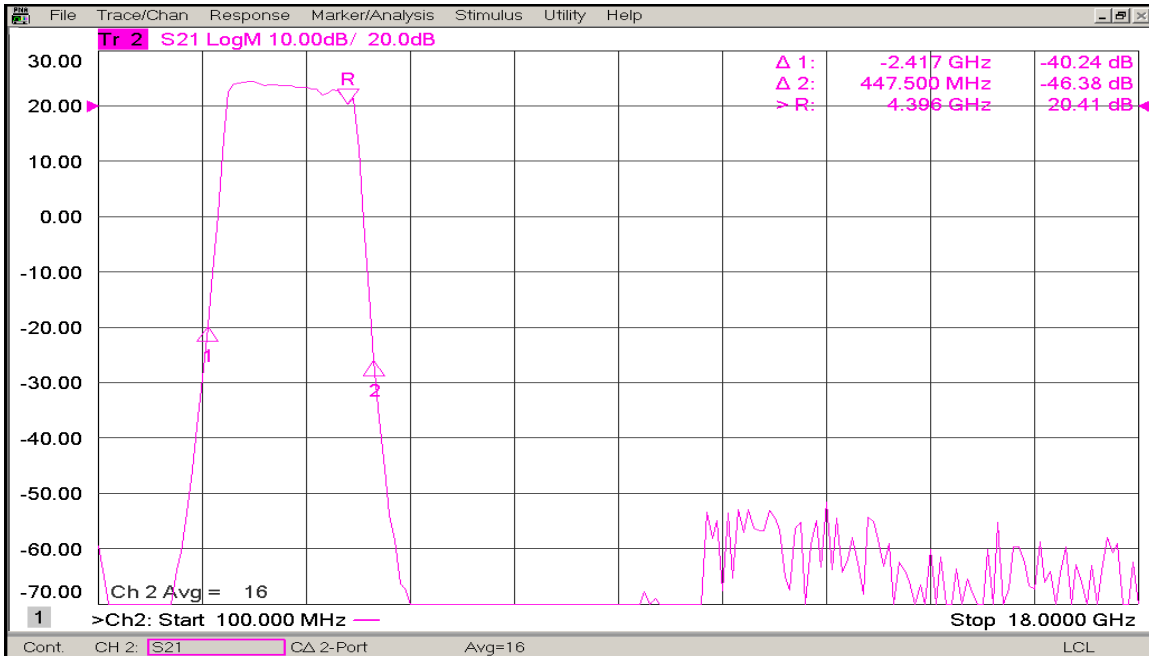
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**RX Ch1 High Gain Path Narrow Band (J1 RX IN)**



**RX Ch1 High Gain Path Broadband (J1 RX IN)**

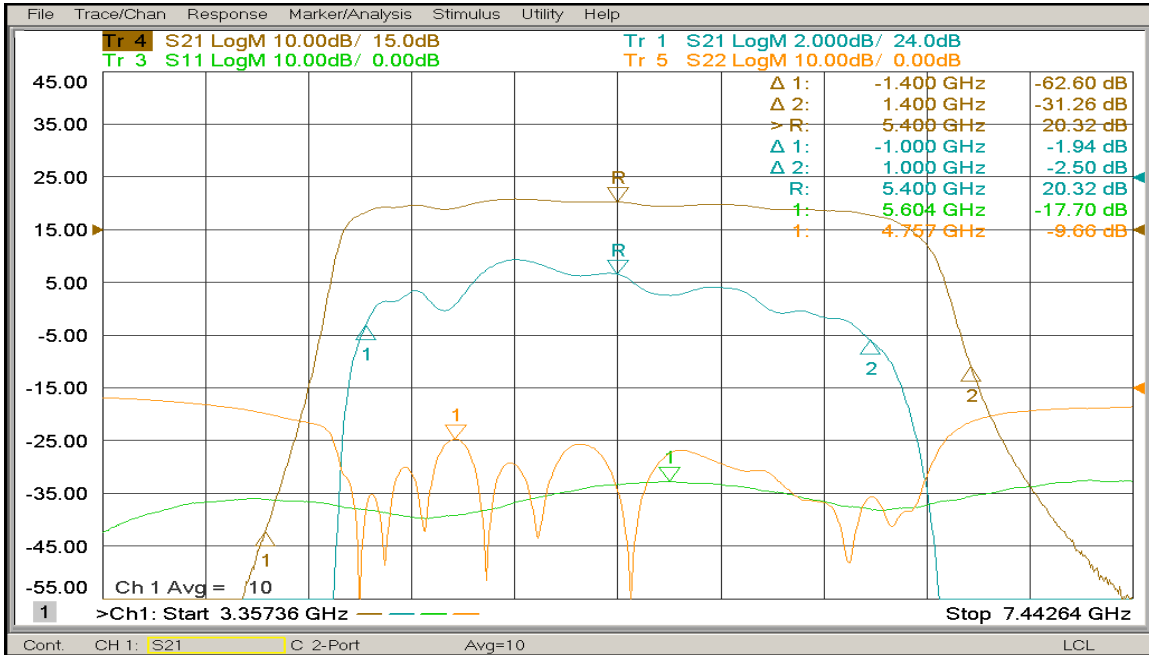




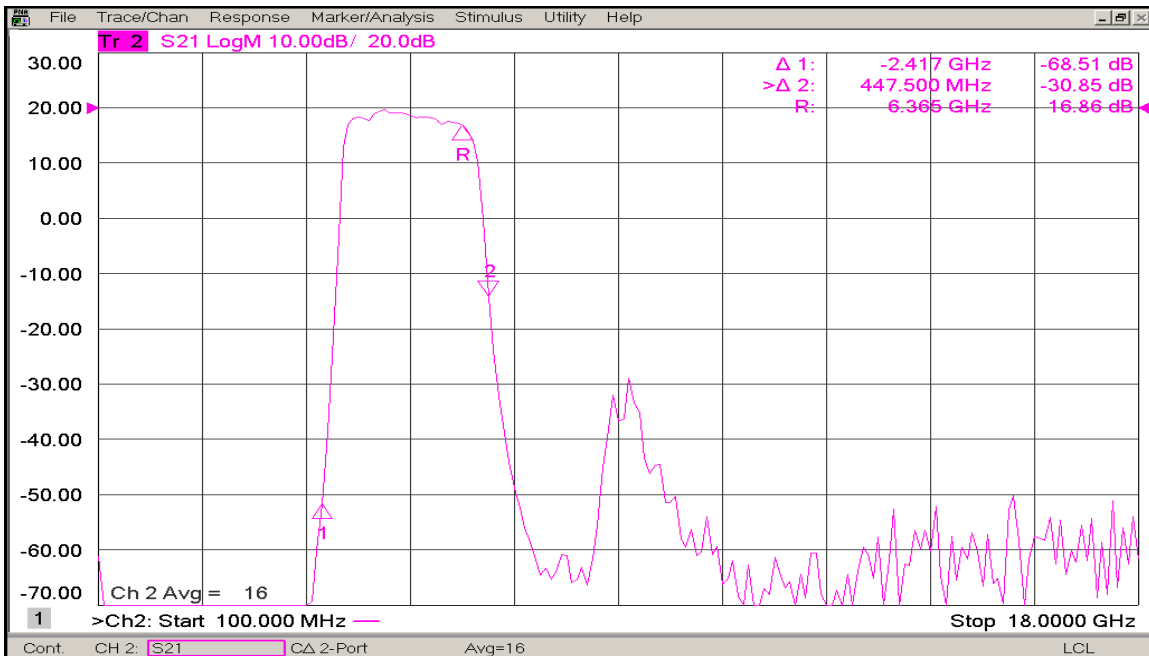
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**RX Ch2 High Gain Path Narrow Band (J1 RX IN)**



**RX Ch2 High Gain Path Broadband (J1 RX IN)**

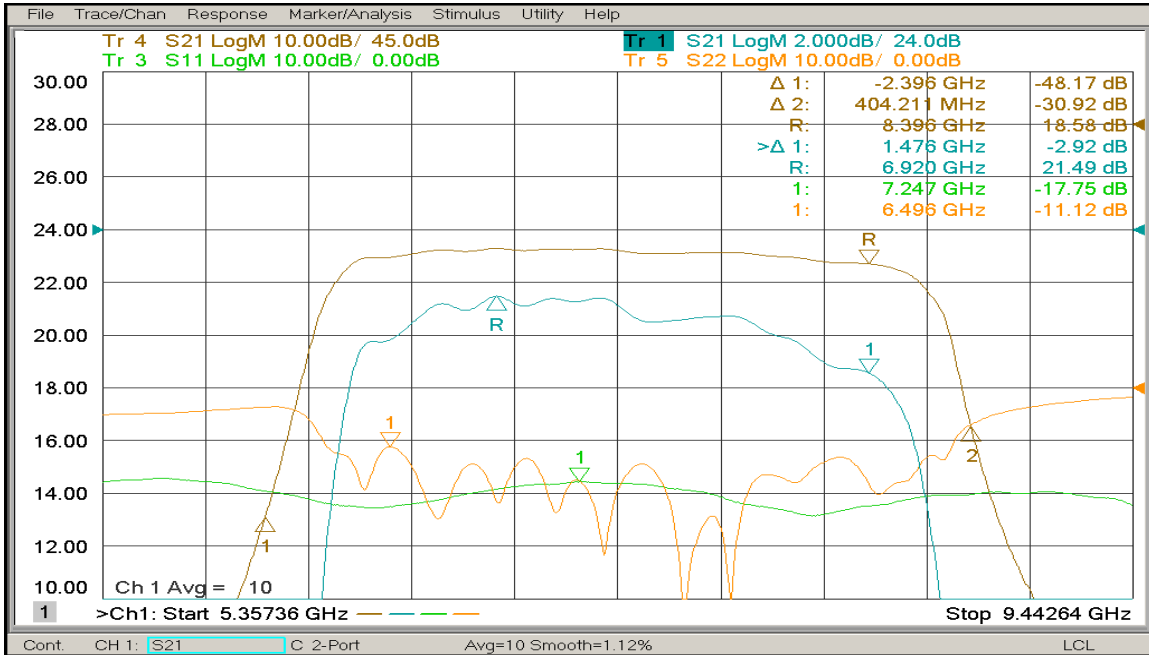




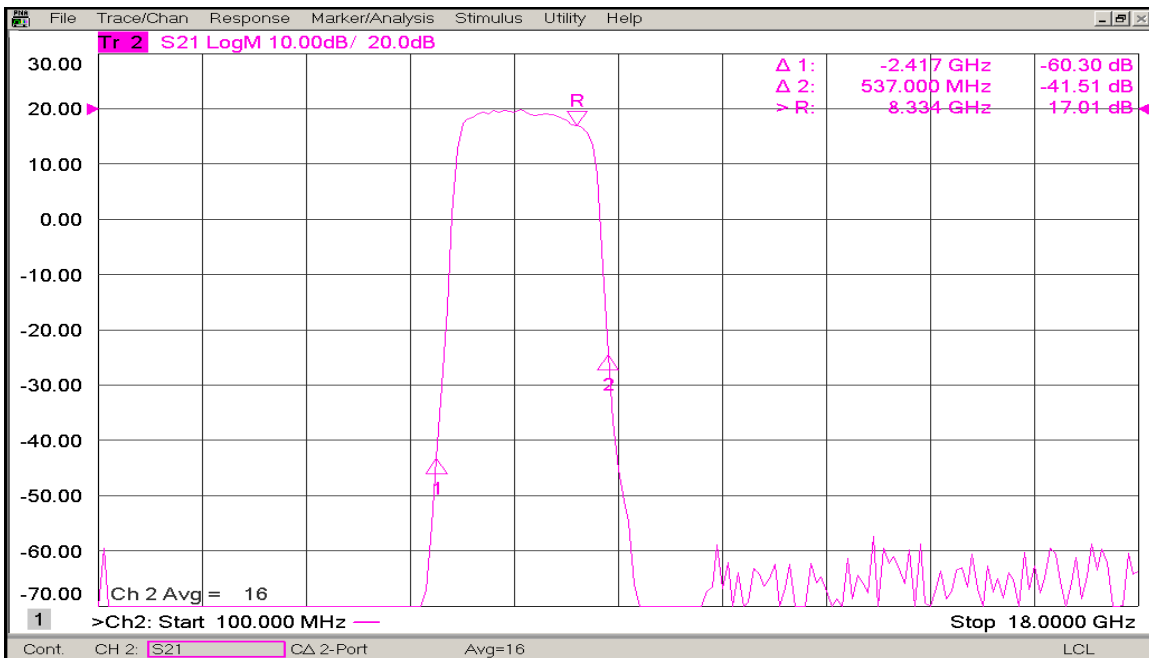
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**RX Ch3 High Gain Path Narrow Band (J1 RX IN)**



**RX Ch3 High Gain Path Broadband (J1 RX IN)**



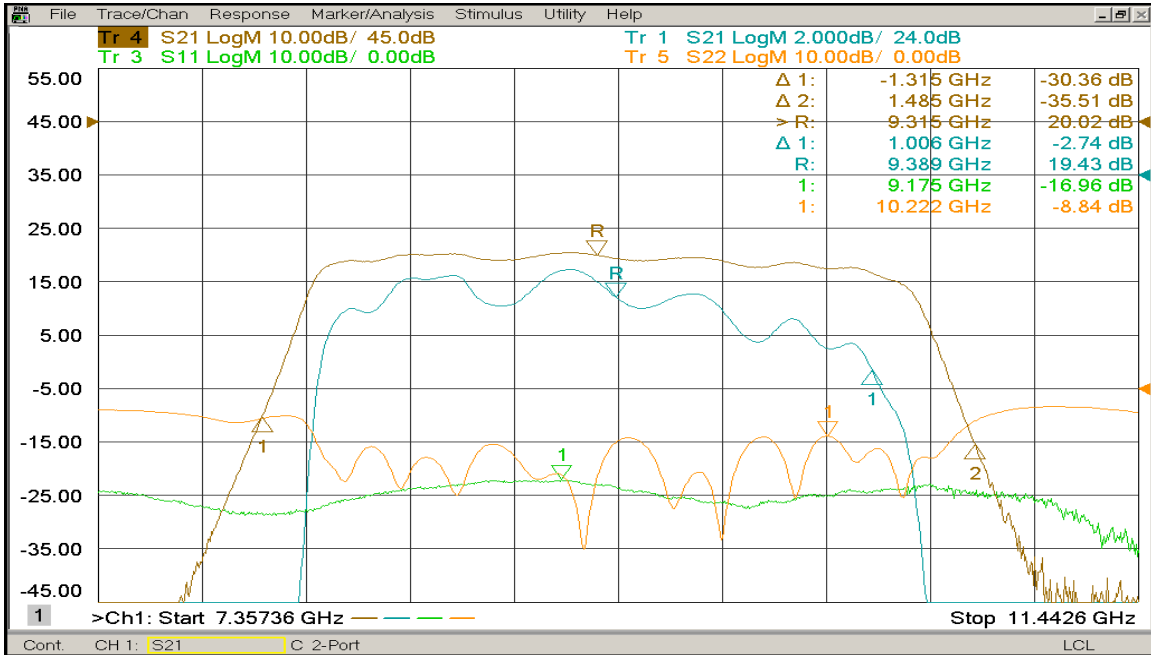




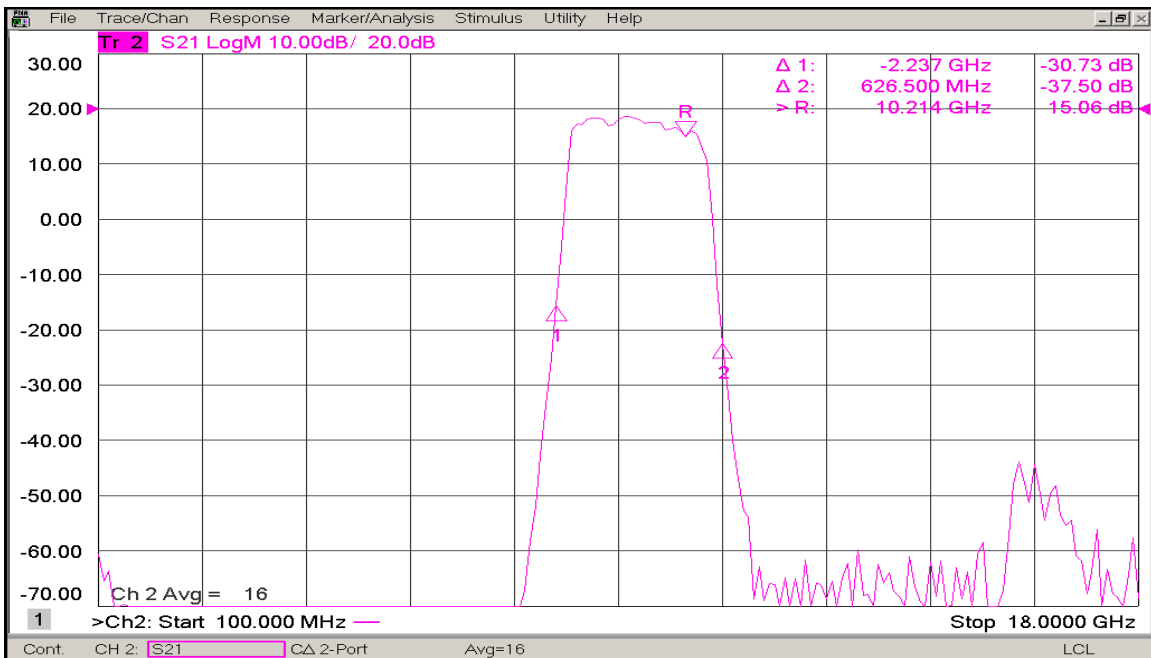
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**RX Ch4 High Gain Path Narrow Band (J1 RX IN)**



**RX Ch4 High Gain Path Broadband (J1 RX IN)**

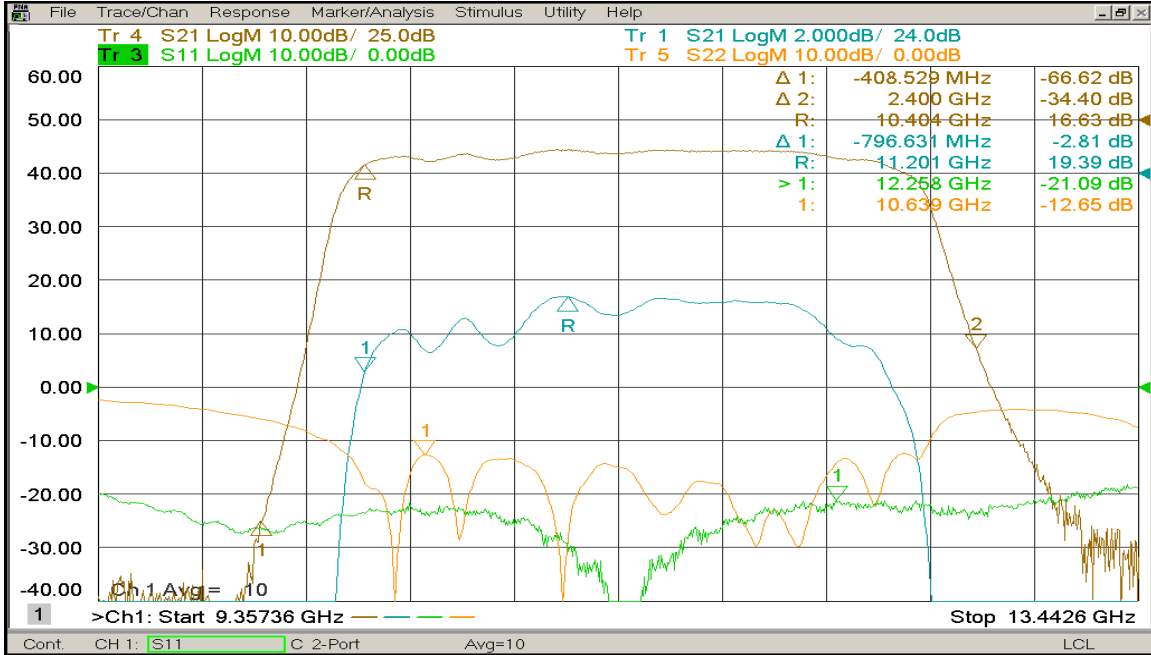




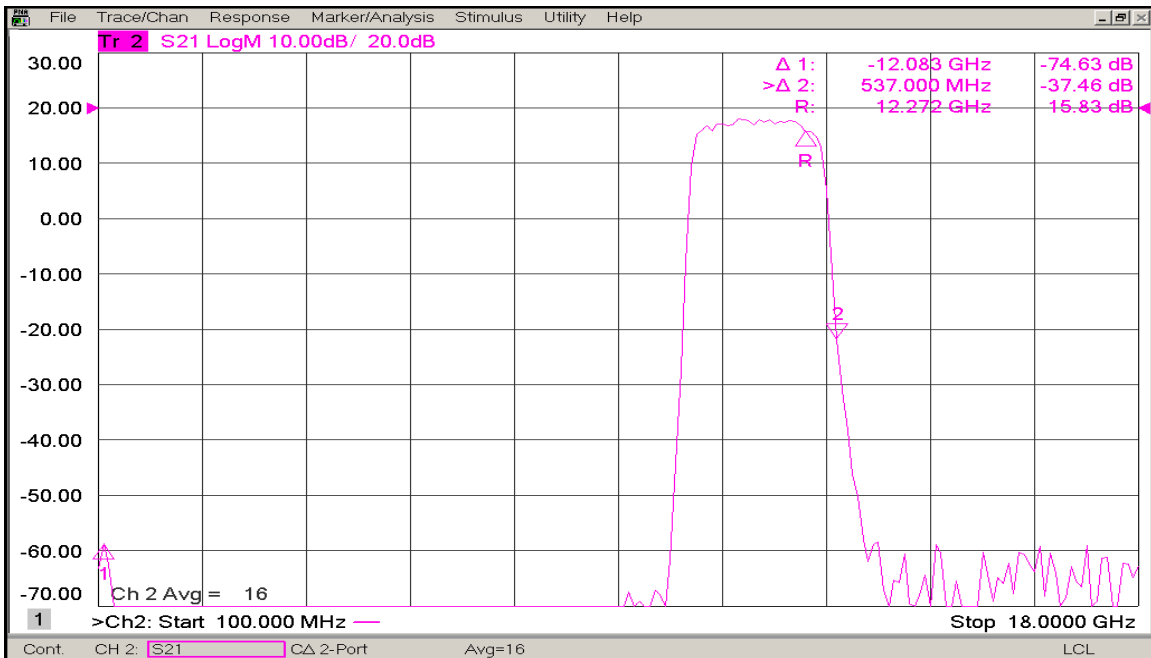
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**RX Ch5 High Gain Path Narrow Band (J1 RX IN)**



**RX Ch5 High Gain Path Broadband (J1 RX IN)**

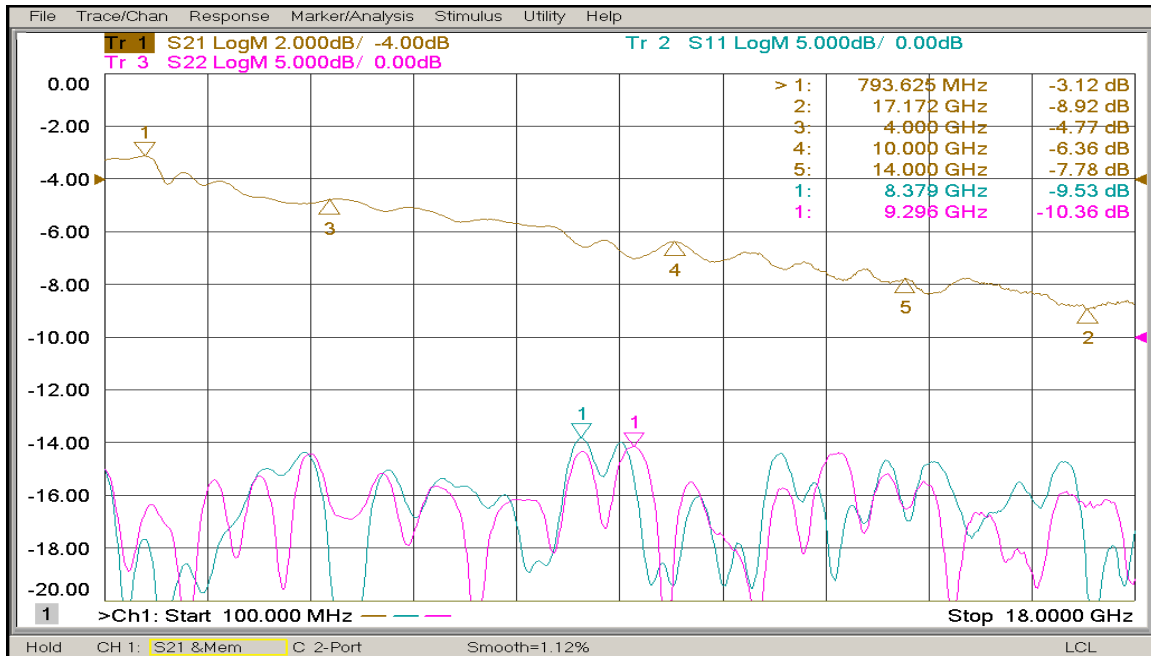




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**RX Low Gain Thru Path (J7 RX BIT IN)**

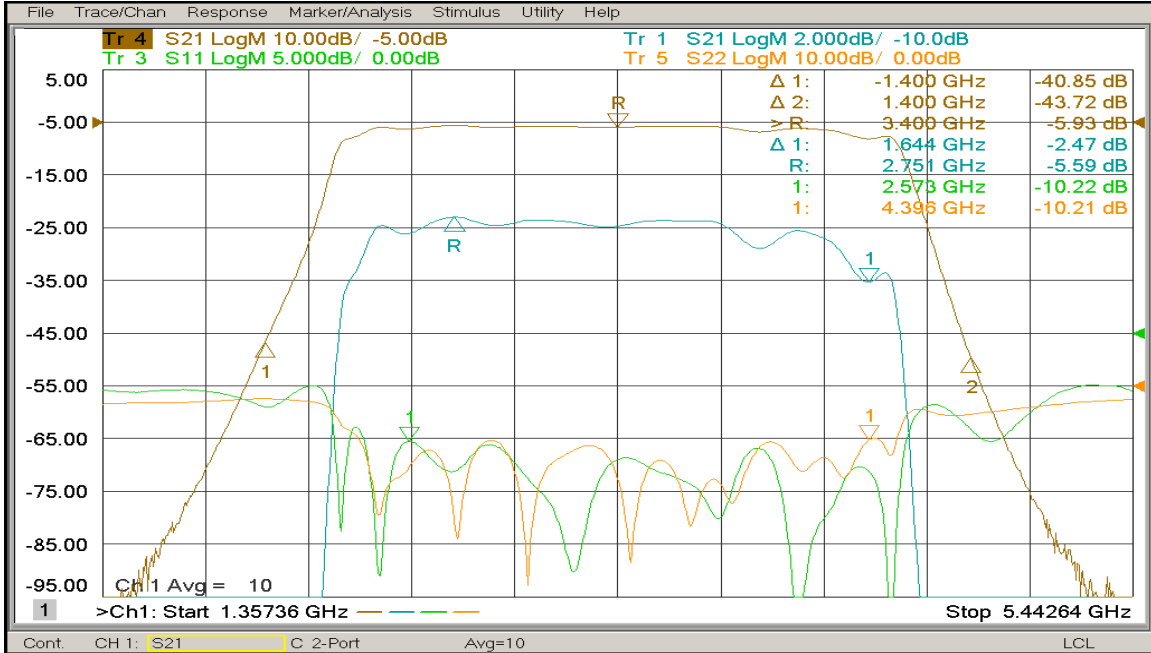




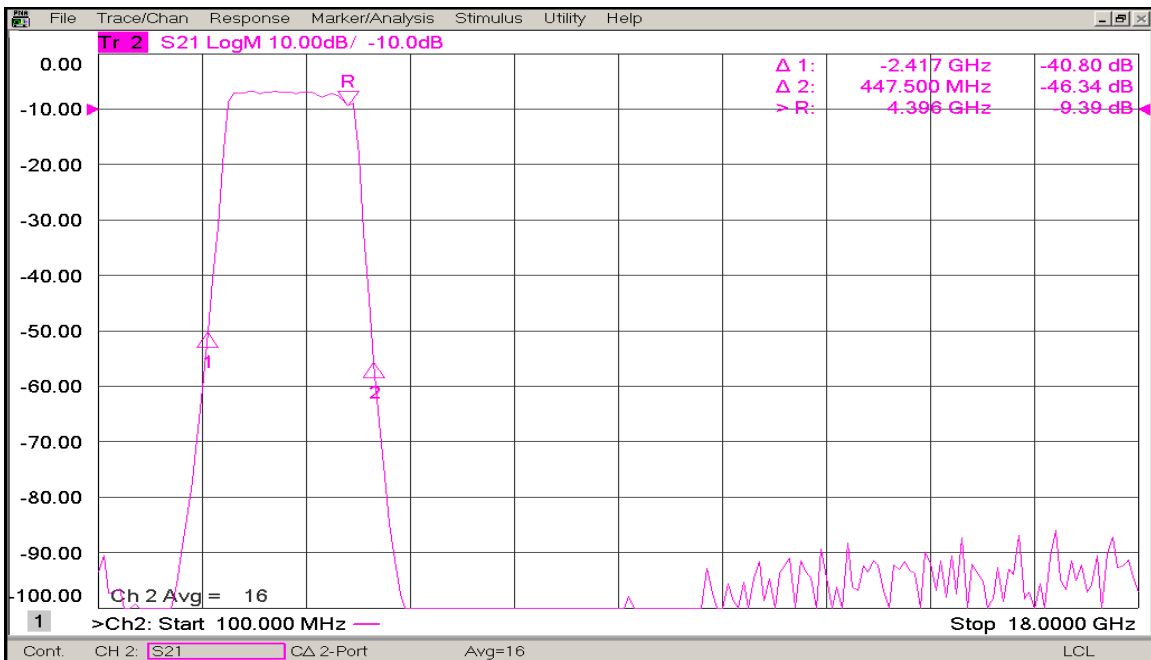
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**RX Ch1 Low Gain Path Narrow Band (J7 RX BIT IN)**



**RX Ch1 Low Gain Path Broadband (J7 RX BIT IN)**

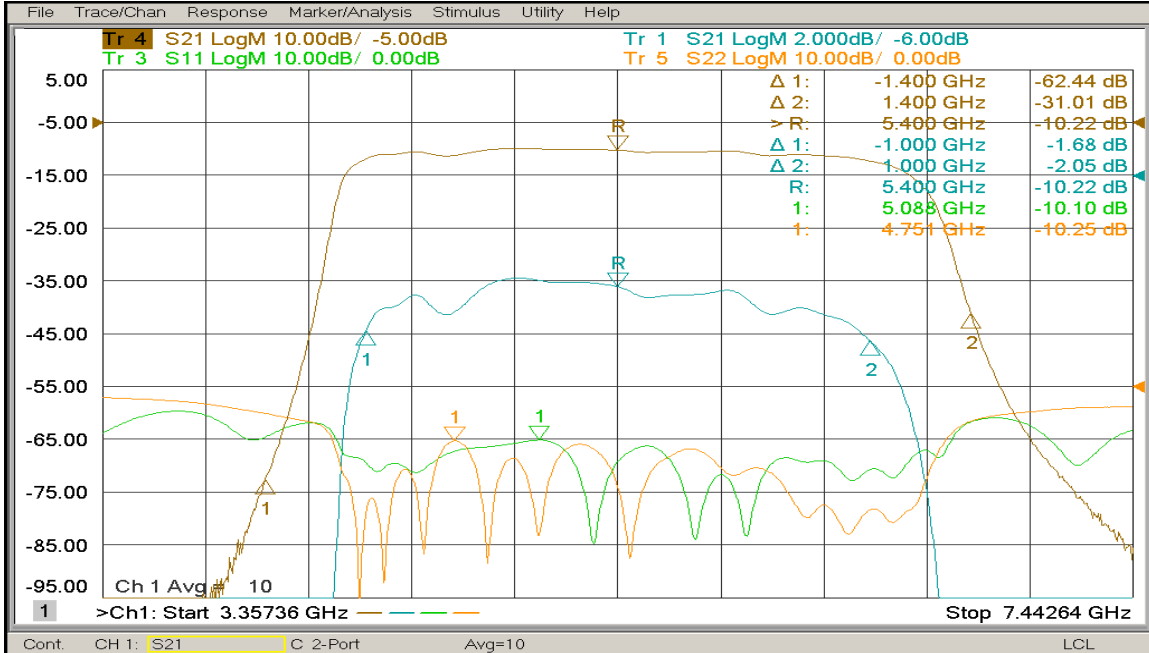




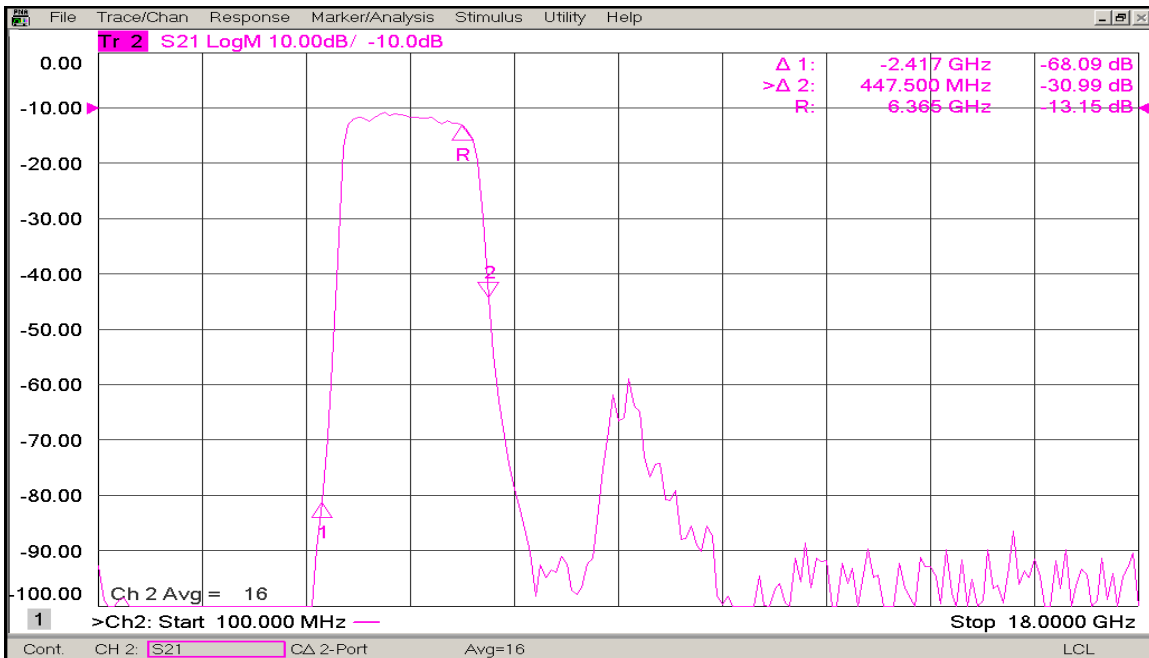
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**RX Ch2 Low Gain Path Narrow Band (J7 RX BIT IN)**



**RX Ch2 Low Gain Path Broadband (J7 RX BIT IN)**

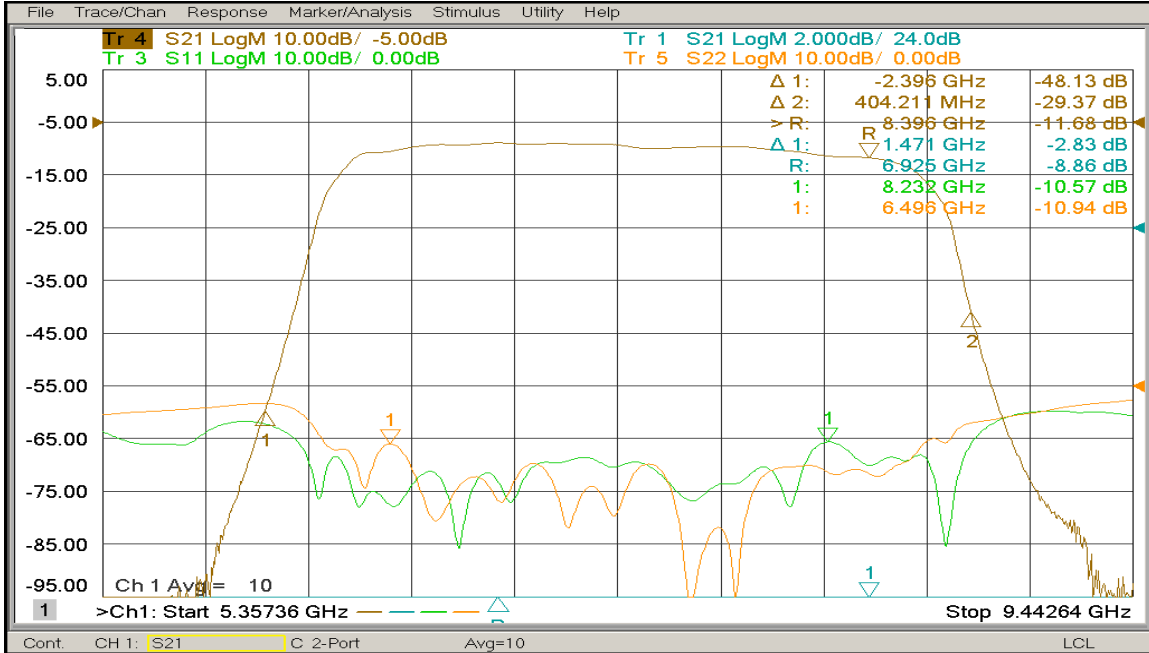




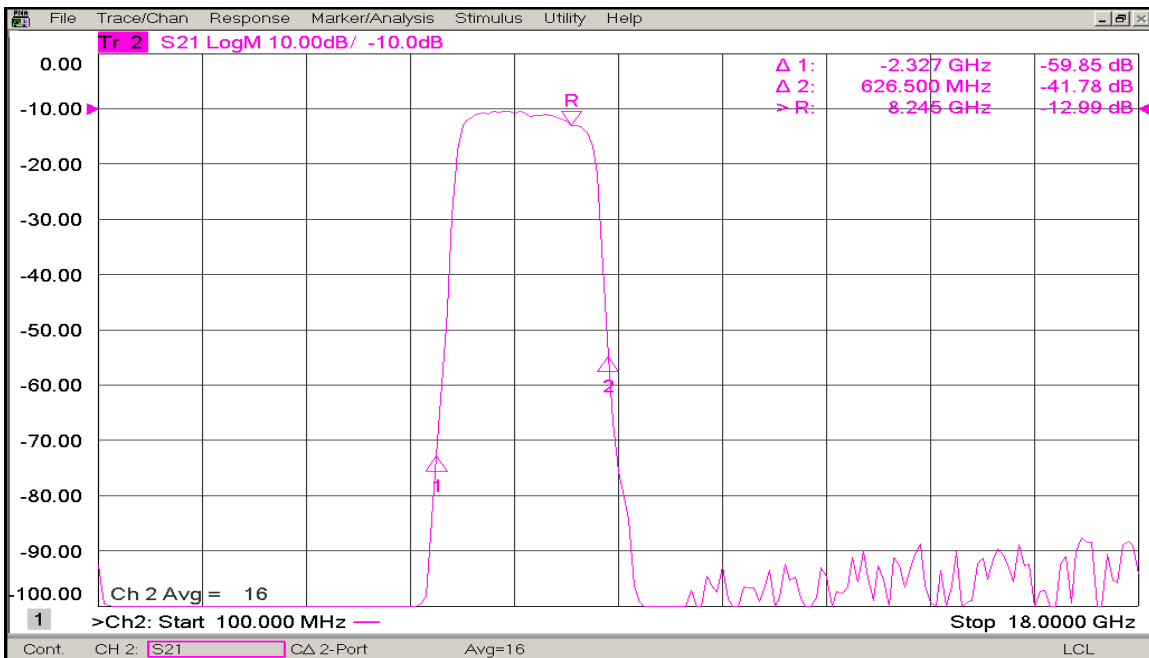
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**RX Ch3 Low Gain Path Narrow Band (J7 RX BIT IN)**



**RX Ch3 Low Gain Path Broadband (J7 RX BIT IN)**

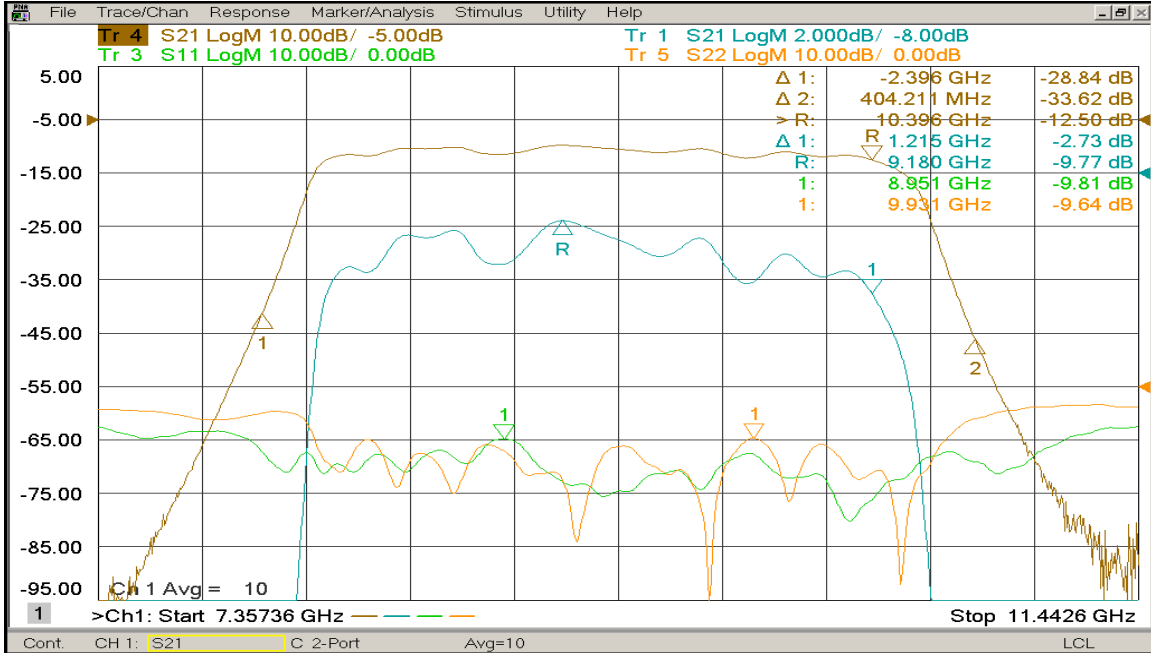




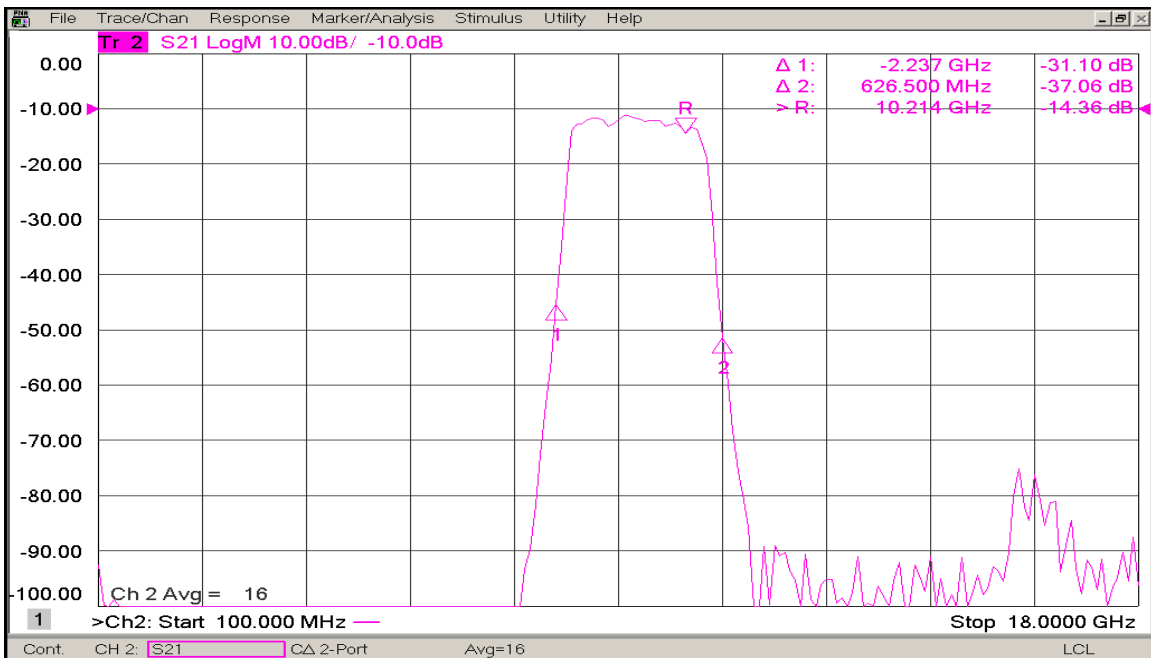
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**RX Ch4 Low Gain Path Narrow Band (J7 RX BIT IN)**



**RX Ch4 Low Gain Path Broadband (J7 RX BIT IN)**

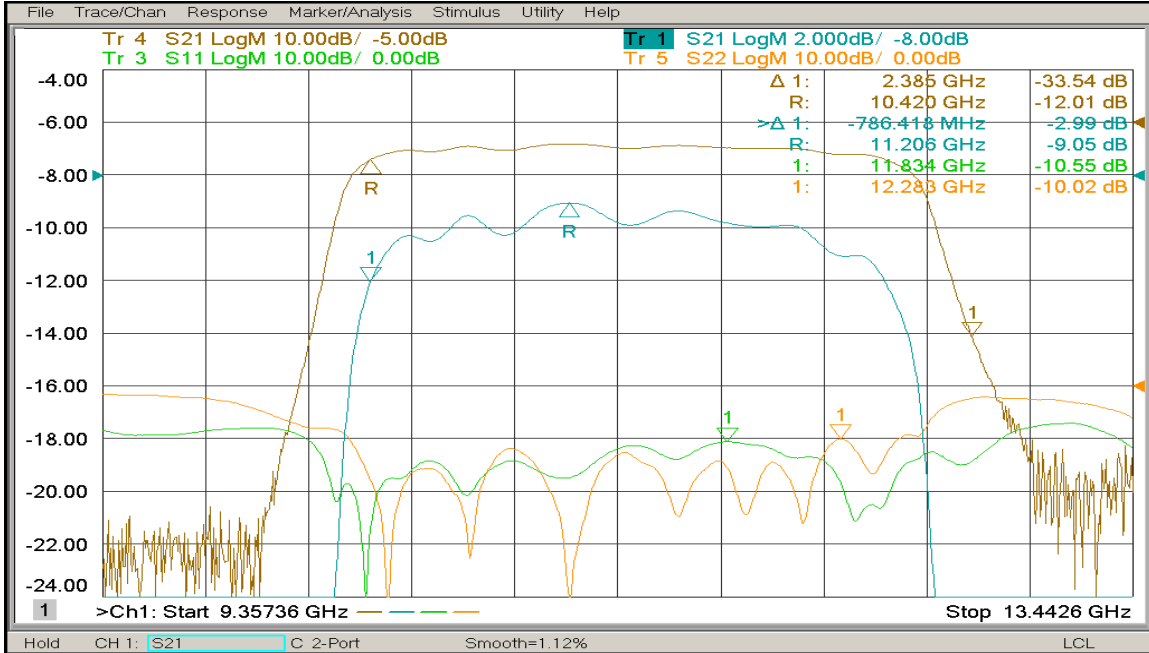




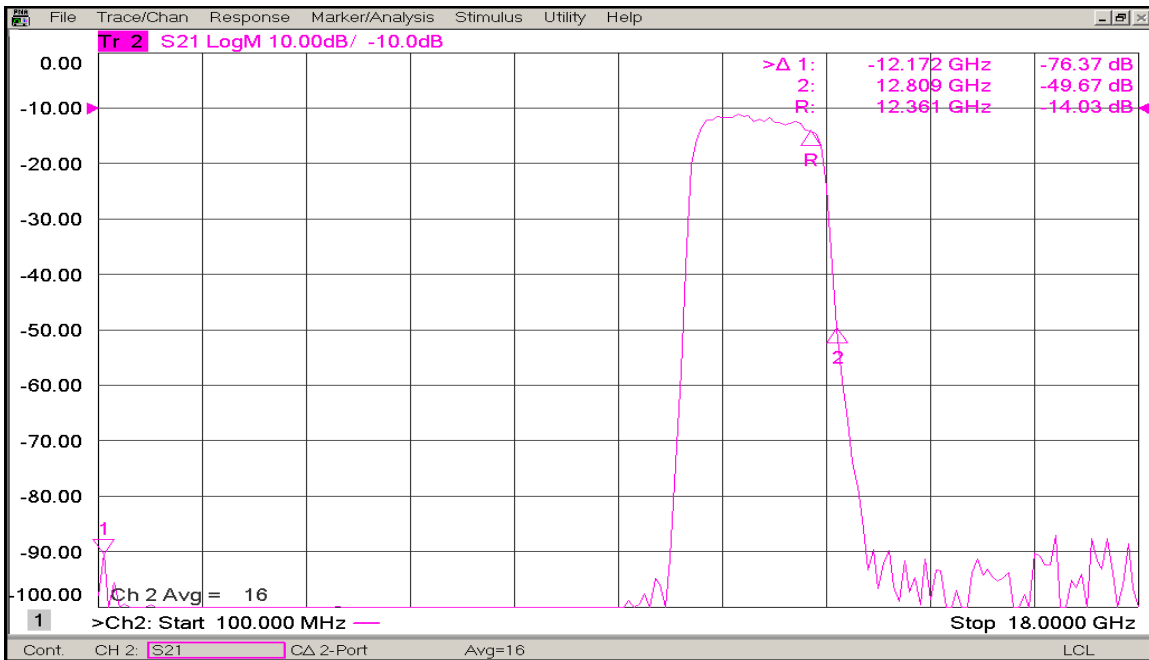
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**RX Ch5 Low Gain Path Narrow Band (J7 RX BIT IN)**



**RX Ch5 Low Gain Path Broadband (J7 RX BIT IN)**



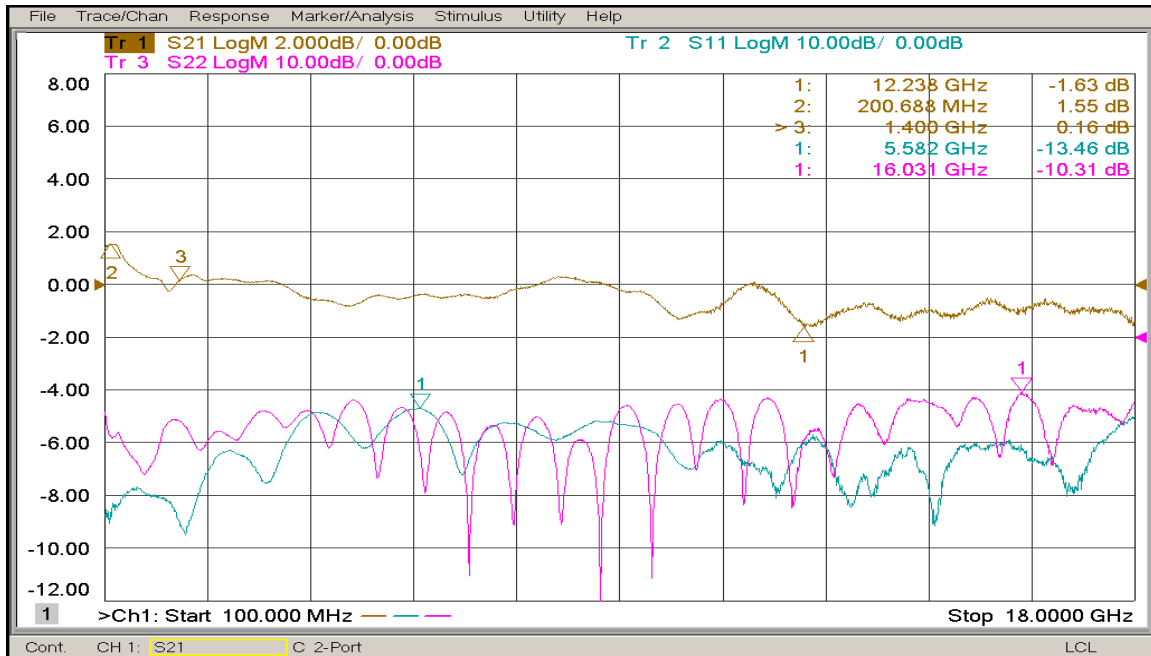




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**TX Thru Path**

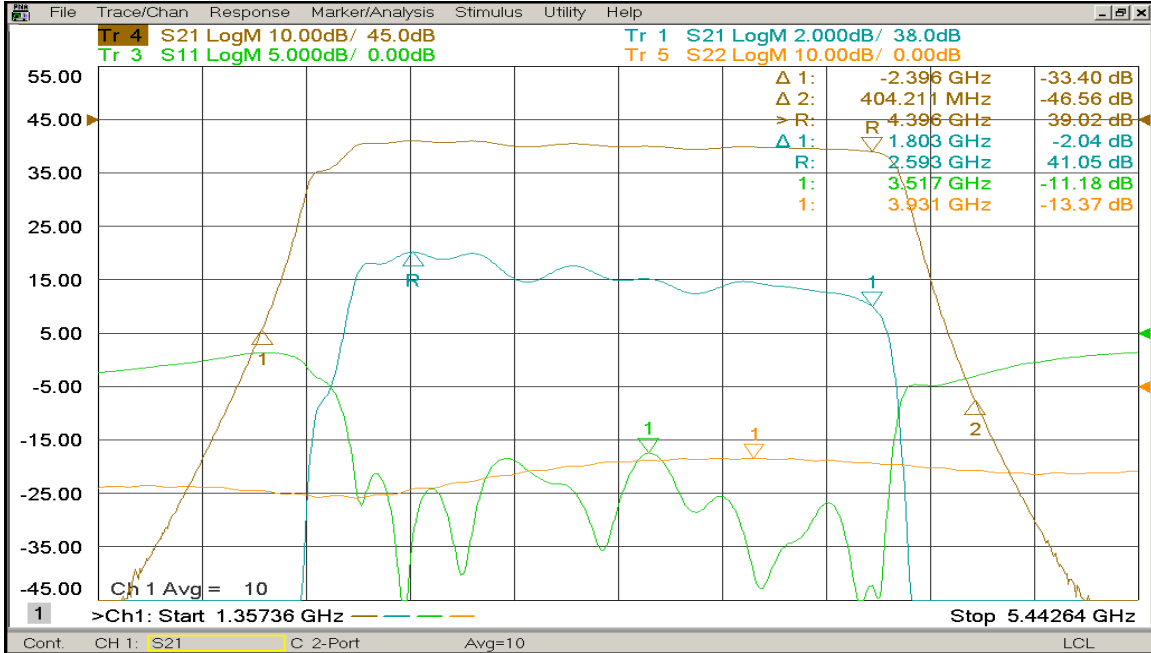




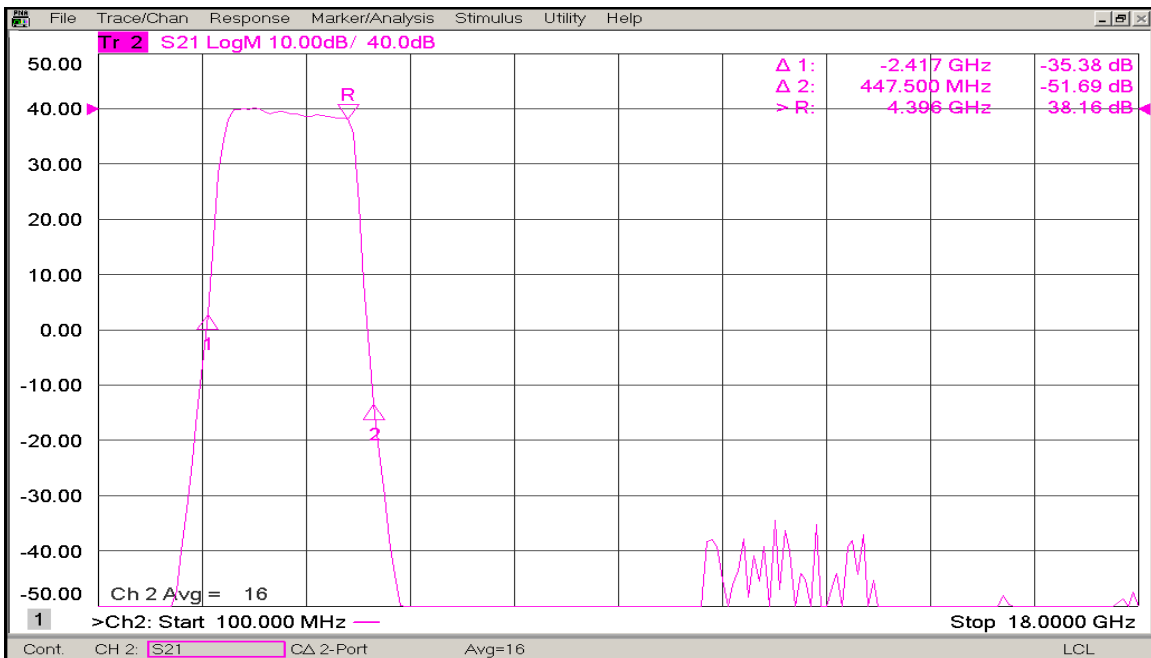
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**TX Ch1 Path Narrow Band**



**TX Ch1 Path Broadband**

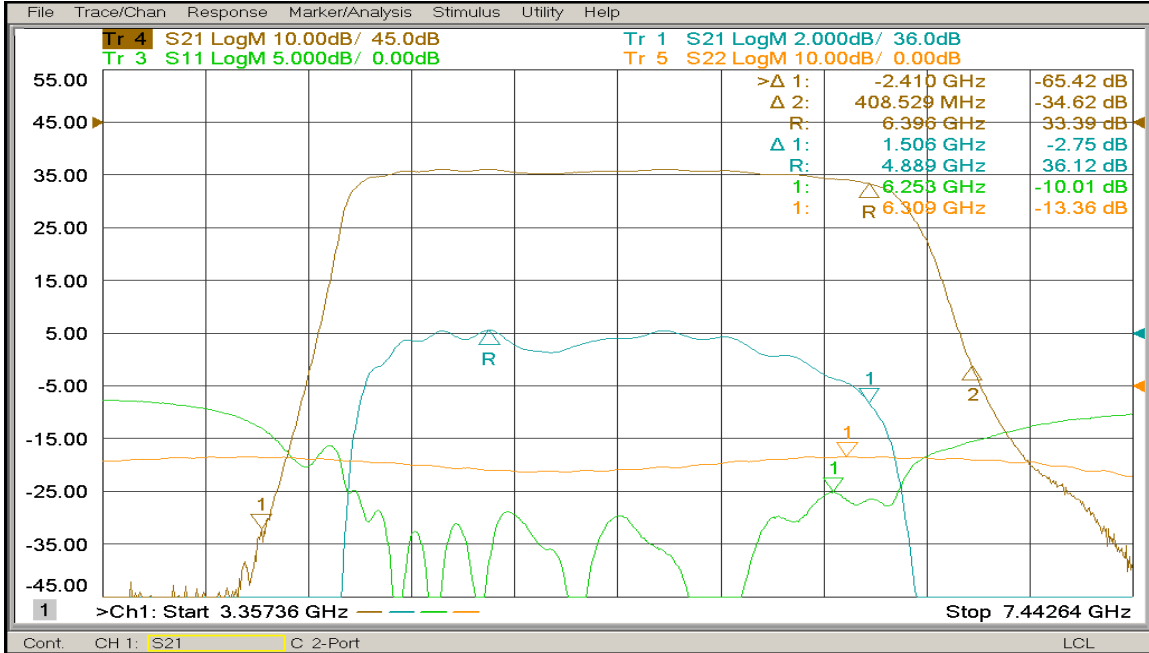




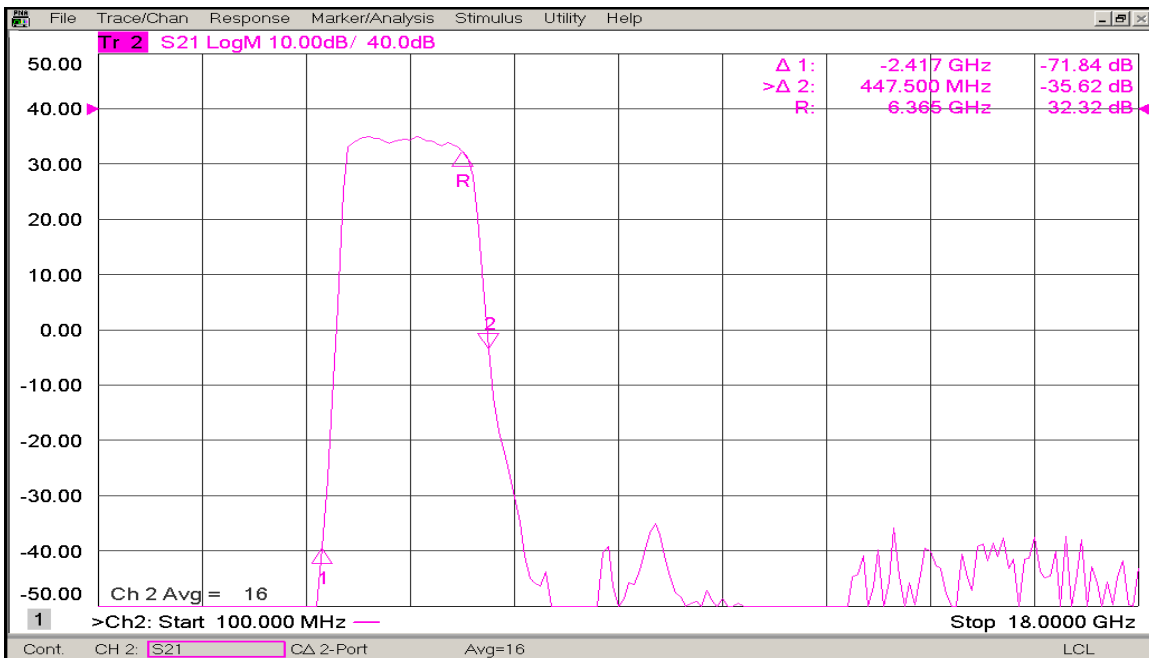
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## TX Ch2 Path Narrow Band



## TX Ch2 Path Broadband

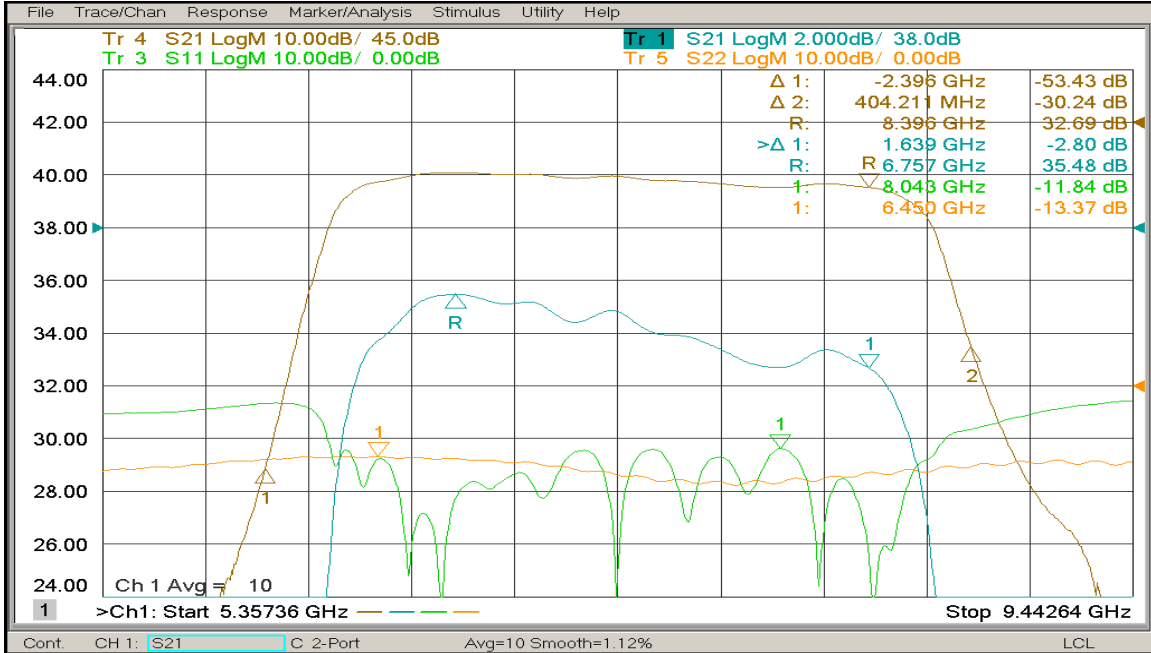




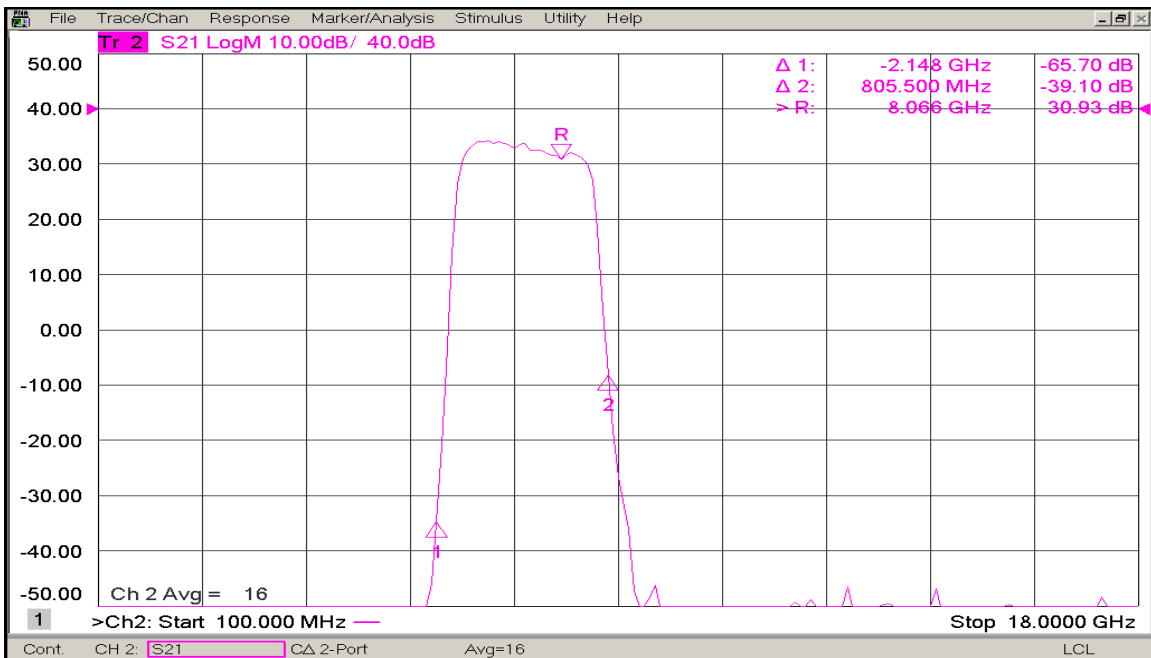
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**TX Ch3 Path Narrow Band**



**TX Ch3 Path Broadband**

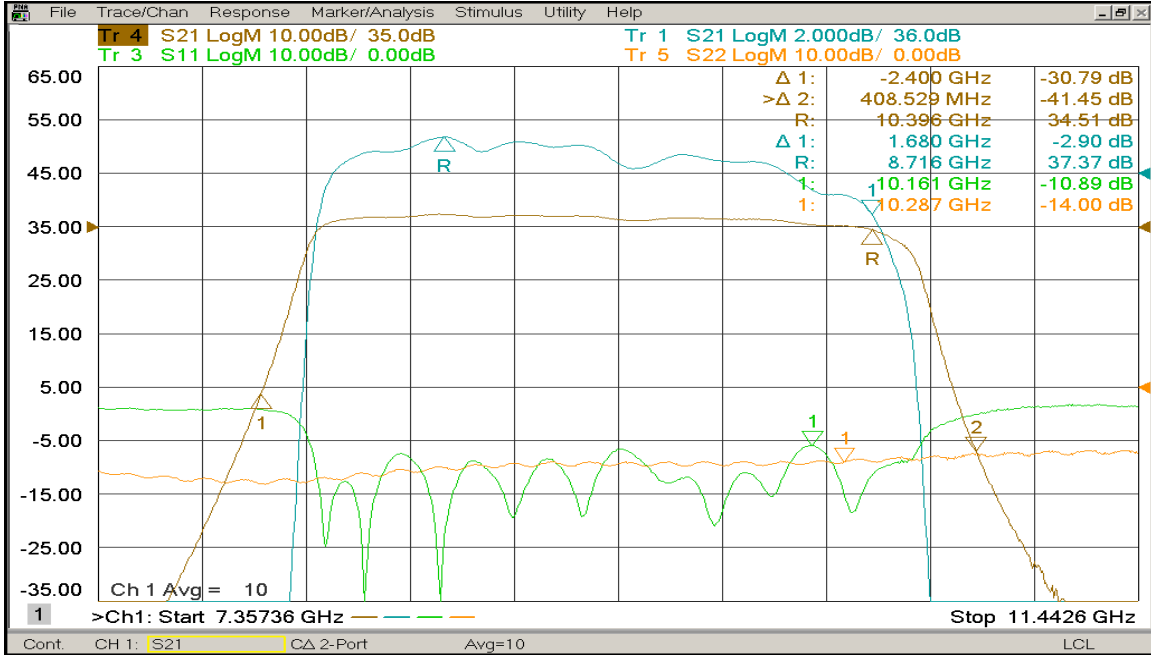




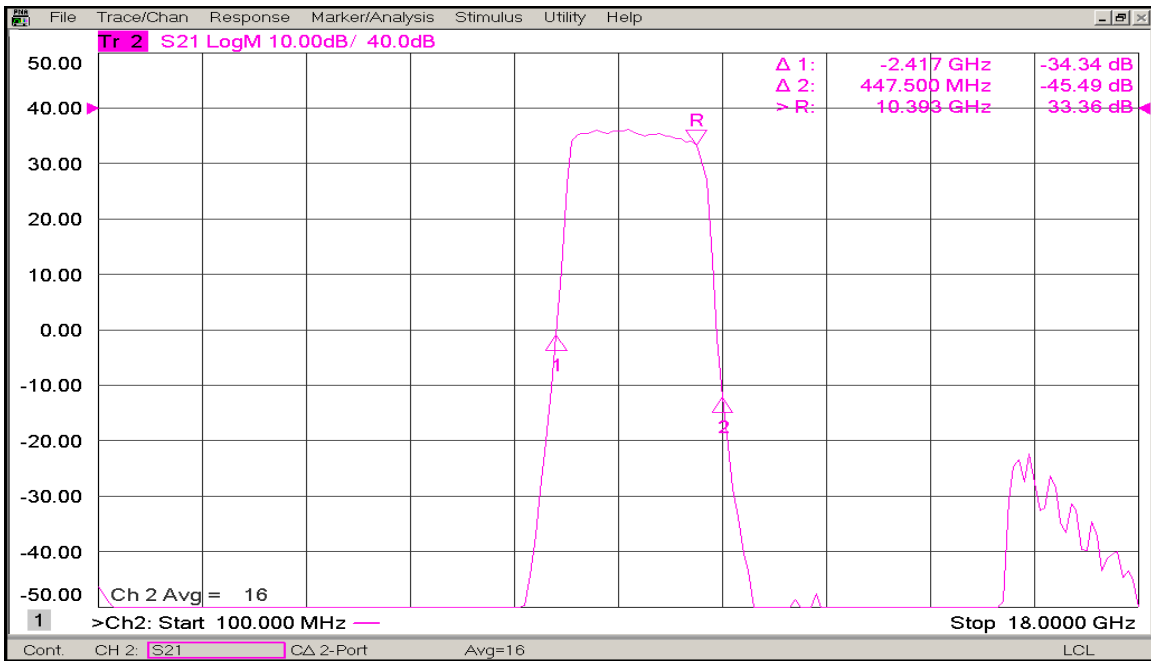
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**TX Ch4 Path Narrow Band**



**TX Ch4 Path Broadband**

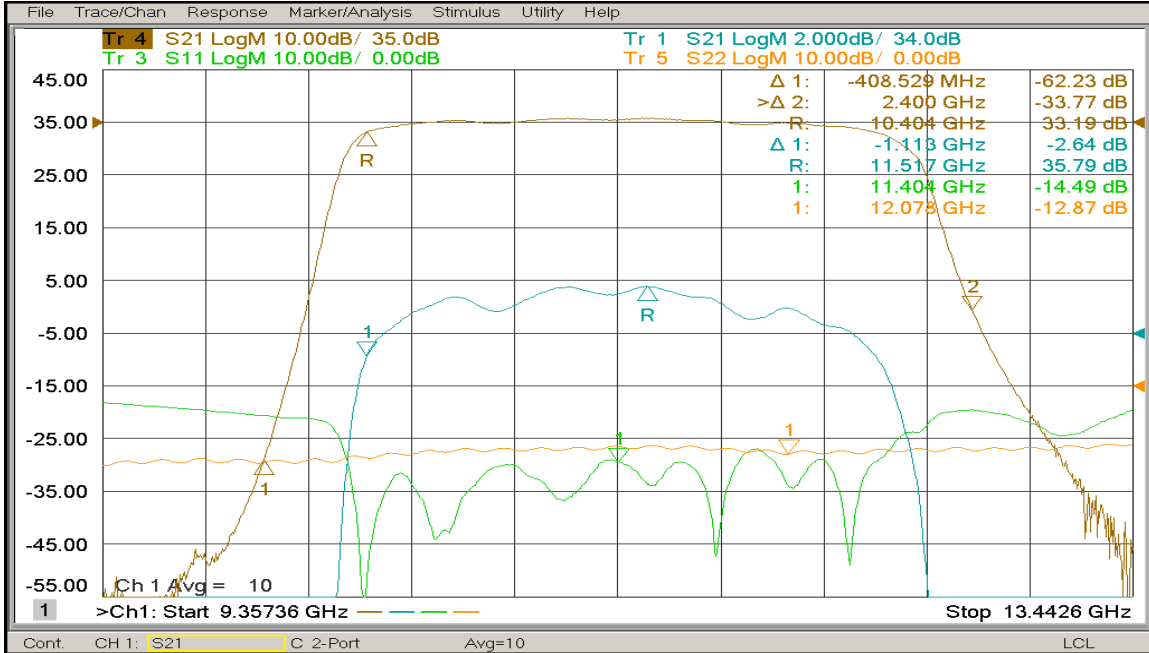




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**TX Ch5 Path Narrow Band**



**TX Ch5 Path Broadband**

