

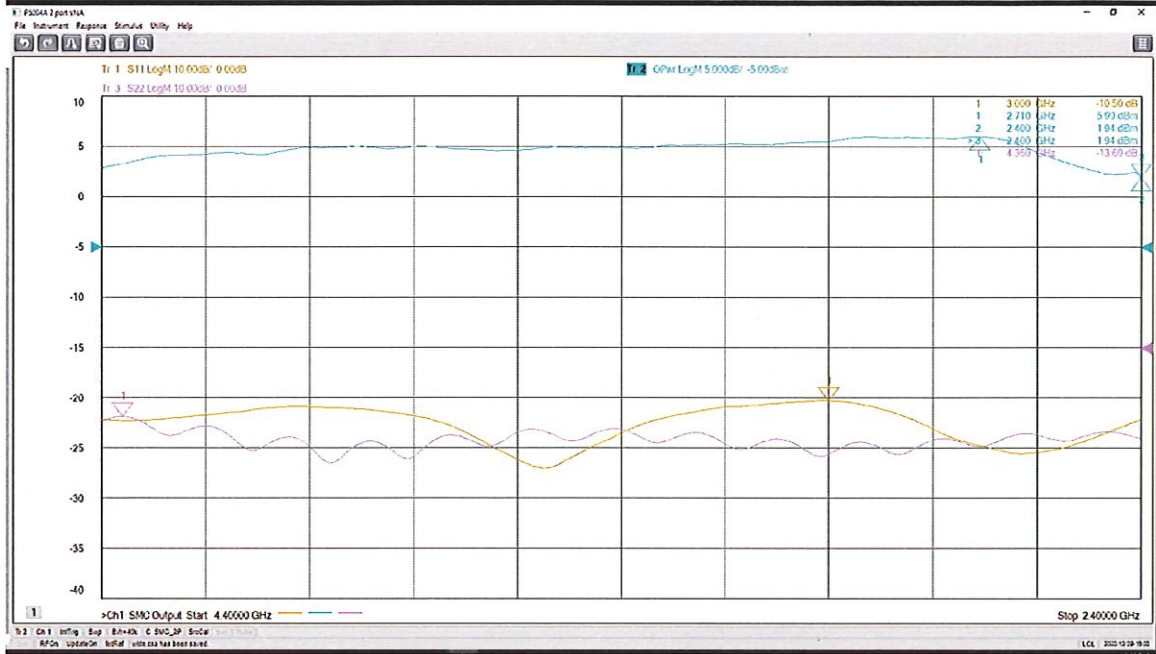


**SUMMARY TEST DATA  
ON  
PTRAN-100M18G-SDLVA-SFB-3UVPX-10HP-MAH-MX**

PL20394/1702

**IF INPUT to TRANSMIT OUTPUT PLOTS**

IF1 Input (J4A) To RF Transmit Output (J6) Channel 1 (-8dBm Input)



QA/QC: *[Handwritten Signature]*

Date: 1/3/2023

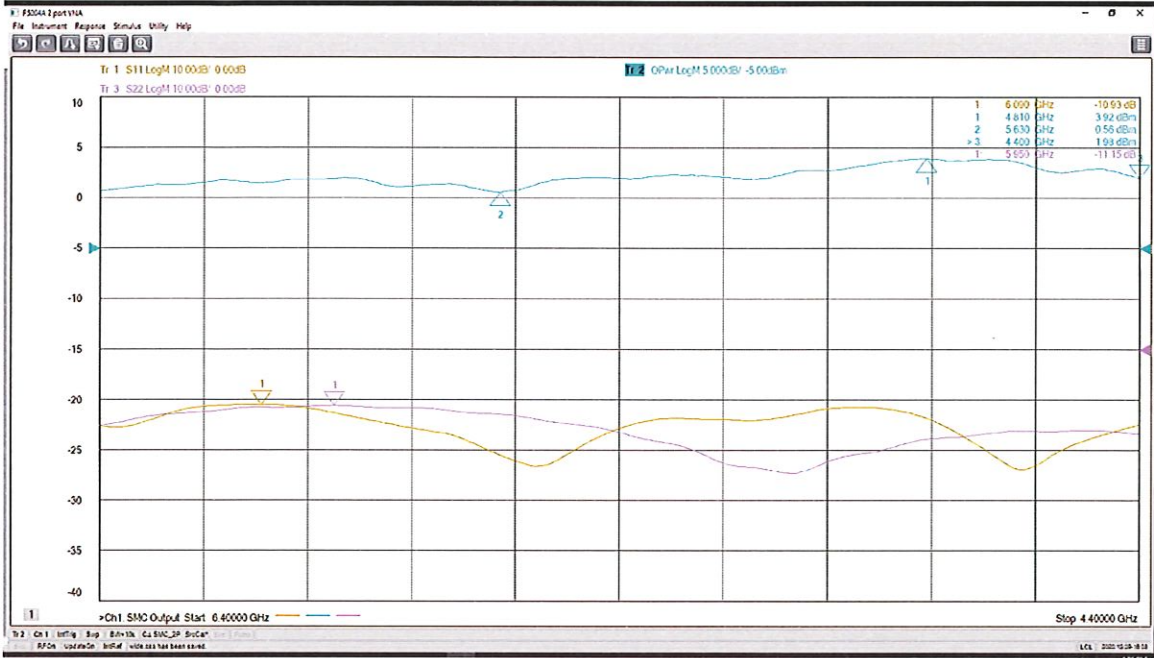


**SUMMARY TEST DATA  
ON  
PTRAN-100M18G-SDLVA-SFB-3UVPX-10HP-MAH-MX**

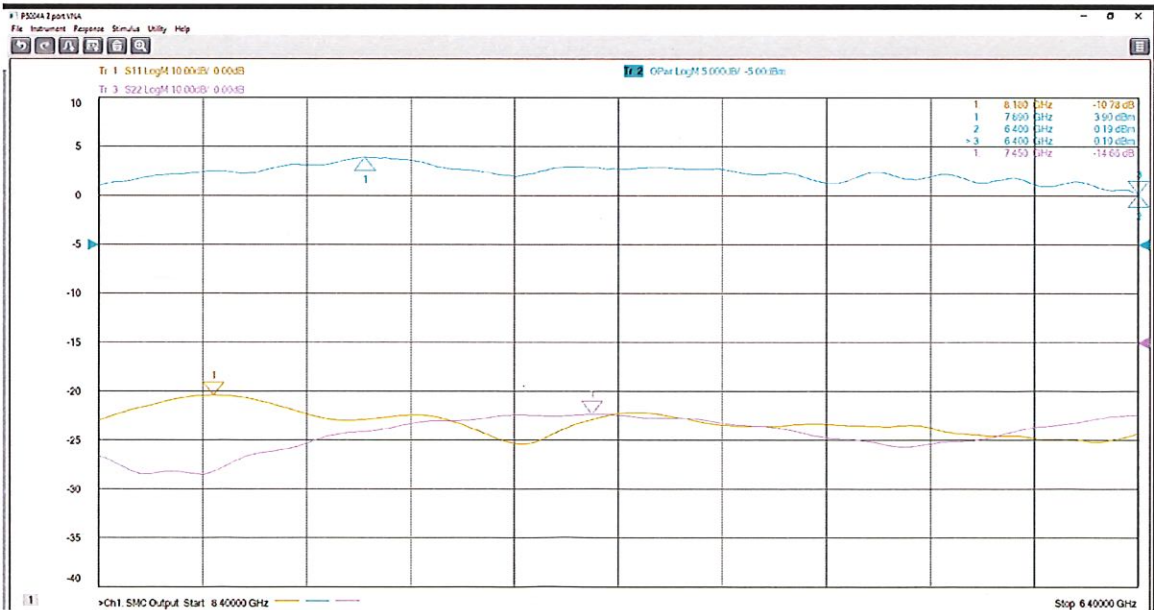
PL20394/1702

**IF INPUT to TRANSMIT OUTPUT PLOTS**

**IF1 Input (J4A) To RF Transmit Output (J6) Channel 2 (-8dBm Input)**



**IF1 Input (J4A) To RF Transmit Output (J6) Channel 3 (-8dBm Input)**



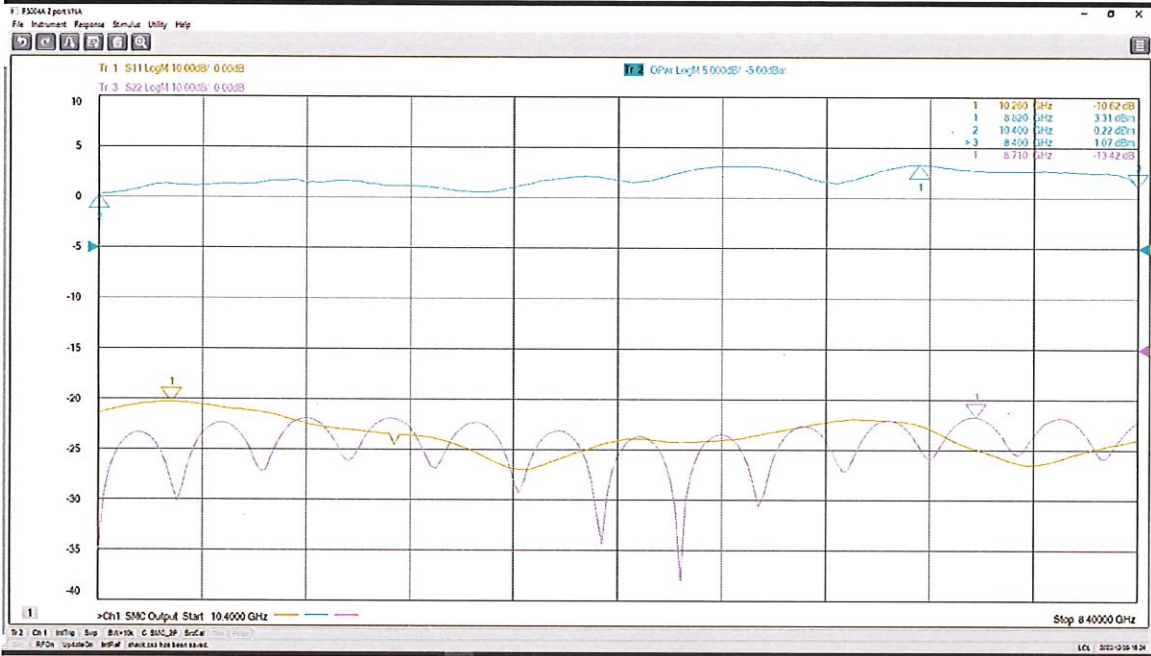


**SUMMARY TEST DATA  
ON  
PTRAN-100M18G-SDLVA-SFB-3UVPX-10HP-MAH-MX**

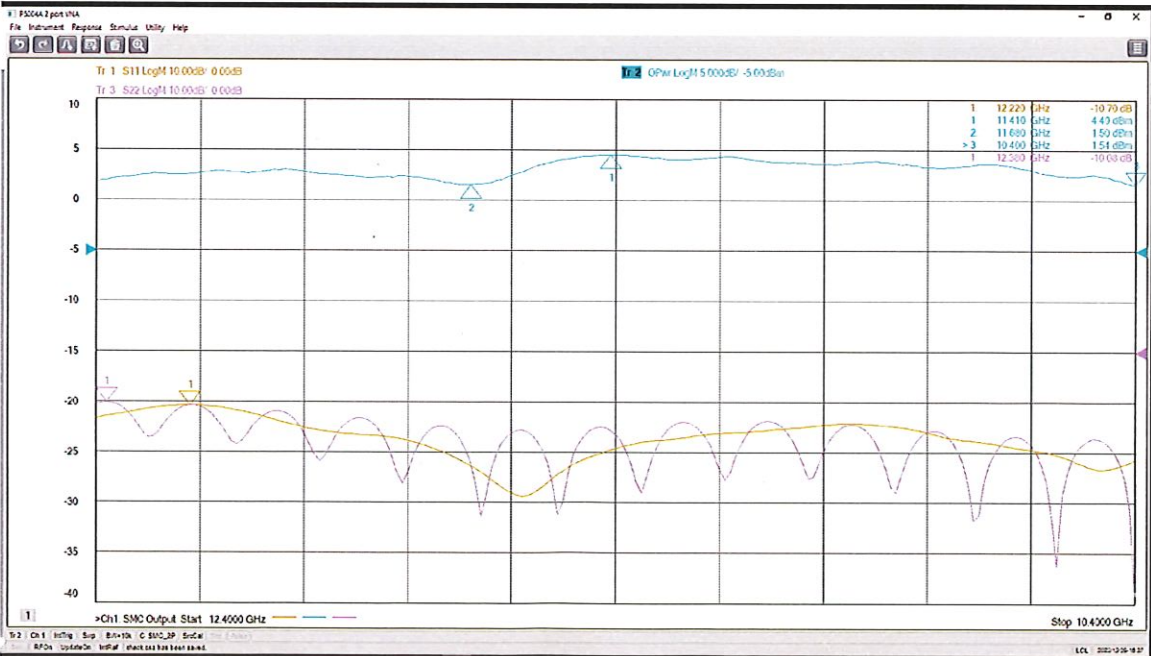
PL20394/1702

**IF INPUT to TRANSMIT OUTPUT PLOTS**

**IF1 Input (J4A) To RF Transmit Output (J6) Channel 4 (-8dBm Input)**



**IF1 Input (J4A) To RF Transmit Output (J6) Channel 5 (-8dBm Input)**



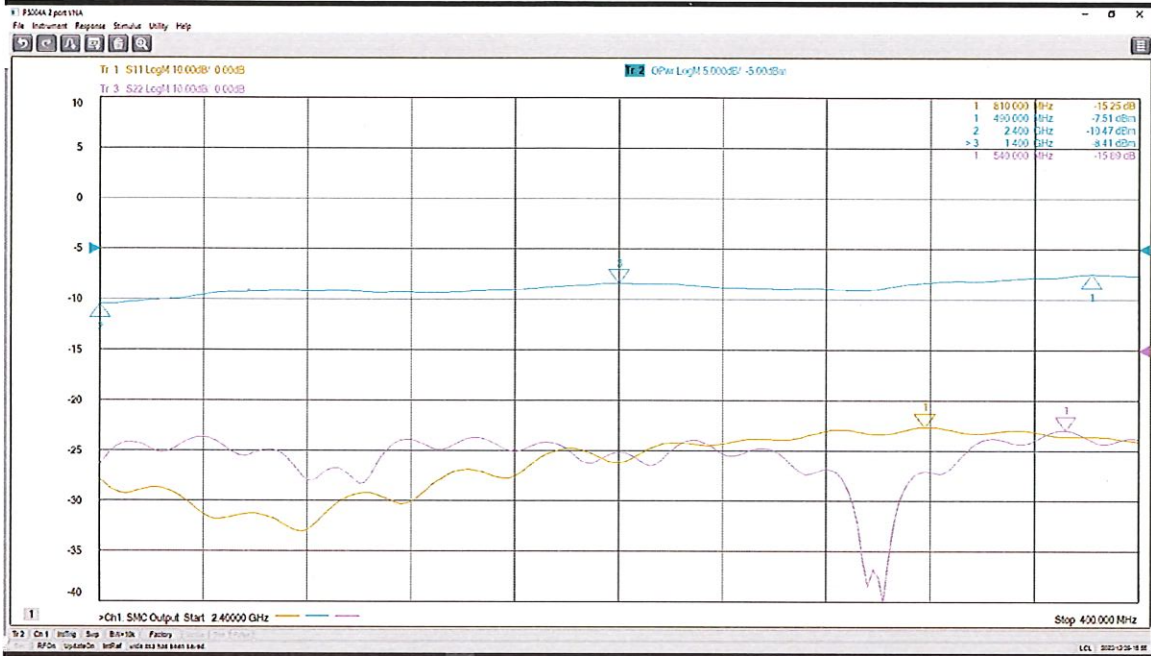


**SUMMARY TEST DATA  
ON  
PTRAN-100M18G-SDLVA-SFB-3UVPX-10HP-MAH-MX**

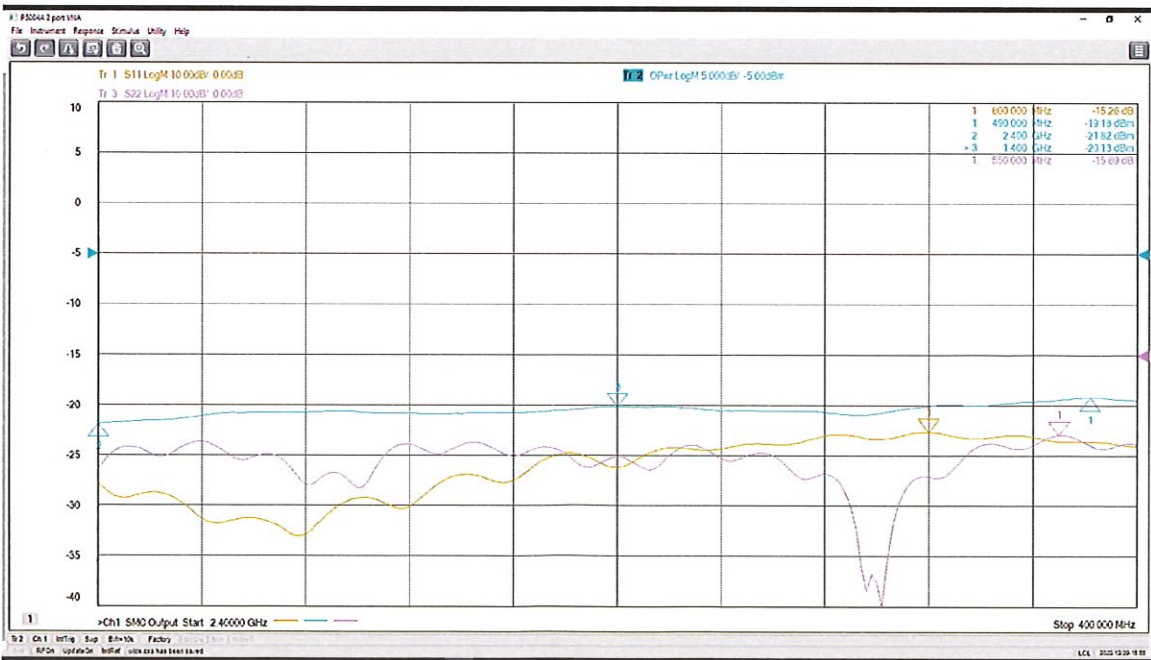
PL20394/1702

**RECEIVE INPUT to IF OUTPUT PLOTS – Channel 2**

RF Receive Input (J1) To IF1 Output (J3A) Channel 2 Limited (-10dBm Input)



RF Receive Input (J1) To IF1 Output (J3A) Channel 2 Linear (-10dBm Input)



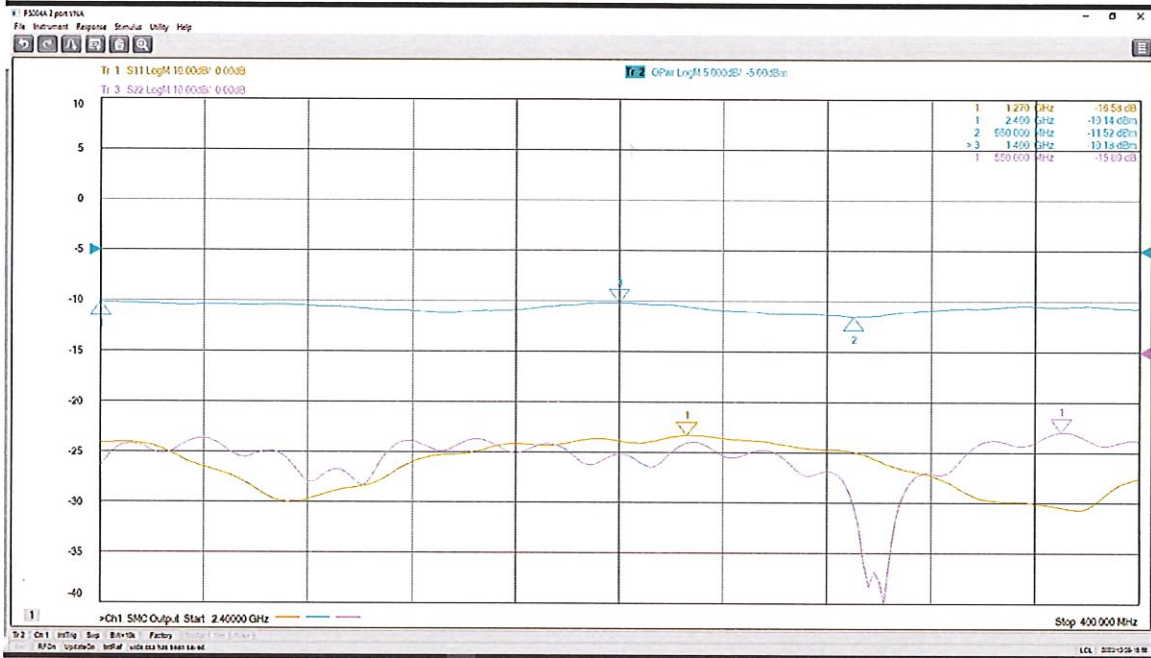


**SUMMARY TEST DATA  
ON  
PTRAN-100M18G-SDLVA-SFB-3UVPX-10HP-MAH-MX**

PL20394/1702

**RECEIVE INPUT to IF OUTPUT PLOTS – Channel 3**

**RF Receive Input (J1) To IF1 Output (J3A) Channel 3 Limited (-10dBm Input)**



**RF Receive Input (J1) To IF1 Output (J3A) Channel 3 Linear (-10dBm Input)**

