



**SUMMARY TEST DATA
ON
SDLVA-500M4G-CD-2**

PL20653/1710

Customer: _____ SO No: _____ Model No: <u>SDLVA-500M4G-CD-2</u> Serial No: <u>PL20653/1710</u>	Tested By: <u>J.Emperador</u> Temperature: <u>25°C</u> Date: <u>03/10/17</u> Drawing No: <u>27619941</u> Rev: <u>B2</u>
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TEST. ITEM NO	PARAMETERS	SPECIFIED VALUE	PASS FAIL	QA QC
1	Frequency Range:	500 MHz to 4.0 GHz	500 MHz to 4.0 GHz See Plot #1A	
2	RF Gain (Small Signal):	55dB Typ (J1 to J2)@-50dBm	52.9 dB	
3	Video Flatness over Frequency:	± 50mV Max	± 20.0 mV See Plot #1A	
4	TSS:	-73dBm Typ, -71dBm Max (-40°C, 25°C, +75°C)	-73.5 dBm -72 dBm See Plots #2A, 2B, 2C	
5	VSWR J1, J2 (50 Ohm):	2.0:1 Max	1.57:1, 1.82:1 See Plot #3	
6	PSAT:	+13dBm Typ (J2)	11.30 dBm See Plot #4	
7	Power Input:	+17dBm CW Max (J1)	Pass	
8	Log Slope:	25mV/dB Typ(J3) (50Ω Load) (-40°C, 25°C, +75°C)	24.3, 24.3, 26.3 mV/dB See Plots #1A, 1B, 1C	
9	Log Range:	-70 to 0 dBm	Pass	
10	Log Linearity	±2.5dB (J3) (-40°C, 25°C, +75°C)	-0.91, -0.90, 1.94dB See Plots #1A, 1B, 1C	
11	DC Offset:	50 ± 50mV(J3)	+50 mV	
12	Pulse Range:	30ns to CW	30 ns to CW	
13	Rise Time:	10ns Max (5ns Typ)	7.83 ns	

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14	Recovery Time:	60ns Max (40ns Typ)	53.89 ns	
15	Harmonics:	15dBc Min -70 to 0dBm	15.6 dBc	
16	DC Supply:	+15VDC @ 350 mA -15VDC @ 180 mA	+15VDC @ 178 mA -15VDC @ 94 mA	

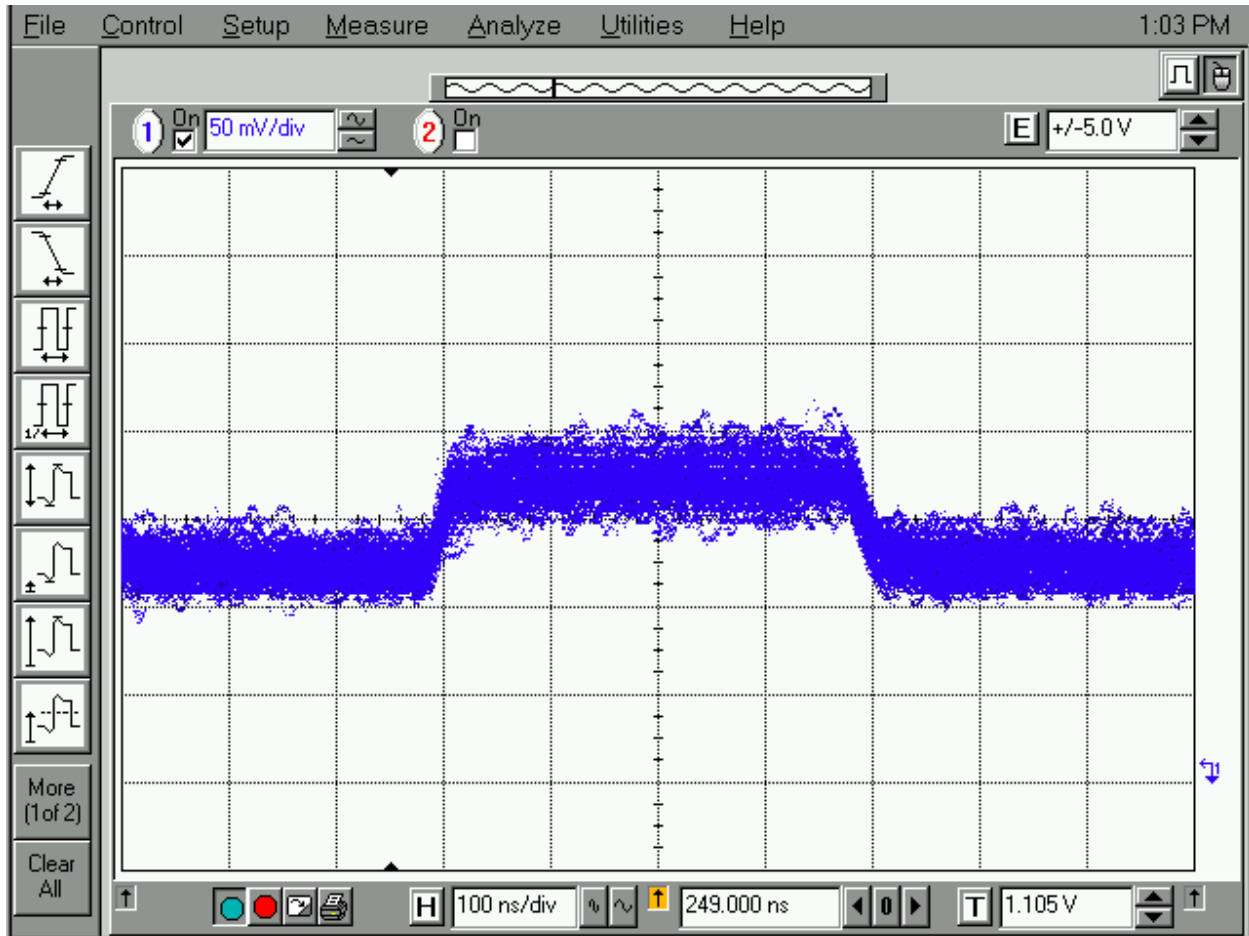
QA/QC Approval: _____ Date: _____



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TSS PLOT # 2A
-40C @ -73.5dBm

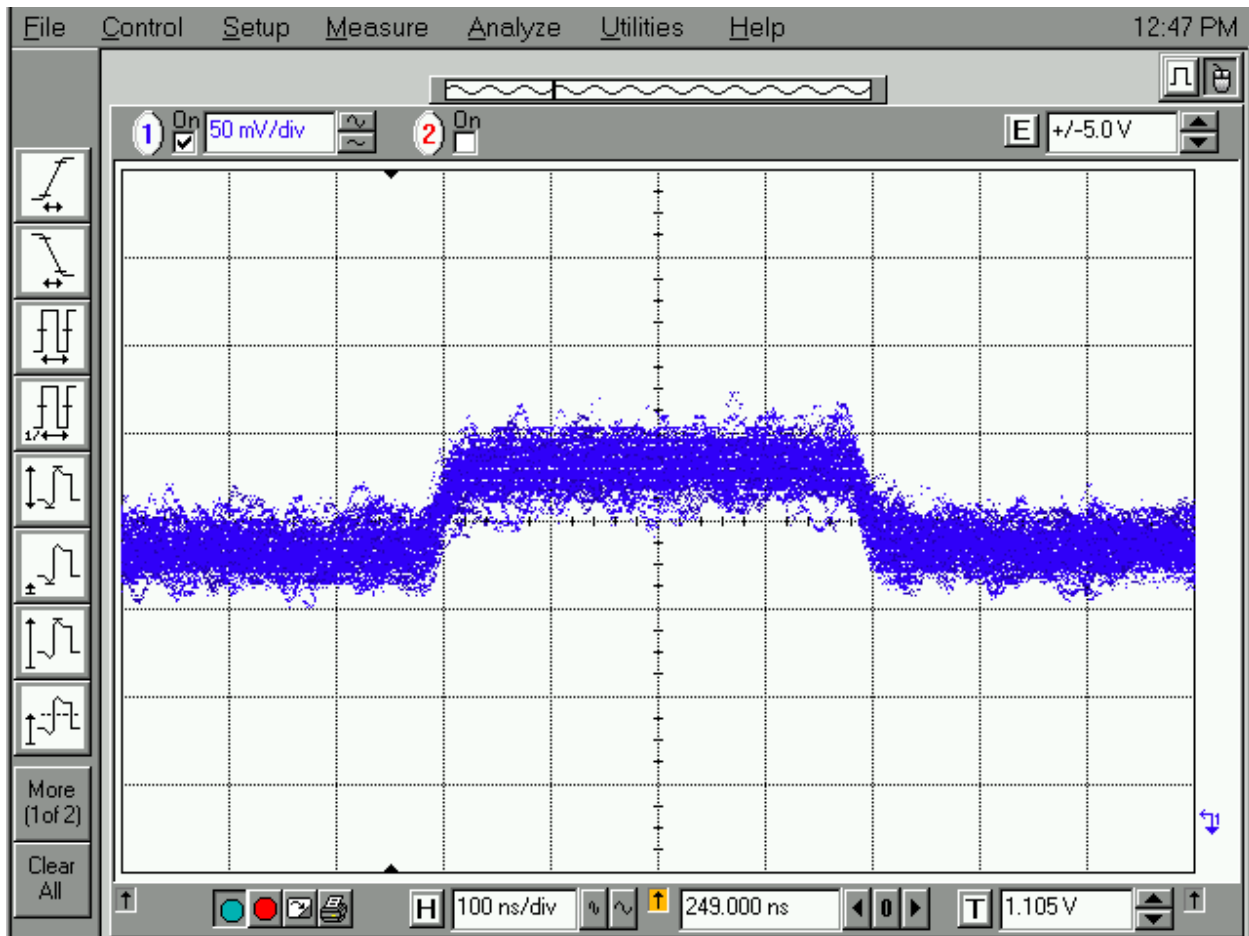




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TSS PLOT # 2B
+25C@-73dBm

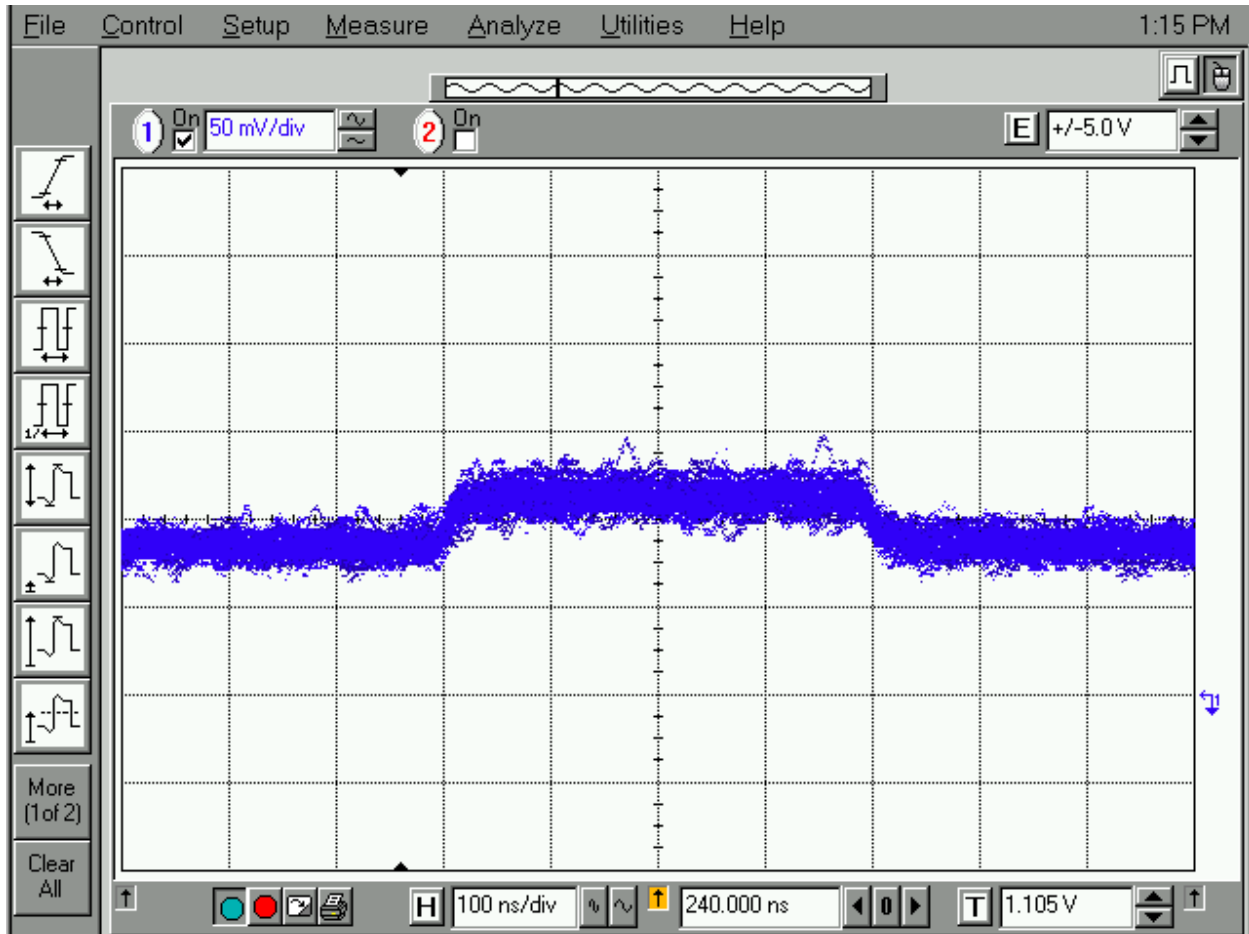




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TSS PLOT # 2C
+75C@-72dBm

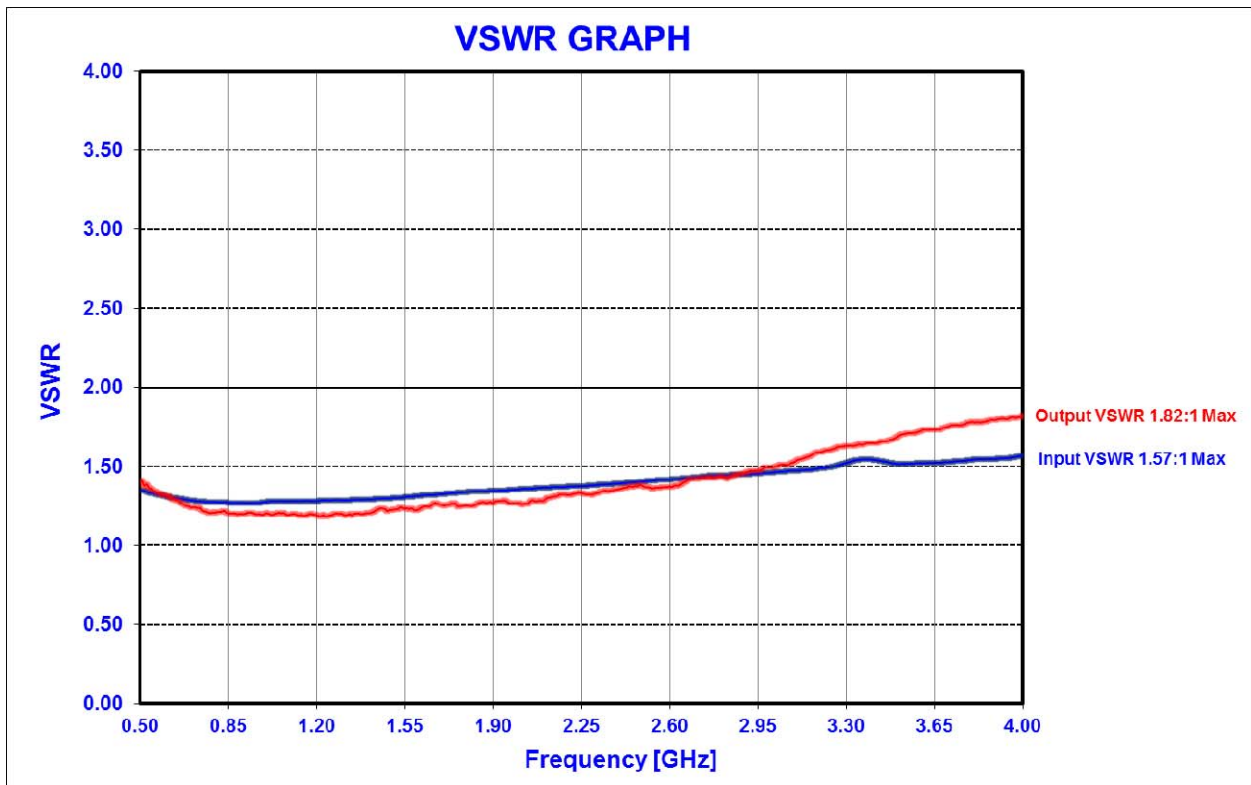




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VSWR PLOT # 3

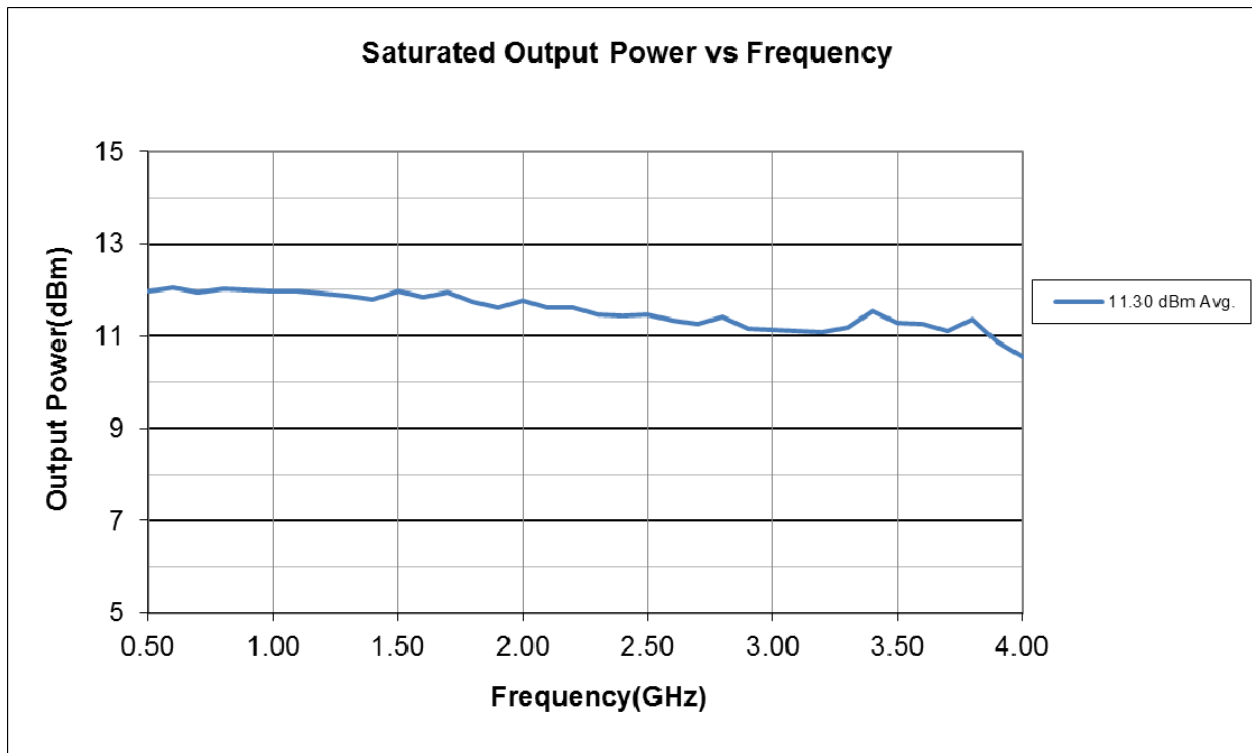




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PSAT PLOT # 4

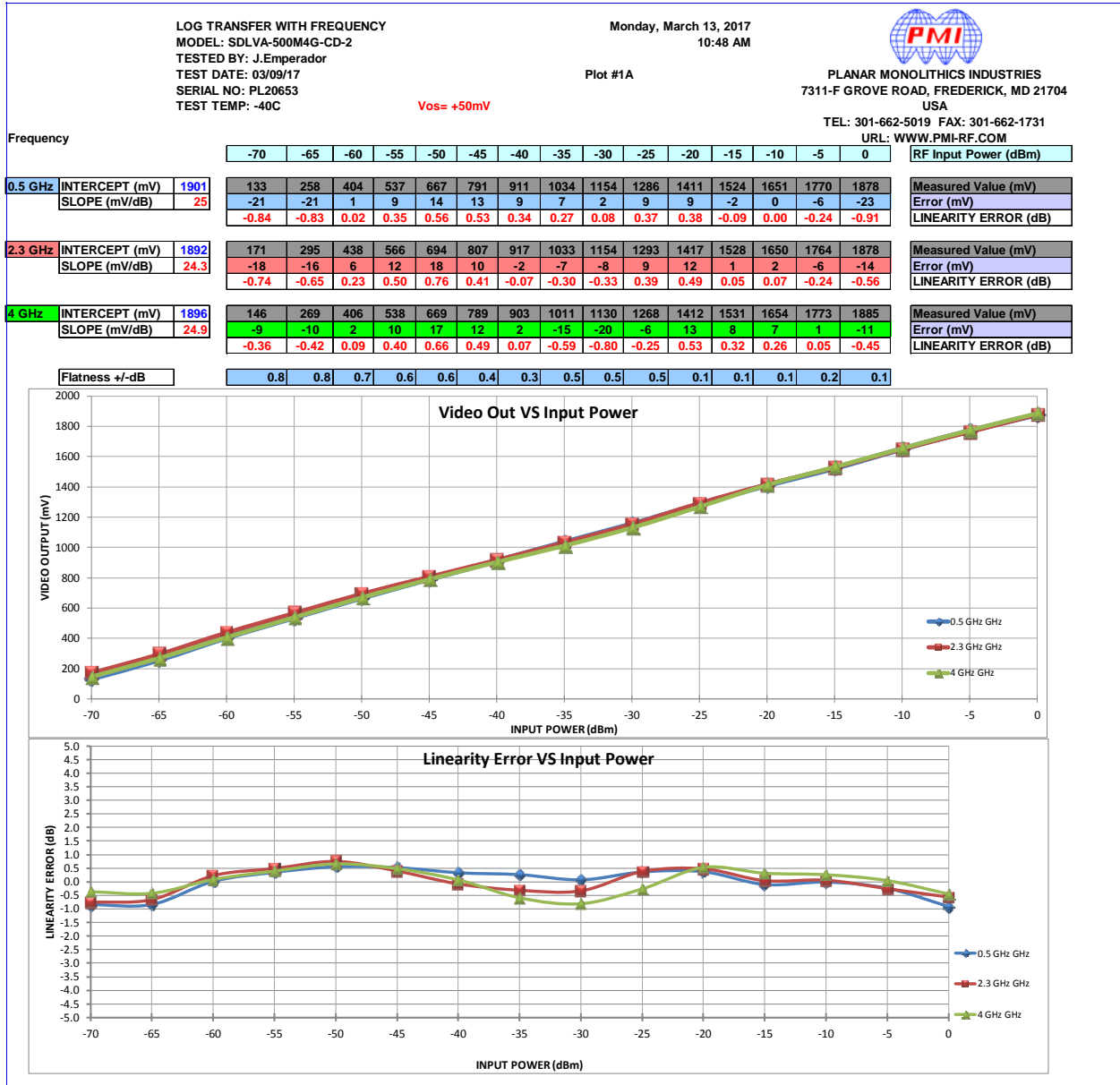




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PLOT # 1A

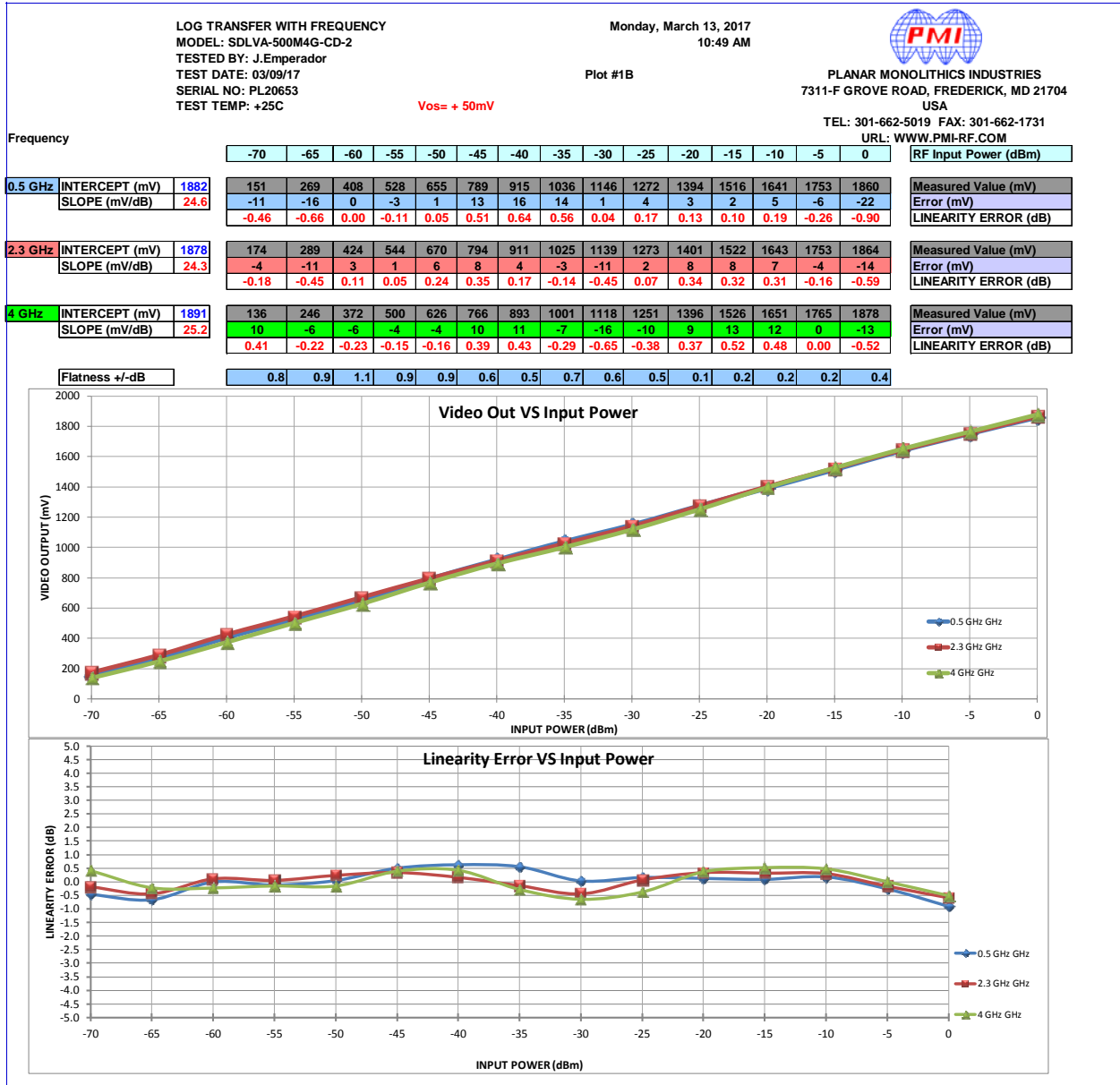




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PLOT # 1B





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PLOT # 1C

