



**SUMMARY TEST DATA
ON
TD-30T-SHS-218-12G18G-PECL**

PL21539/1735

Customer: _____ Tested By: E. Kretz
SO No: _____ Temperature: +25°C
Model No: TD-30T-SHS-218-12G18G-PECL Date: 08/30/17
Serial No: PL21539/1735 Drawing No: 27619243 Rev: A1

TEST. ITEM NO	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA QC
1	Frequency Range:	2 TO 18 GHz (Optimized 12 – 18 GHz)	2 TO 18 GHz (Optimized 12 – 18 GHz)	
2	VSWR:	3.0: 1 Max @ -20dBm	2.8:1 (See Plot)	
3	Minimum Signal Level for Logic 1:	-20 dBm @ 2.0 GHz -17 dBm @ 18.0 GHz	- 19 dBm @ 2.0 GHz - 17 dBm @ 18.0 GHz	
4	Threshold Variation over Frequency (Any 1 GHz Window)	± 0.5 dB	± 0.4dB	
5	Minimum Signal Level for Logic 0	-25 dBm	25 dBm	
6	Dynamic Range:	+10 dBm Max	10 dBm	
7	Propagation Delay from 50% of an input of -20 dBm	15 ns Max	See Typical Characteristics	
8	Propagation Delay from 50% of an input of +10 dBm	15 ns Max	See Typical Characteristics	
9	Output Logic:	LVPECL Compatible	LVPECL Compatible	
10	DC Supply:	+15VDC @ 100 mA Max -15VDC @ 100 mA Max	+15VDC @ 87 mA -15VDC @ 36 mA	

QA/QC Approval: _____ Date: _____



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VSWR PLOT

