



**SUMMARY TEST DATA
ON
PLO-9G-CD-1**

PL23999/1849

Customer:	_____	Tested By:	<u>Sarjit</u>
SO No:	_____	Temperature:	<u>+25°C</u>
Model No:	<u>PLO-9G-CD-1</u>	Date:	<u>11/30/18</u>
Serial No:	<u>PL23999/1849</u>	Drawing No:	<u>27622744</u> Rev: <u>A1</u>

TEST. ITEM NO	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA QC
1	Output Frequency (fo):	9.0GHz	9.0GHz	PMI QA 1
2	Output Power:	+21dBm Min +25dBm Max	+21.0 dBm (See Plot)	
3	Reference (fi):	100 ±0.01 MHz 0 ±3dBm	100 MHz	
4	VSWR:	< 1.5:1 (ALL PHASES)	1.4:1	
5	Spurious:	-60dBc Min	-93 dBc (See Plot)	
6	Harmonics:	-25dBc Min	-29 dBc (See Plot)	
7	Phase Noise:	20 log(fo/fi)+6dB Typ. @ 10KHz – 100KHz -100 dBc/Hz Min @ 100KHz -120 dBc/Hz Min @ 1MHz	-114 dBc @ 100KHz -140 dBc @ 1MHz (See Plots)	
8	Lock Alarm:	TTL "H" : Out Of Lock TTL "L" : Lock	Fail	
9	Primary Power:	+12 to +15.5 Vdc @ 500mA Max -12 to -15.5 Vdc @ 100mA Max	281 mA @ +12 to +15.5 Vdc 22 mA @ -12 to -15.5 Vdc	
10	Weight:	250g (10 oz) Max	3.5 oz	PMI QA 1

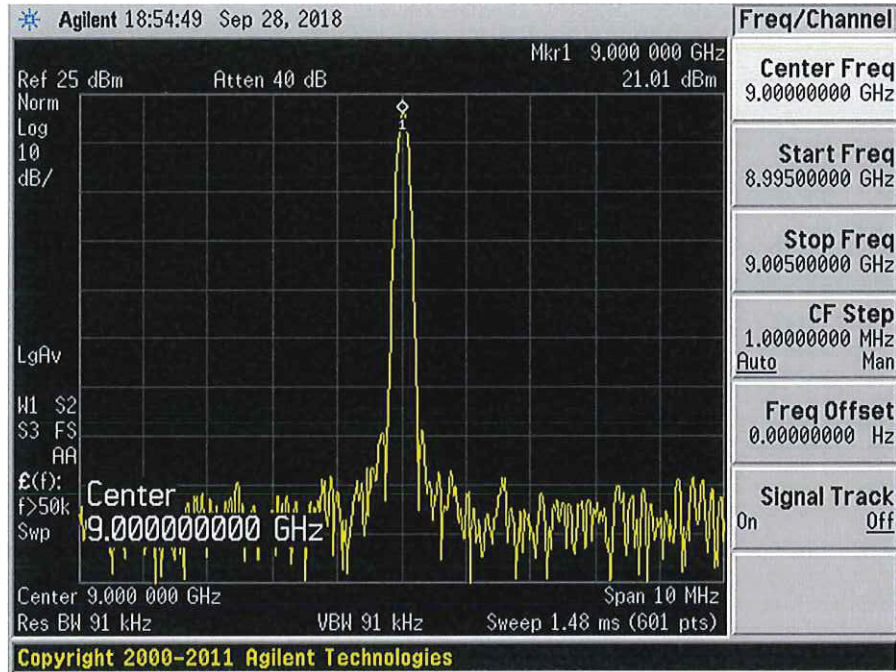
QA/QC Approval: PMI QA 1 Date: 12/6/18



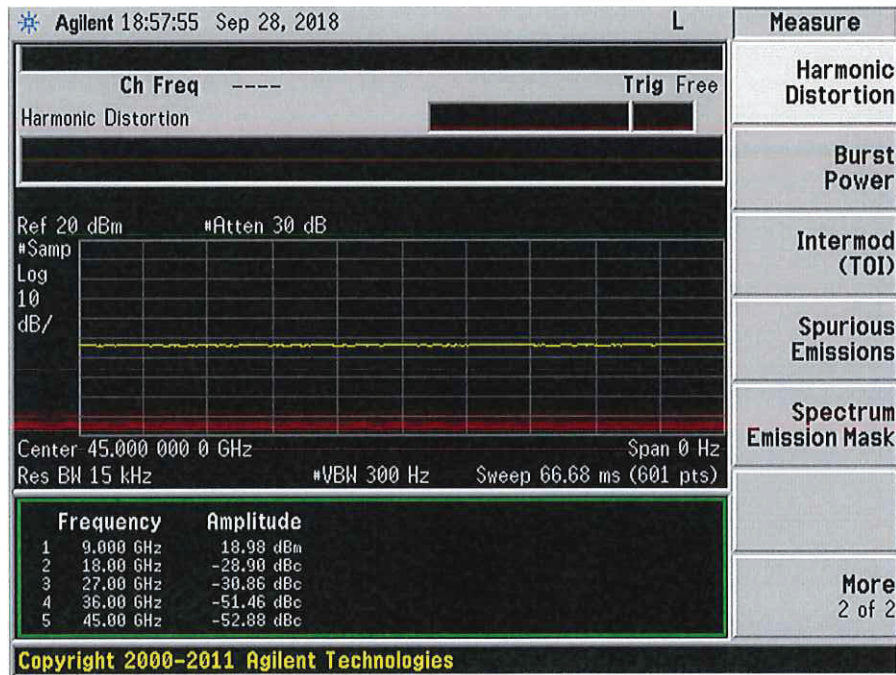
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Carrier



Harmonics

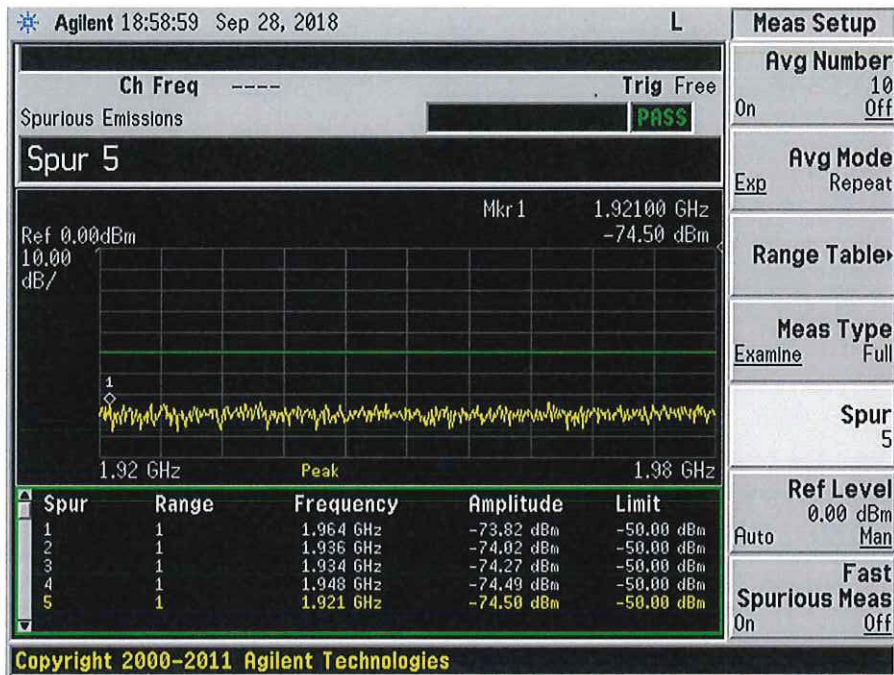




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Spurious



Phase Noise

