



**SUMMARY TEST DATA
ON
PMSN-3DR-05-STANDARD-OPT2G6G-LVTTL**

Customer: _____
 SO No: _____
 Model No: PMSN-3DR-05-STANDARD-OPT2G6G-LV1
 Serial No: PL29471/2027

Tested By: L. McGraw
 Temperature: +25°C
 Date: 6/30/2020
 Drawing No: 27634520 Rev: A1

TEST ITEM NO	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA QC	
1	Frequency Range:	2.0 GHz - 6.0 GHz	2.0 GHz - 6.0 GHz	PMI QA2	
2	Insertion Loss:	1.2 dB @ 2.0 - 4.0 GHz Max. 1.5 dB @ 4.0 - 6.0 GHz Max.	1.06 dB 2.0 - 4.0 GHz 1.31 dB 4.0 - 6.0 GHz See Plot		
3	Isolation:	60 dB Min.	93 dB See Plot		
4	VSWR: In/Out	1.75:1 Max.	In 1.64:1 Out 1.67 :1 See Plot		
5	Power Handling:	Reflective 1 Watt CW or Peak (Without Performance Degredation)	1 Watt See Typical Characteristics		
6	Control Input Impedance:	3.3/5.0 V CMOS Logic	Pass See Typical Characteristics		
7	Switching Speed:	Rise/Fall: 10 nsec Max. On Time: 25 nsec Max. Off Time: 20 nsec Max.	Rise/Fall: <10 nsec On Time: <25 nsec Off Time: <20 nsec See Typical Characteristics		
8	Survival Power:	1 W Average 75 Watts Peak (1 sec Max. Pulse Width)	1 Watt Average 75 Watts Peak See Typical Characteristics		
9	Power Supply:	+5 V ±5% @ 75 mA -12 V @ 30 mA	+5 V @ 55 mA -12 V @ 24 mA		
10	Control Signal:	"0" = On @ 0 V to 1.2 V "1" = Off @ 3.3 V to 5.5 V	"0" = On @ 0 V to 1.2 V "1" = Off @ 3.3 V to 5.5 V		PMI QA2

QA/QC Approval: PMI QA2

Date: 6.30.2020



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