## SUMMARY TEST DATA
**ON**
PLA-14D65G15G35G-20DB-SFF-250W

<table>
<thead>
<tr>
<th>TEST ITEM NO.</th>
<th>PARAMETERS</th>
<th>SPECIFIED VALUE</th>
<th>TEST DATA</th>
<th>QA QC</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Frequency Range:</td>
<td>14.65 TO 15.35 GHz</td>
<td>14.65 to 15.35 GHz</td>
<td>PMI</td>
</tr>
<tr>
<td>2</td>
<td>Insertion Loss:</td>
<td>3.59 dB Max&lt;sup&gt;1&lt;/sup&gt;</td>
<td>2.71 dB See Graph</td>
<td>QA 2</td>
</tr>
</tbody>
</table>
| 3            | Peak Power Handling            | Low Temp (-55 °C) 100 W Max<sup>2</sup>  
Room Temp (+25 °C) 125 W Max<sup>2</sup>  
High Temp (+85 °C) 100 W Max<sup>2</sup> | Pass See Typical Characteristics |       |
| 4            | Pulse Width                    | 40 µs Typ<sup>2</sup> | Pass               |       |
| 5            | Average Power                  | Low Temp (-55 °C) 10 W Max<sup>2</sup>  
Room Temp (+25 °C) 12.5 W Max<sup>2</sup>  
High Temp (+85 °C) 10 W Max<sup>2</sup> | Pass See Typical Characteristics |       |
| 6            | Attenuation:                   | Logic TTL "0" - 0 dB Attenuation  
Logic TTL "1" - 20dB Attenuation | Pass   |       |
| 7            | Attenuation Flatness:          | ± 1 dB Max       | 0.19 dB See Graph  |       |
| 8            | Attenuation Accuracy:          | ± 1 dB Max       | 0.1 dB See Graph   |       |
| 9            | P1dB Limiting Threshold:      | +5 dBm Min       | Pass See Typical Characteristics |       |
| 10           | Flat Leakage                   | (-55°C) +20 dBm Max @ 0 dB  
Attenuation And 100 W Max  
(+25°C) +20 dBm Max @ 0 dB  
Attenuation And 125 W Max  
(+85°C) +20 dBm Max @ 0 dB  
Attenuation And 100 W Max | Pass See Typical Characteristics |       |
| 11           | Switching Speed                | 90 ns, 50% TTL To 10% RF Max  
90 ns, 50% TTL To 90% RF Max | Pass See Typical Characteristics |       |
| 12           | Phase Matching                 | 15° Max (Unit To Unit) | 6.5° See Graph  |       |
| 13           | DC Consumption                 | +5 V @ 150 mA Max | +5 V @ 27 mA      |       |
| 14           | DC Consumption                 | -15 V @ 150 mA Max | -15 V @ 24 mA     |       |
| 15           | VSWR                           | 2.0:1 Max @ 10 dBm Input | 1.87:1 See Graph  | PMI   |

1 @ -10 dBm Input & 0 dB Attenuation  
2 @ 0 dB & 20 dB Attenuations with 10% Duty Cycle

QA/QC Approval: [Signature]  
Date: 9-3-2020
SUMMARY TEST DATA
ON
PLA-14D65G15G35G-20DB-SFF-250W

Attenuation Flatness -20dB

Loss (dB)

14.65 14.7 14.75 14.8 14.85 14.9 14.95 15 15.05 15.1 15.15 15.2 15.25 15.3 15.35
18 18.4 18.8 19.2 19.6 19.8 20 20.4 20.8 21.2 21.6 22

Frequency (GHz)

Phase Matching

Phase (deg)

14.65 14.7 14.73 14.8 14.85 14.9 14.95 15 15.05 15.1 15.15 15.2 15.25 15.3 15.35
20 18 16 14 12 10 8 6 4 2 0 -2 -4 -6 -8 -10 -12 -14 -16 -18 -20

Frequency (GHz)