



**SUMMARY TEST DATA
ON
PEC-40/25-218-21-12-SFF-TTLVG**

PL31722/2111

12	VSWR In/Out:	2.0:1 Max	+1.7:1 In +1.8:1 Out See Plot	PMI QA3	
13	Input/Output Impedance:	50 Ω Nominal	50 Ω Pass		
14	Input Power: (Without Damage)	+20 dBm CW Max.	+20 dBm Pass		
15	In-Band Harmonics: @ or below the 1 dB Compression Point	-10 dBc Min.	>-10 dBc		
16	Spurious Output Signal @ Any Signal Level Up To The Max Input Level:	-80 dBc Max.	<-80 dBc		
17	Pulse Rise Time: with Input Signals up to 20 dBm	<5 ns	<5 ns		
18	Pulse Overshoot: with Input Signals up to 20 dBm	<0.5 dB	<0.5 dB		
19	Pulse Droop: with Pulses up to 250 us in Duration Input Signals up to -20 dBm	<2.0 dB	<2.0 dB		
20	Pulse Recovery Time: with Pulses up to 250 us in Duration Input Signals up to -20 dBm	15 ns	<15 ns		
21	Gain Switching Time:	<500 ns	<500 ns		
22	Gain Switch Control:	TTL High "1" - Max. Gain TTL Low "0" - Min. Gain	Pass		
23	DC Supply:	780 mA Max. @ +12 V \pm 5% Max. Gain Position 610 mA Max. @ +12 V \pm 5% Min. Gain Position	400 mA Max. Gain Position 400 mA Min. Gain Position		PMI QA3

QA/QC Approval: Arthur Zimmerman

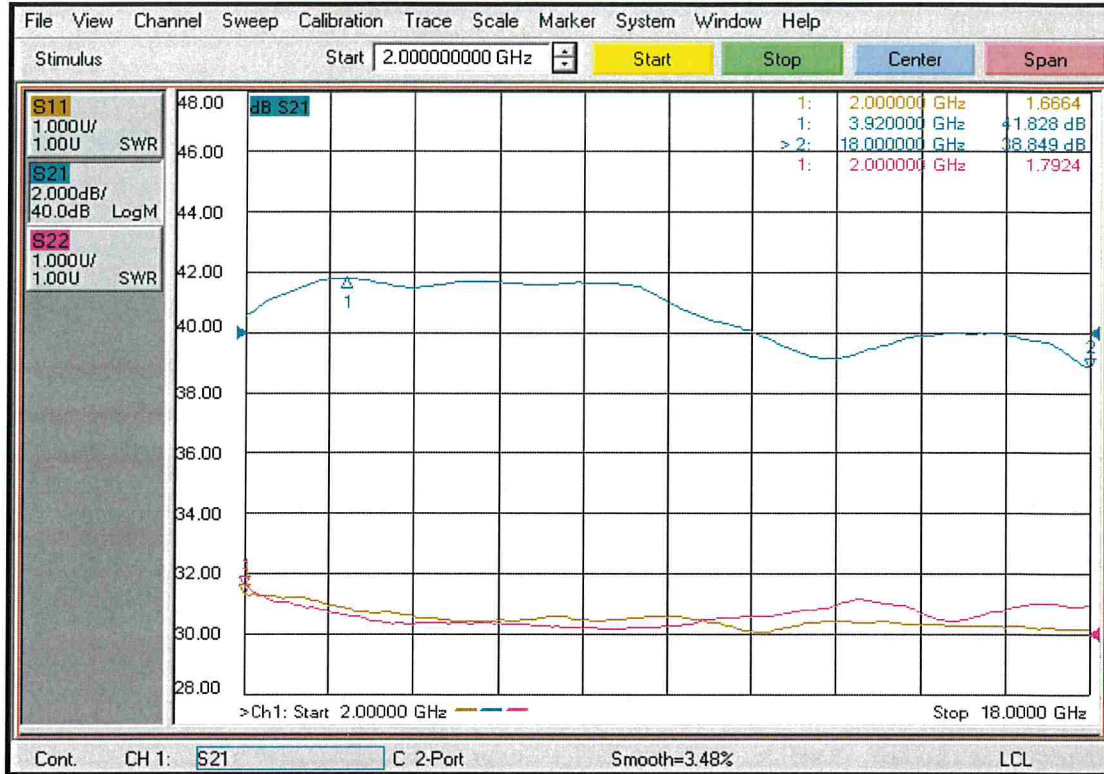
Date: 3-24-21



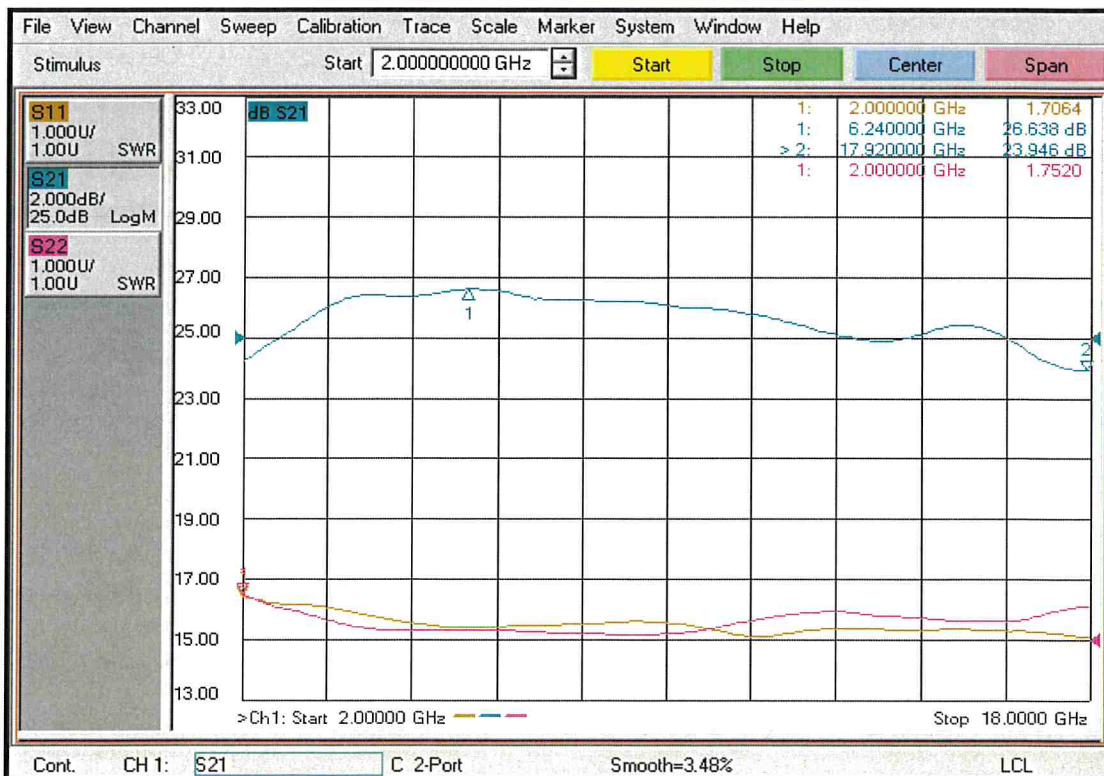
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High Gain Mode Gain and VSWR Plot at +25°C (TTL = 1)



Low Gain Mode Gain and VSWR Plot at +25°C (TTL = 0)

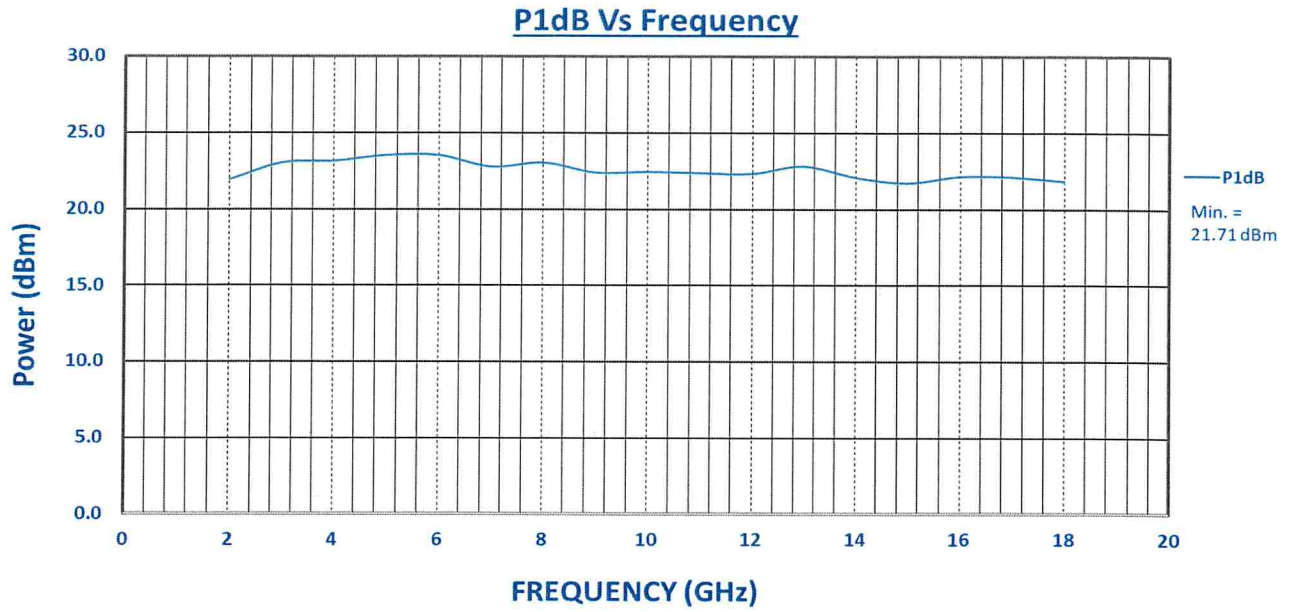




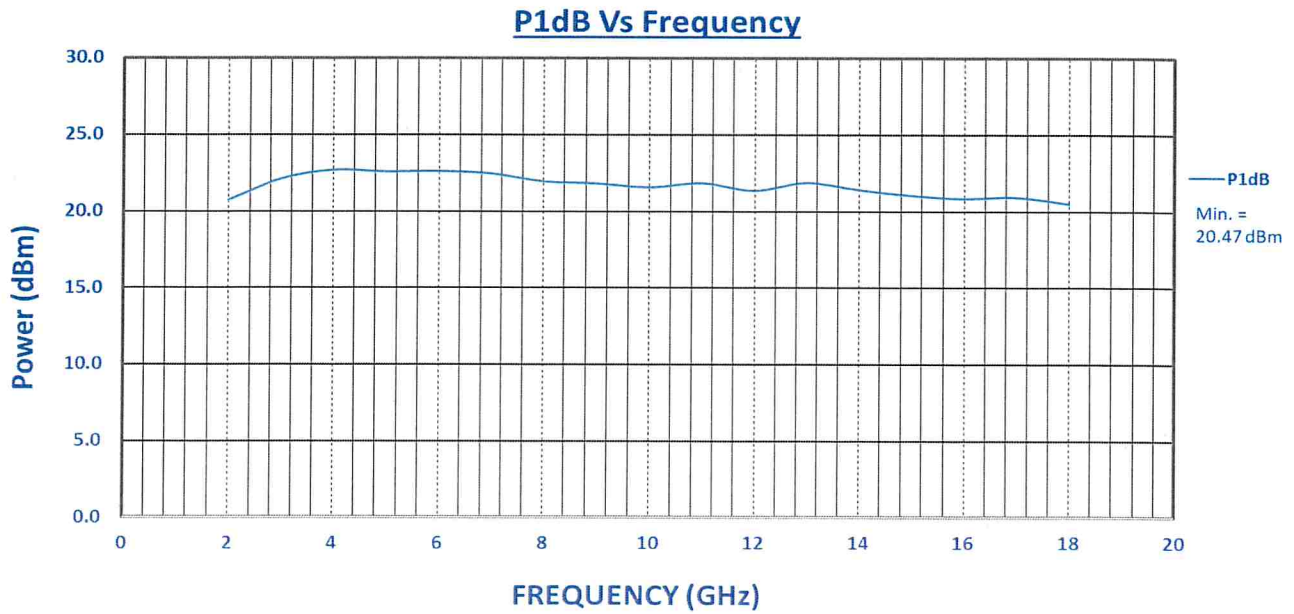
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High Gain Mode P1dB Plot at +25°C (TTL = 1)



Low Gain Mode P1dB Plot at +25°C (TTL = 0)





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Noise Figure Plot

