



SUMMARY TEST DATA ON P4T-50M40G-55-T-292FF

Customer: _____
 SO No: _____
 Model No: P4T-50M40G-55-T-292FF
 Serial No: PL32238/2118

Tested By: A. Lopez
 Temperature: 25°C
 Date: 7/9/2021
 Drawing No: 27618988 Rev: A1

TEST ITEM NO.	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA
			+25°C	QC
1	Frequency Range:	50MHz to 40GHz	50MHz to 40GHz	PMI QA 2
2	Insertion Loss:	8dB Max	5.47 dB See Graph	
3	Isolation:	55dB Min	65.96 dB See Graph	
4	VSWR:	2.5:1 Max	2.3 :1 See Graph	
5	Speed:	50% TTL to 10%/90% RF 85ns Typ, 100ns Max	Pass See Typical Characteristics	
6	Input Power:	+20dBm CW	Pass See Typical Characteristics	
7	Survival Power:	0.5W CW, 5W Peak, 1us	Pass See Typical Characteristics	
8	Control TTL	4 Line Independent Logic "0" = Insertion Loss Logic "1" = Isolation	Pass	
9	Power Supply:	+12 to +15VDC @ 150mA * -12 to -15VDC @ 100mA *	+12 to +15VDC @ 115mA -12 to -15VDC @ 70mA	PMI QA 2

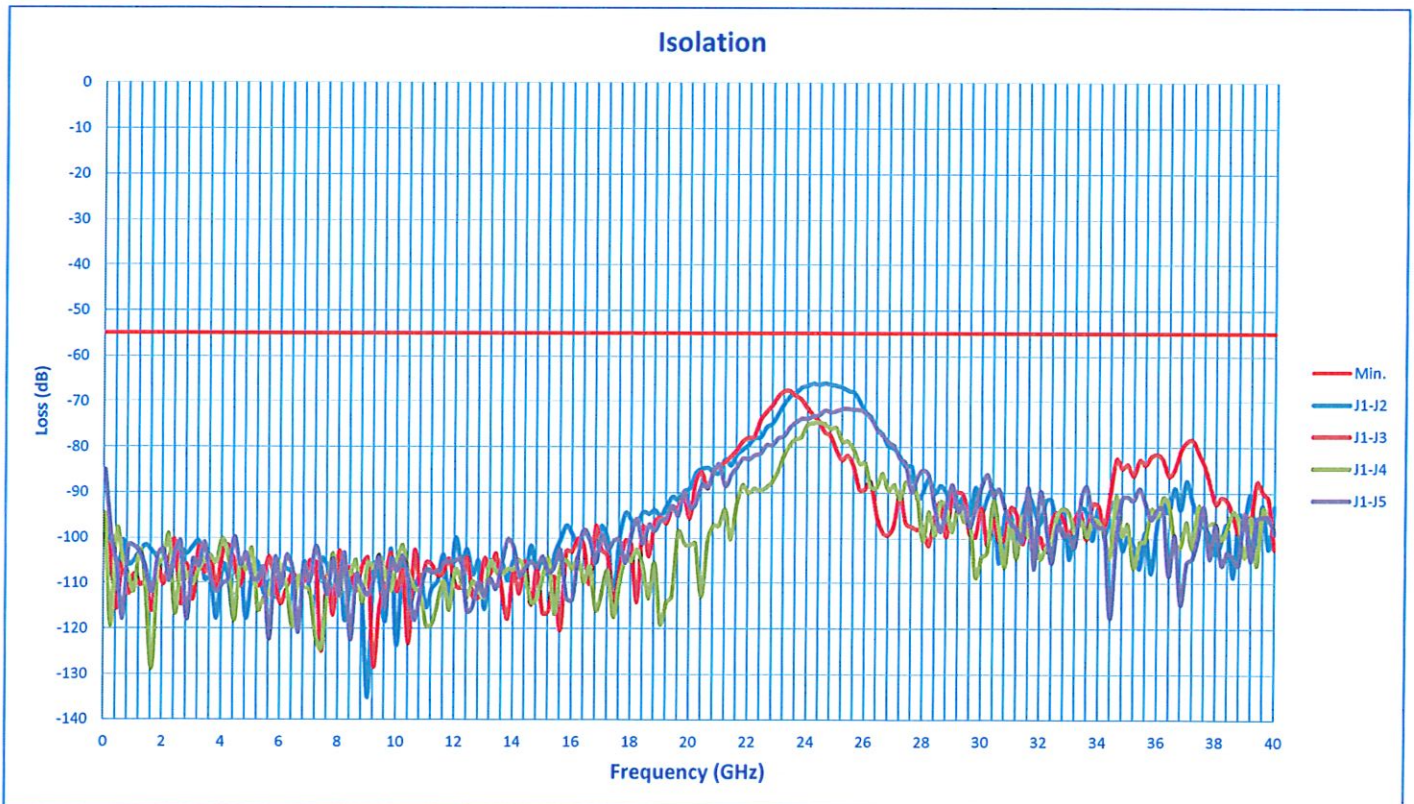
*Voltage Regulation & Reverse Bias Protection Included

QA/QC Approval:  PMI
QA 2

Date: 7/9/2021



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