

**SUMMARY TEST DATA
ON
PMSN-3DR-05-STANDARD-OPT2G6G-LVTTL**

Customer: _____
 SO No: _____
 Model No: PMSN-3DR-05-STANDARD-OPT2G6G-LVTTL
 Serial No: PL37651/2235

Tested By: K. Mansfield
 Temperature: +25°C
 Date: 8/29/2022
 Drawing No: 27634520 Rev: A1

TEST ITEM NO.	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA QC
1	Frequency Range:	2.0 GHz - 6.0 GHz	2.0 GHz - 6.0 GHz	PMI QA 2
2	Insertion Loss:	1.2 dB @ 2.0 - 4.0 GHz Max. 1.5 dB @ 4.0 - 6.0 GHz Max.	1.2 dB 2.0 - 4.0 GHz 1.24 dB 4.0 - 6.0 GHz See Plot	
3	Isolation:	60 dB Min.	92 dB See Plot	
4	VSWR: In/Out	1.75:1 Max.	In 1.67:1 Out 1.66 :1 See Plot	
5	Power Handling:	Reflective 1 Watt CW or Peak (Without Performance Degredation)	See Typical Characteristics	
6	Control Input Impedance:	3.3/5.0 V CMOS Logic	Pass	
7	Switching Speed:	Rise/Fall: 10 nsec Max. On Time: 25 nsec Max. Off Time: 20 nsec Max.	Rise/Fall: <10 nsec On Time: <25 nsec Off Time: <20 nsec See Typical Characteristics	
8	Survival Power:	1 W Average 75 Watts Peak (1 sec Max. Pulse Width)	1 Watt Average 75 Watts Peak See Typical Characteristics	
9	Power Supply:	+5 V ±5% @ 75 mA -12 V @ 30 mA Max.	+5 V @ 66 mA -12 V @ 28 mA	
10	Control Logic:	"0" = On @ 0 V to 1.2 V "1" = Off @ 3.3 V to 5.5 V	"0" = On @ 0 V to 1.2 V "1" = Off @ 3.3 V to 5.5 V	PMI QA 2

QA/QC Approval:  PMI QA 2

Date: 8/30/2022

