



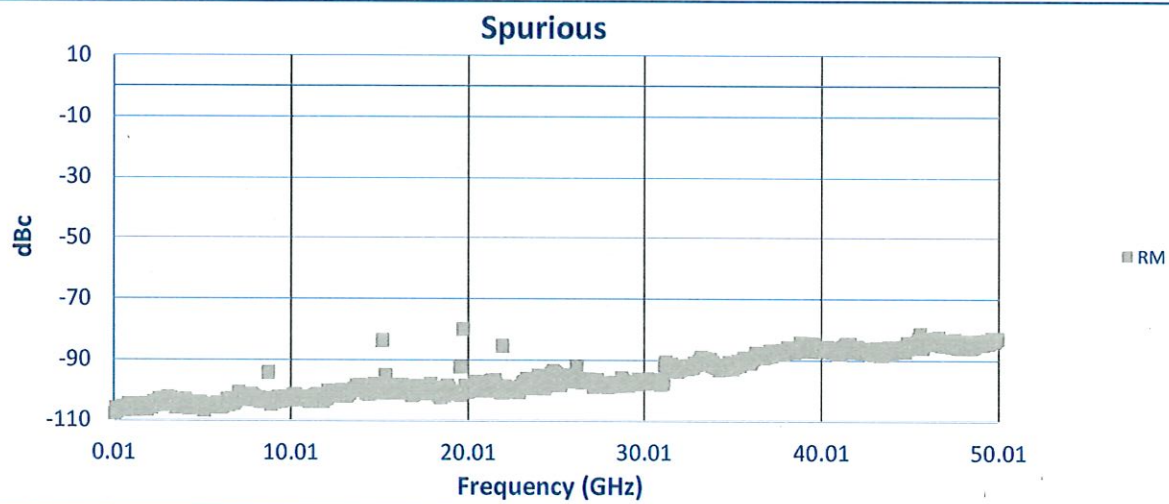
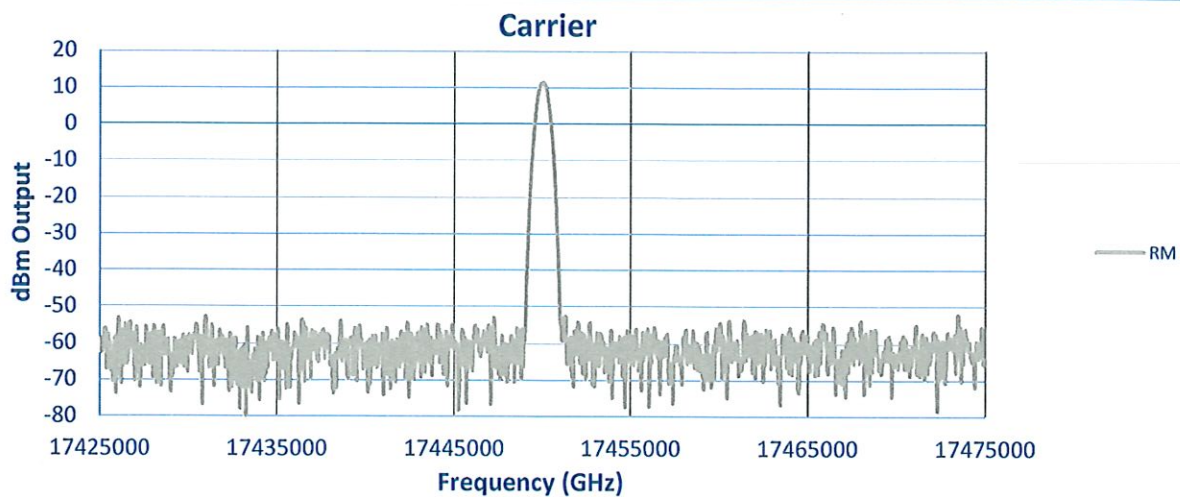
**SUMMARY TEST DATA
ON
PLDRO-17D45G-25M-EXT-ROHS**

Customer: _____
 SO No: _____
 Model No: **PLDRO-17D45G-25M-EXT-ROHS**
 Serial No: **PL41292/2327**

Tested By: **K Craven**
 Temperature: **25°C**
 Date: **7/3/2023**
 Drawing No: **27646020** Rev: **A3**

TEST ITEM	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC
1	Output Frequency	17.45 GHz	17.45 GHz	PMI QA2
2	Reference Frequency	25 MHz External	25 MHz External	
3	Output Power	13 to 15 dBm	13.9 dBm	
4	No Lock Break VSWR	3:1 Min	Pass See Typical Characteristics	
5	Output Spurious (10 Mhz to 50 GHz)	-75 dBc Max	-80.1 dBc	
6	Output Harmonics	-25 dBc Max	Pass See Typical Characteristics	
7	Sub Harmonics	-60 dBc Max (0.5*n Fo)	Pass See Typical Characteristics	
8	Output Ssb Phase Noise	See Outline	Pass See Typical Charactersitics	
9	Reference Input Power	10 to -3 dBm	10 dBm	
11	Reference Input Spurious	-80 dBc Max	Pass By Test Setup	
12	Reference Phase Noise	See Outline	Pass By Test Setup	
13	DC Supply	12 +- 0.5 V, 500 mA	12V @ 360 mA	
14	Lock Detector	TTL HIGH In Lock, TTL Low Out Lock	Pass	
15	Phase Voltage Monitor Output	0 to 12 V	0 to 12 V	
16	Cold Start Temperature	-45 Degrees C Max	Pass By Design	PMI QA2

QA/QC Approval: PMI QA2 Date: 7/13/2023



25°C Phase noise curve

