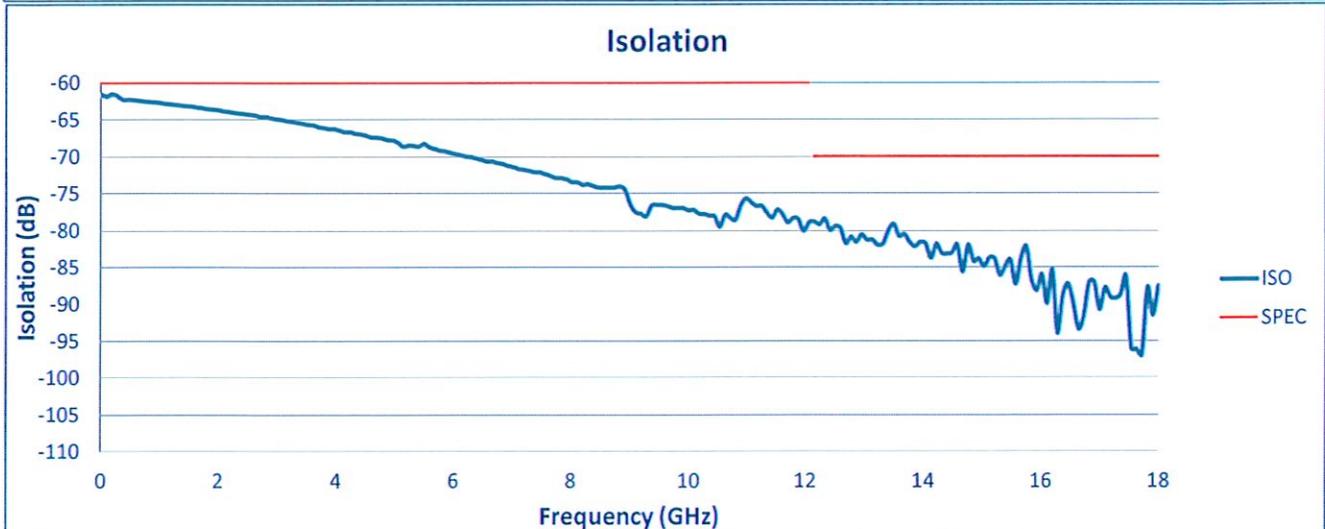
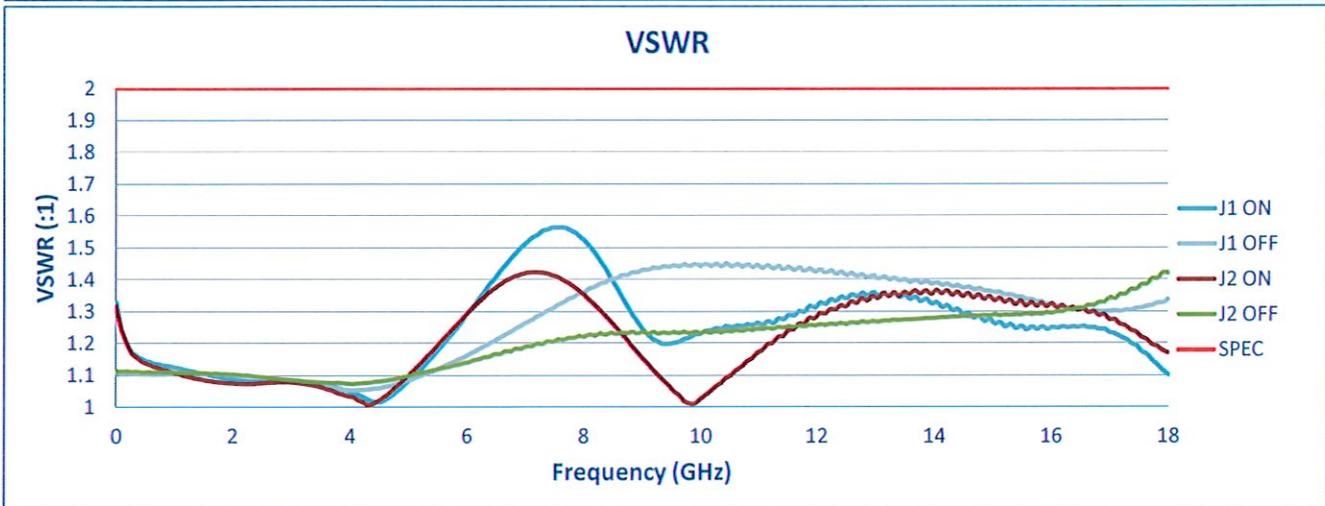
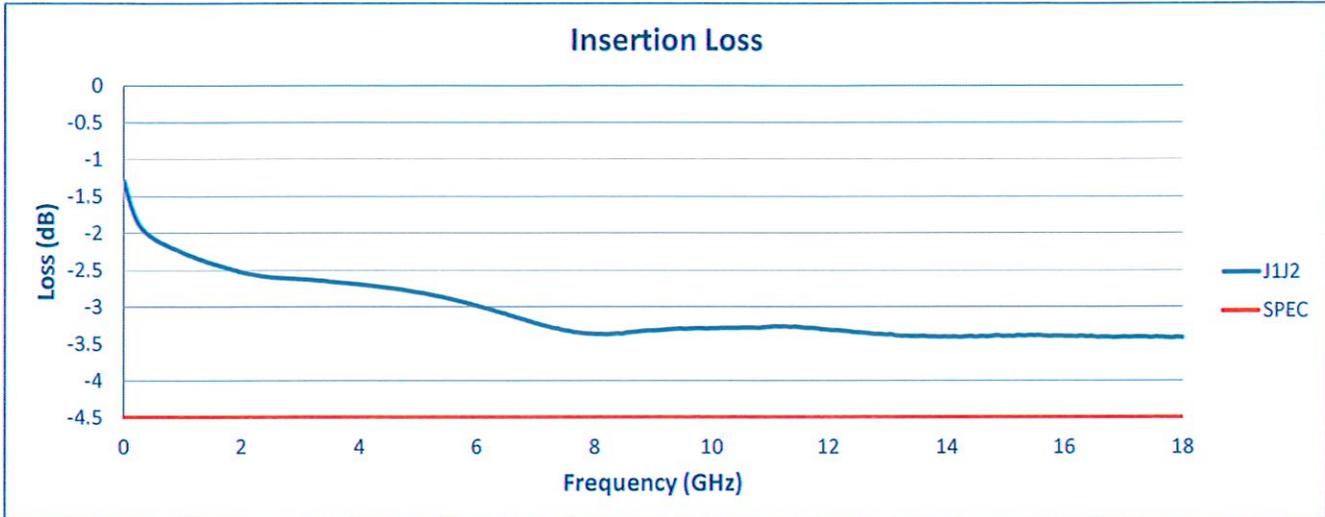


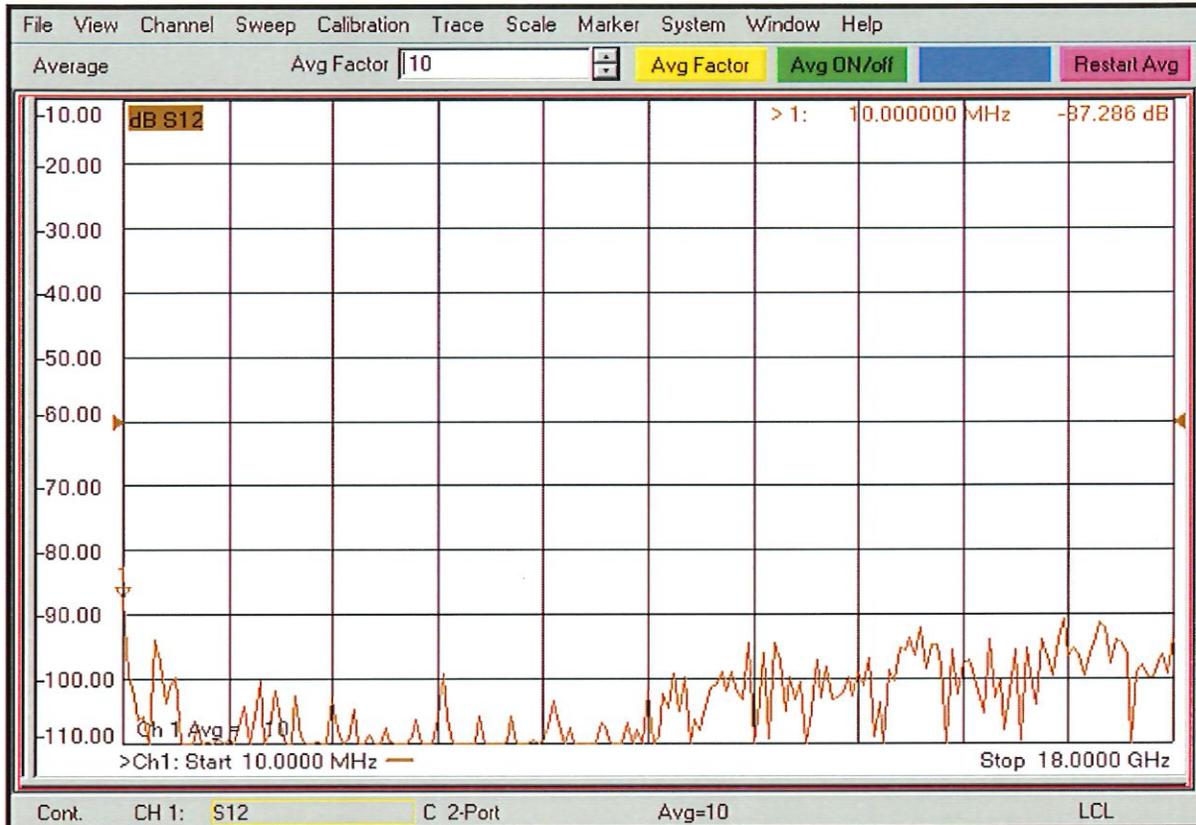
**SUMMARY TEST DATA
ON
P1T-DC18-60-T-SFF-HSLVT**

PL43102/2348



SUMMARY TEST DATA ON P1T-DC18-60-T-SFF-HSLVT

Leakage



Test Notes:

- Connect Port 1 of PNA to input to DUT
- Terminate RF output of DUT to 50 Ohms
- Use Port 2 of PNA and perform "sniff" test around CTRL and DC lines
- Plot worst case (must meet RF Leakage specifications)