

SUMMARY TEST DATA ON P2T-100M40G-95-T-512-292FF

Customer: _____
 SO No: _____
 Model No: P2T-100M40G-95-T-512-292FF
 Serial No: PL51730/2511

Tested By: S. O'Neill
 Temperature: +25°C
 Date: 3/12/2025
 Drawing No: 26921044 REV: C1

| TEST ITEM NO. | PARAMETERS | SPECIFIED VALUE | TEST RESULTS | QA QC |
|---------------|------------------|---|--|------------|
| | | | +25°C | |
| 1 | Frequency Range: | 0.1 to 40 GHz | Spec 0.1 to 40 GHz | PMI QA4 |
| 2 | Insertion Loss: | 4.0 dB (0.1 to 18 GHz) Typ. 5.0 dB (18 to 26.5 GHz) Typ. 5.5 dB (26.5 to 40 GHz) Typ. 6.0 dB (0.1 to 40 GHz) Max | 3.2 dB | |
| | | | 4.6 dB | |
| | | | 5.8 dB | |
| | | | See Graphs | |
| 3 | VSWR: | 2.5 : 1 Max | 2.1 :1 Input | |
| | | | 2.1 :1 Output | |
| | | | 1.8 :1 Output (Term.) | |
| | | | See Graphs | |
| 4 | Isolation: | 95 dB (0.1 to 18 GHz) Min. 80 dB (18 to 26.5 GHz) Min. 70 dB (26.5 to 40 GHz) Min. | 98.8 dB | |
| | | | 94.5 dB | |
| | | | 84.9 dB | |
| | | | See Graphs | |
| 5 | Input Power: | +20 dBm CW Max. | +20 dBm CW Max. | |
| 6 | Switching Speed: | 100 ns Max. | Pass See Typical Characteristics | |
| 7 | Control Signal: | TTL Logic "0" = J1-J2 On TTL Logic "1" = J1-J3 On | TTL Logic "0" = J1-J2 On TTL Logic "1" = J1-J3 On | |
| 8 | DC Voltage: | +5 VDC @ 120 mA Max. -12 VDC @ 65 mA Max. | +5 VDC @ 38 mA -12 VDC @ 27 mA | PMI QA4 |

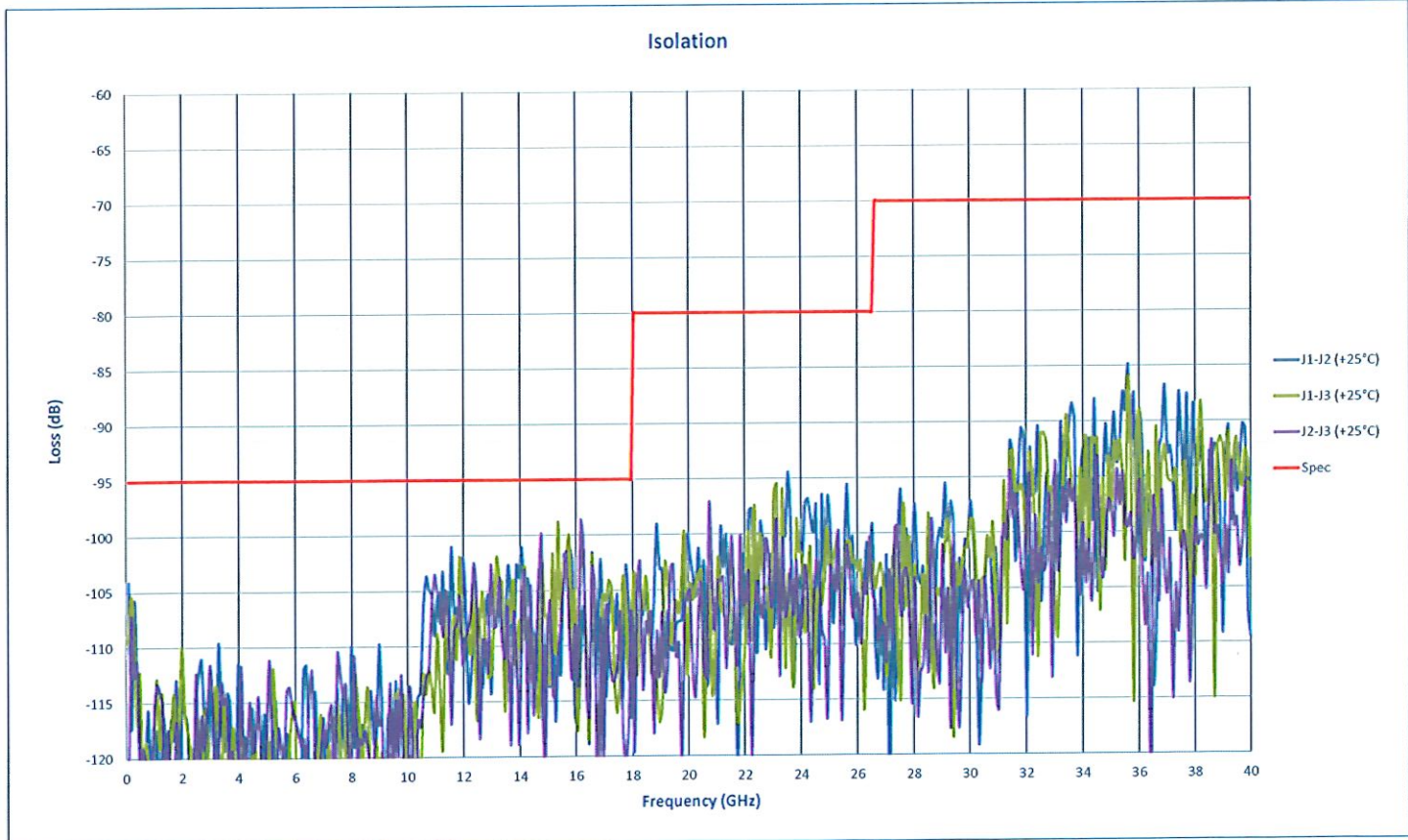
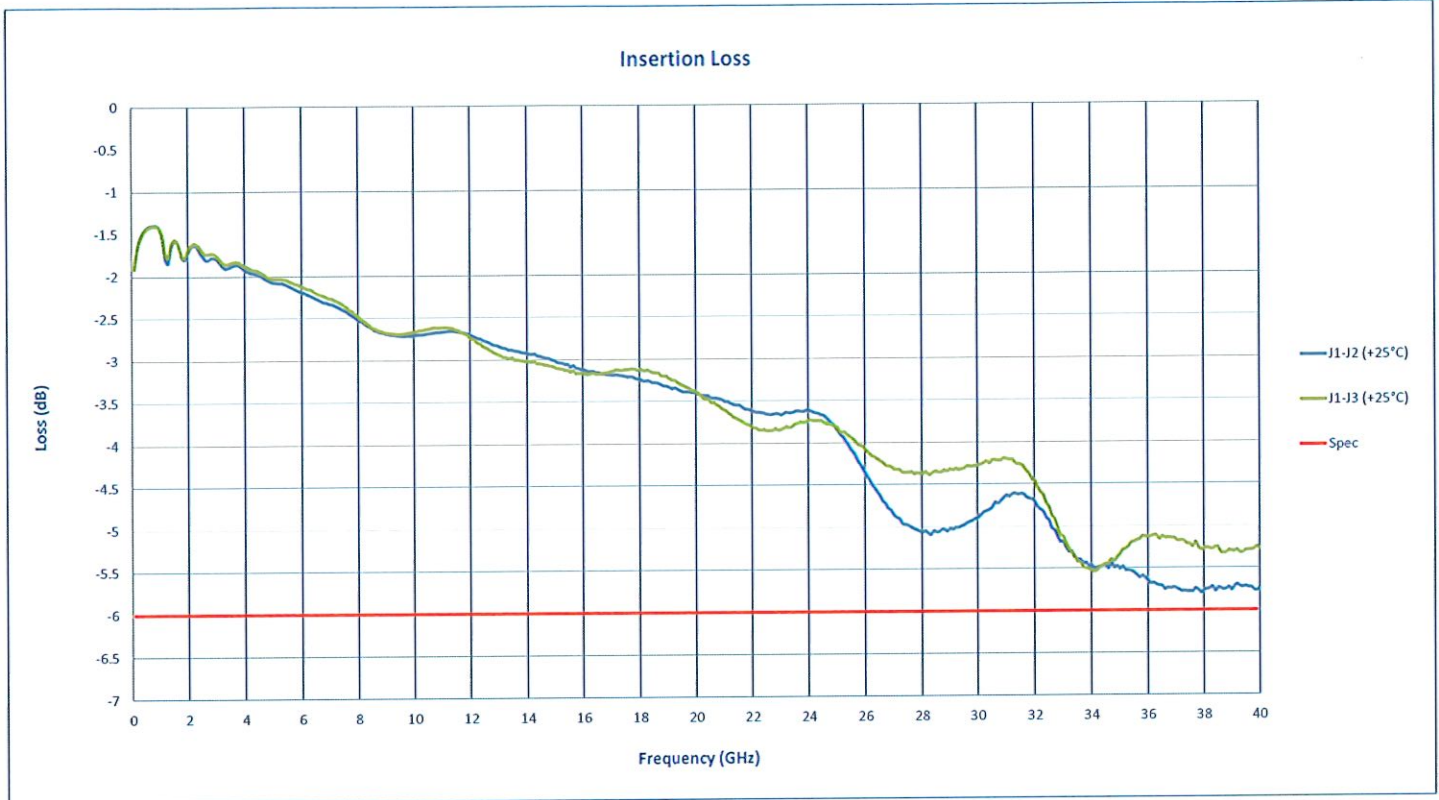
QA/QC Approval:

Cammeron Kelley

Date:

3/12/25

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