

**SUMMARY TEST DATA
ON
PLO-48D8G-EXT**

Customer: _____
SO: _____
Model No: **PLO-48D8G-EXT**
Serial No: **PL52980/2520**

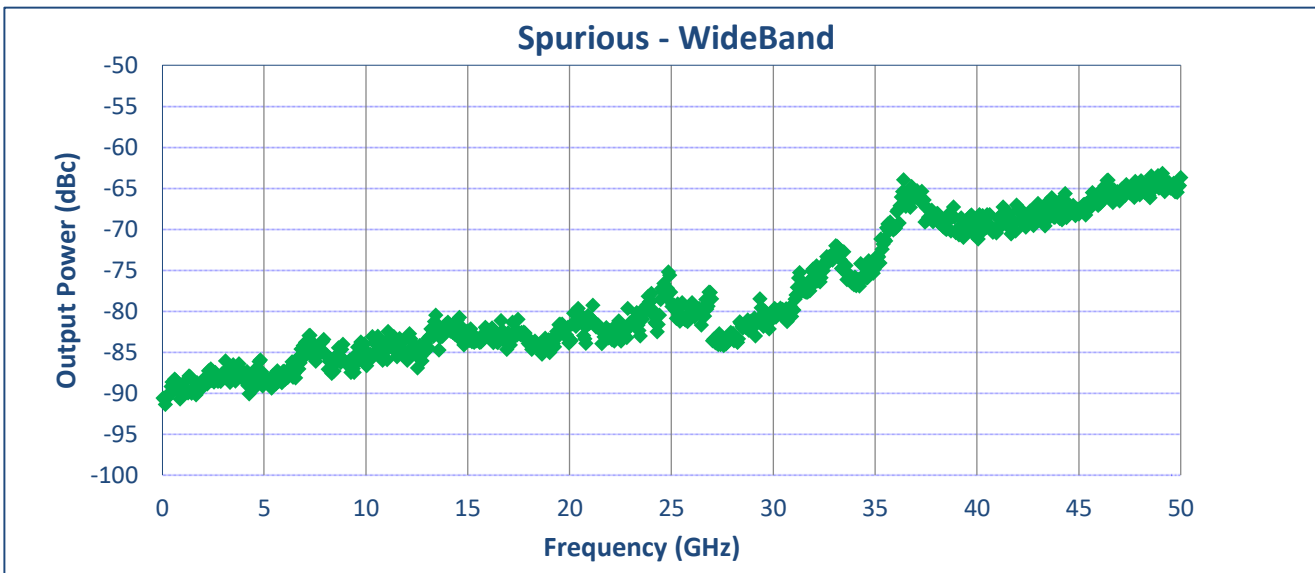
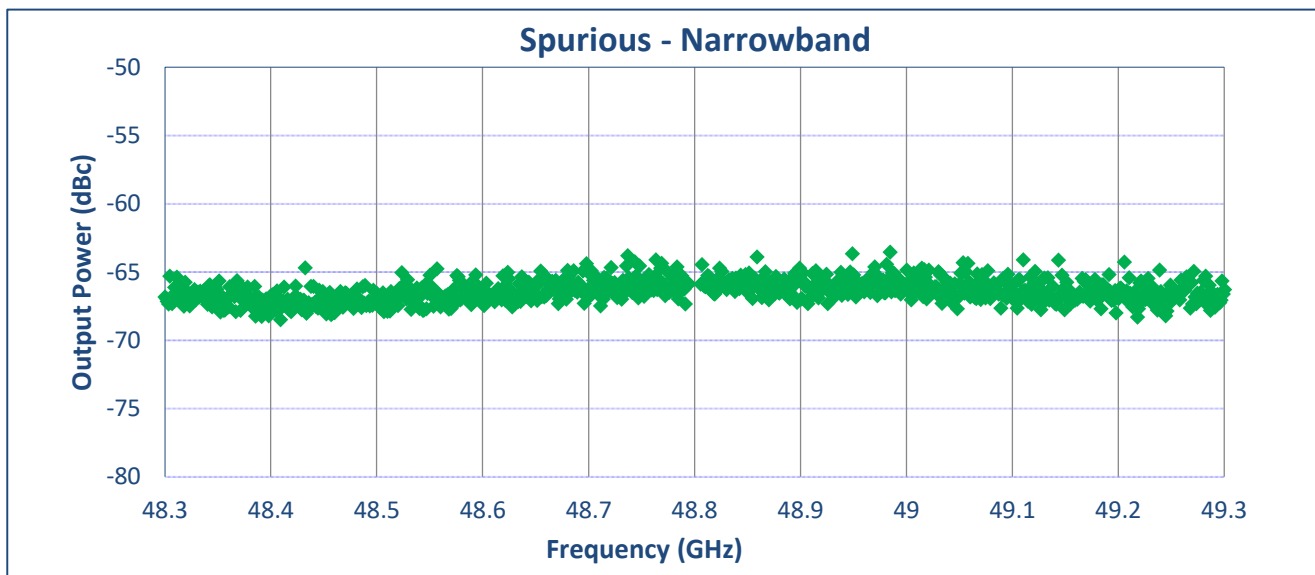
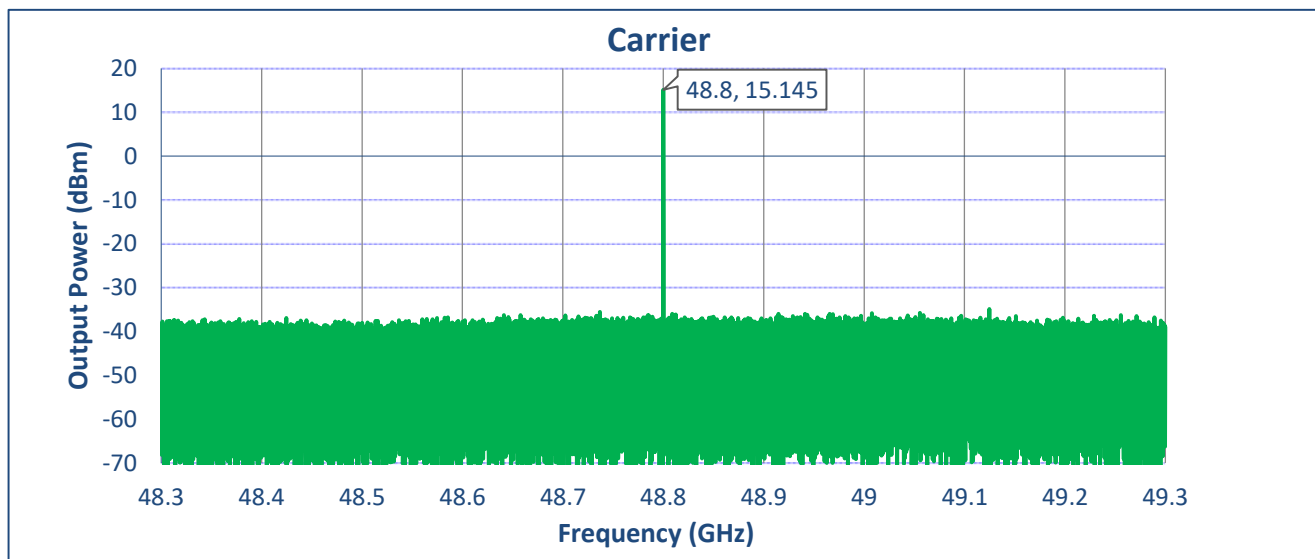
Tested By: **S. Lange**
Temperature: **25°C**
Date: **5/7/2025**
Drawing No: **27651200** Rev: **A1**

TEST ITEM	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC
1	Output Frequency	48.8 GHz	48.8 GHz	
2	Reference Frequency	100 MHz (Sine Wave)	100 MHz (Sine Wave)	
3	Reference Input Level	+3 to +10 dBm	+3 to +10 dBm	
4	Output Level	+10 dBm Min.	15.1dBm	
5	Spurious	-60 dBc Max.	-63.2dBc	
6	Load VSWR	1.5:1 Max.	1.5:1 Max.	
7	Output Phase Noise	-80 dBc Max. @ 100 Hz	-82.3dBc @ 100 Hz	
		-93 dBc Max. @ 1 kHz	-104.4dBc @ 1 kHz	
		-94 dBc Max. @ 10 kHz	-109.7dBc @ 10 kHz	
		-102 dBc Max. @ 100 kHz	-109.8dBc @ 100 kHz	
		-128 dBc Max. @ 1 MHz	-130.9dBc @ 1 MHz	
8	Reference Phase Noise	-137 dBc Max. @ 100 Hz	-137 dBc @ 100 Hz	
		-155 dBc Max. @ 1 kHz	-155 dBc @ 1 kHz	
		-160 dBc Max. @ 10 kHz	-160 dBc @ 10 kHz	
9	Supply Voltage	+12 V @ 500 mA Typ.	+12 V @ 349 mA	
10	Lock Detector	TTL High = Lock	TTL High = Lock	

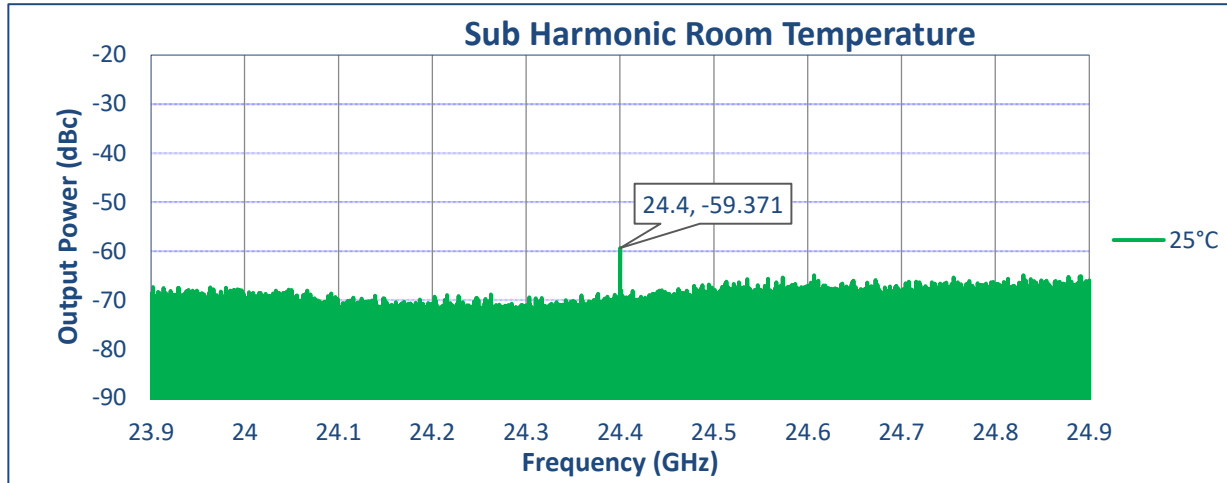
QA/QC Approval _____

Date: _____

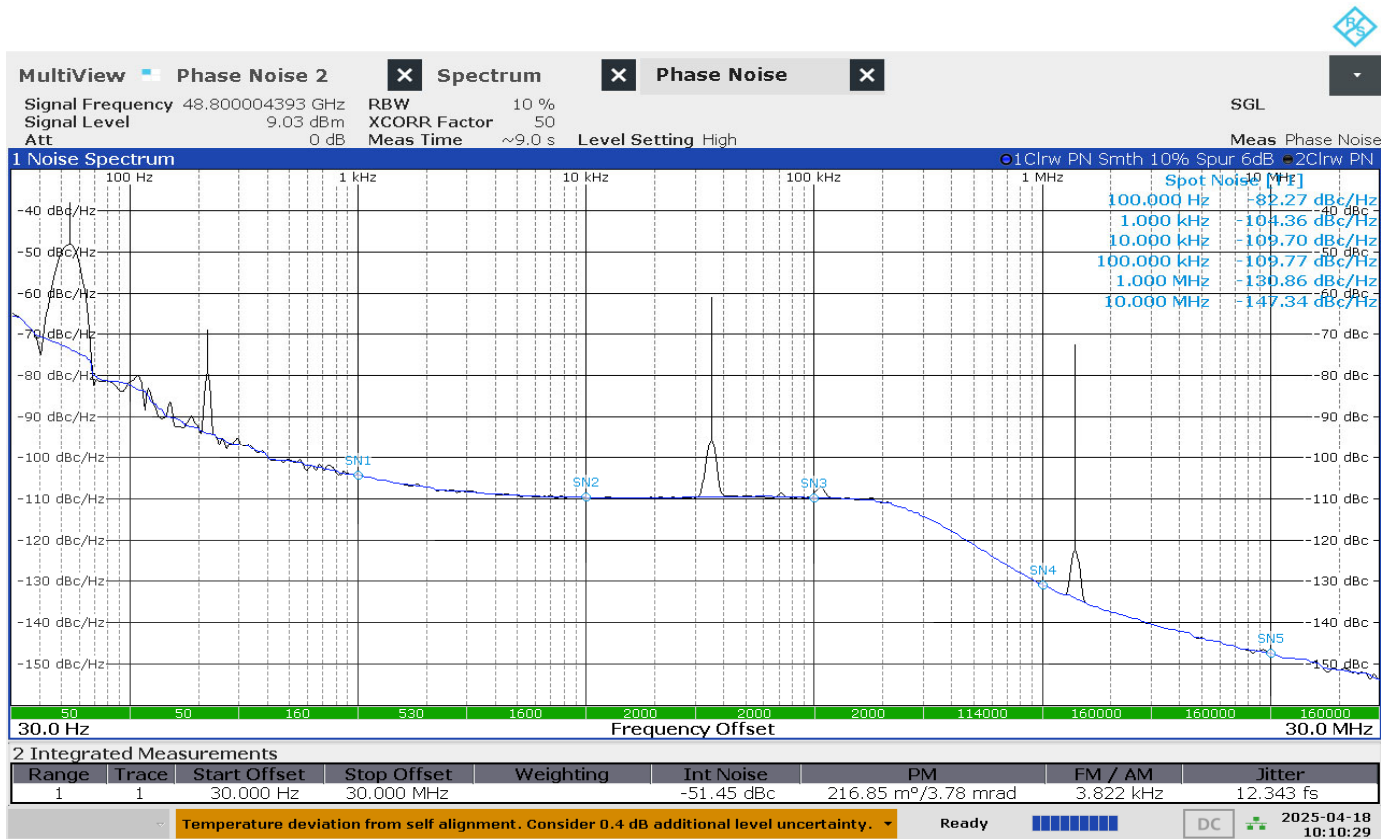
**SUMMARY TEST DATA
ON
PLO-48D8G-EXT**



Sub Harmonics



Phase Noise



10:10:30 AM 04/18/2025