



SUMMARY TEST DATA
ON
PLDRO-6500-13DBM-SFF

Customer: _____
SO No: _____
Model No: PLDRO-6500-13DBM-SFF
Serial No: PL56207/2545

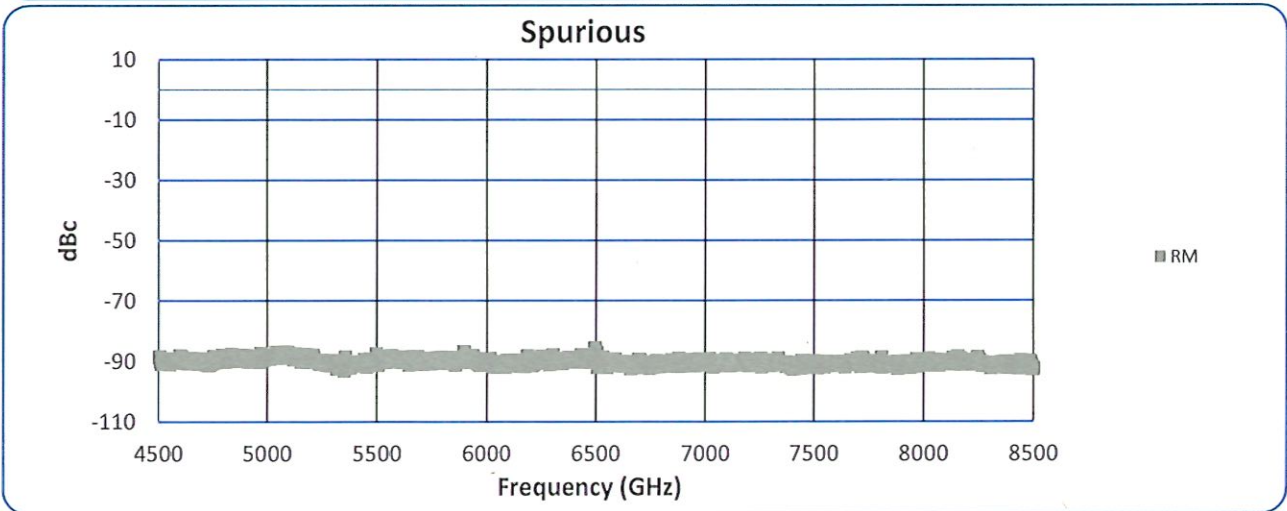
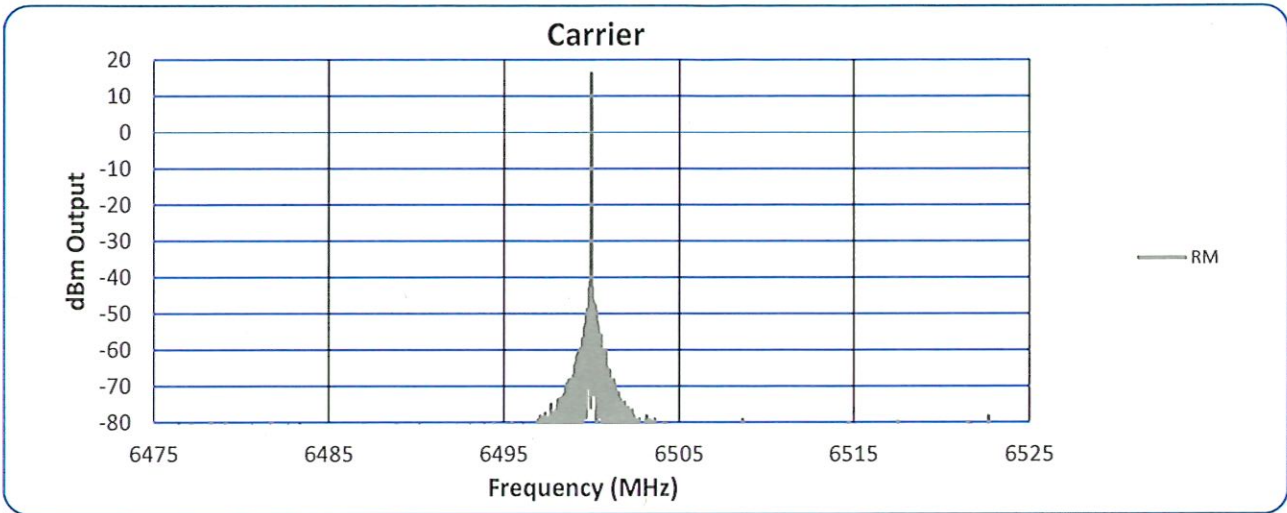
Tested By: K Craven
Temperature: 25°C
Date: 11/12/2025
Drawing No: 27643440 Rev: A1

TEST ITEM	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC
1	Output Frequency	6500 MHz	6500 MHz	PMI QA4
2	Output Level	13 dBm MIN.	16.1 dBm	
3	Spurious	-75 dBc MAX.	-85.7 dBc	
4	Harmonics	-20 dBc MAX.	-32.9 dBc	
5	Phase Noise	-135 dBc/Hz MAX. @ 1 MHz	-140 dBc/Hz @ 1 MHz	
6	DC Supply Voltage	12V @ 300 mA MAX.	12V @ 240 mA	
7	Lock Detector	TTL HIGH In Lock	Pass	PMI QA4

QA/QC Approval: _____

Date: 11/17/2025

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Phase Noise Curve

Settings		Residual Noise [T1 w/o spurs]		Phase Detector +20 dB	
Signal Frequency:	6.500001 GHz	Int PHN (30.0 .. 30.0 M)	-64.0 dBc		
Signal Level:	8.18 dBm	Residual PM	51.039 m°		
Cross Corr Mode	Harmonic 1	Residual FM	14.231 kHz		
Internal Ref Tuned	Internal Phase Det	RMS Jitter	0.0218 ps		

