



## SUMMARY TEST DATA ON PS-85M4G-9B-SFF

Customer: \_\_\_\_\_  
 SO: \_\_\_\_\_  
 Model No: PS-85M4G-9B-SFF  
 Serial No: PL59521/2618

Tested By: E. Castillo  
 Date: Wednesday, April 29, 2026  
 Temp: +25°C  
 Drawing No: 27629014      Rev: C1

Data Taken at -10 dBm Input Power

Test Item No.	Parameters	Specified Value	Measured Value	QA QC
1	Frequency Range:	85 MHz to 4 GHz	85 MHz to 4 GHz	PMI QA5
2	Control:	9 Bit TTL	9 Bit TTL	
3	Insertion Loss:	13 dB MAX.	9.71 dB See Plot	
4	Phase Shift:	360° TYP.	359.43°	
5	Amplitude Error:	±1° TYP.	±0.73° MAX. See Plot	
6	Phase Shift Error:	±10° TYP.	±3.26° MAX. See Plot	
7	Power Supply:	+15 V @ 250 mA MAX -15 V @ 50 mA MAX	+15 V @ 182 mA -15 V @ 48 mA	PMI QA5

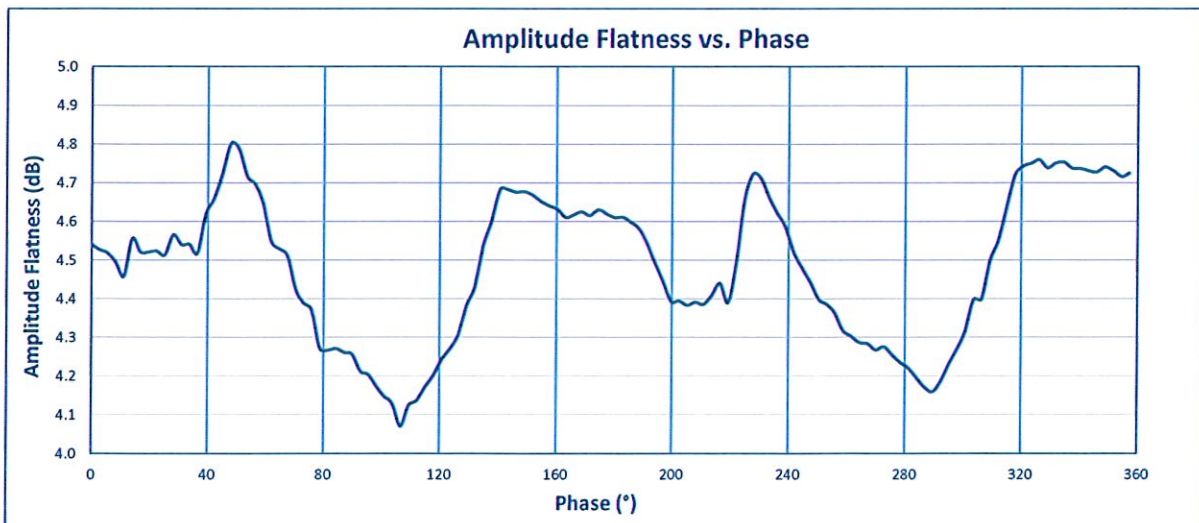
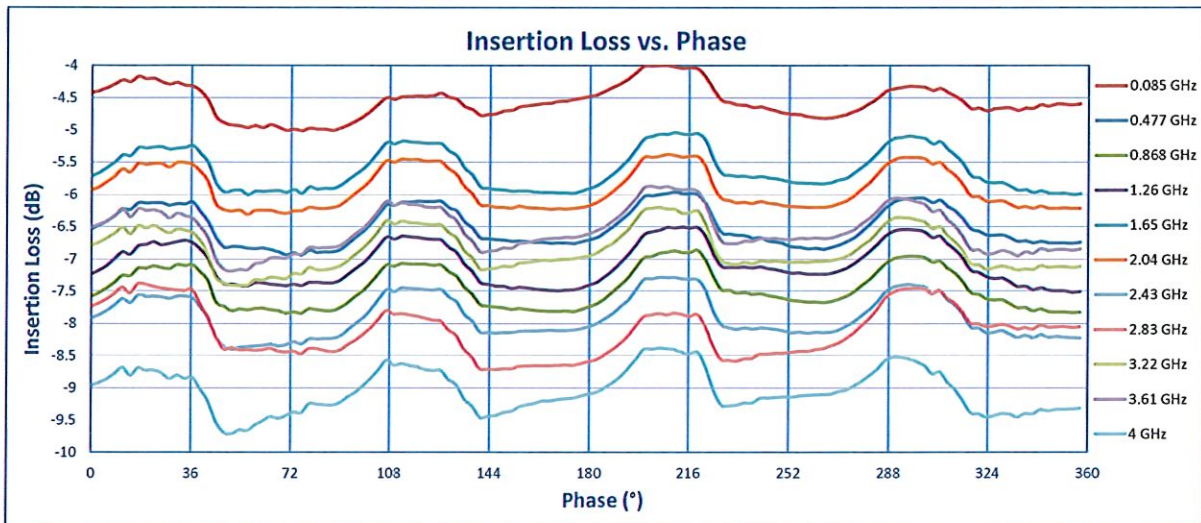
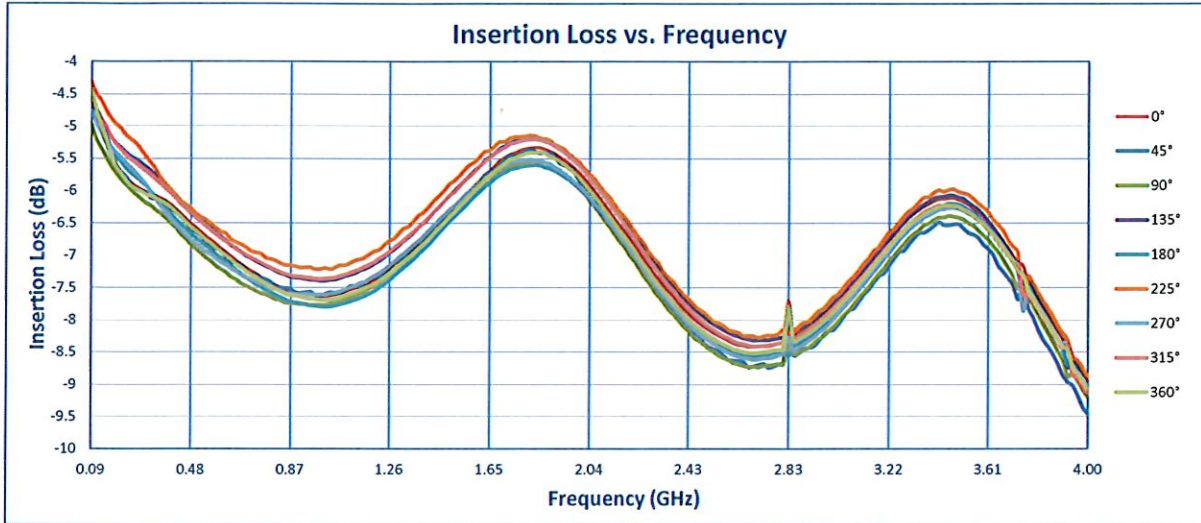
\* FOR ALL OTHER SPECIFICATIONS SEE TYPICAL CHARACTERISTICS ON THE PMI WEBSITE

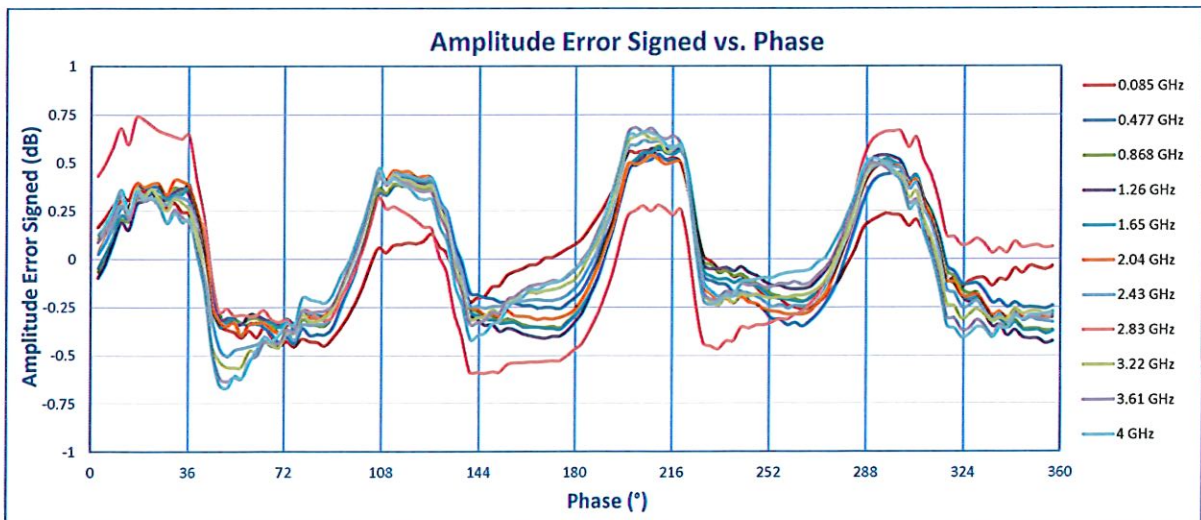
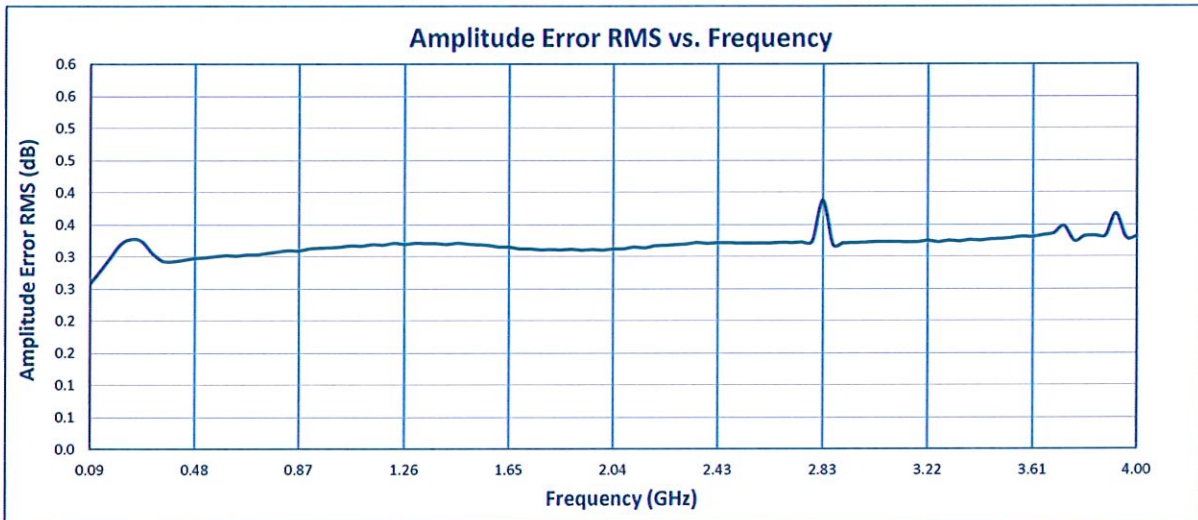
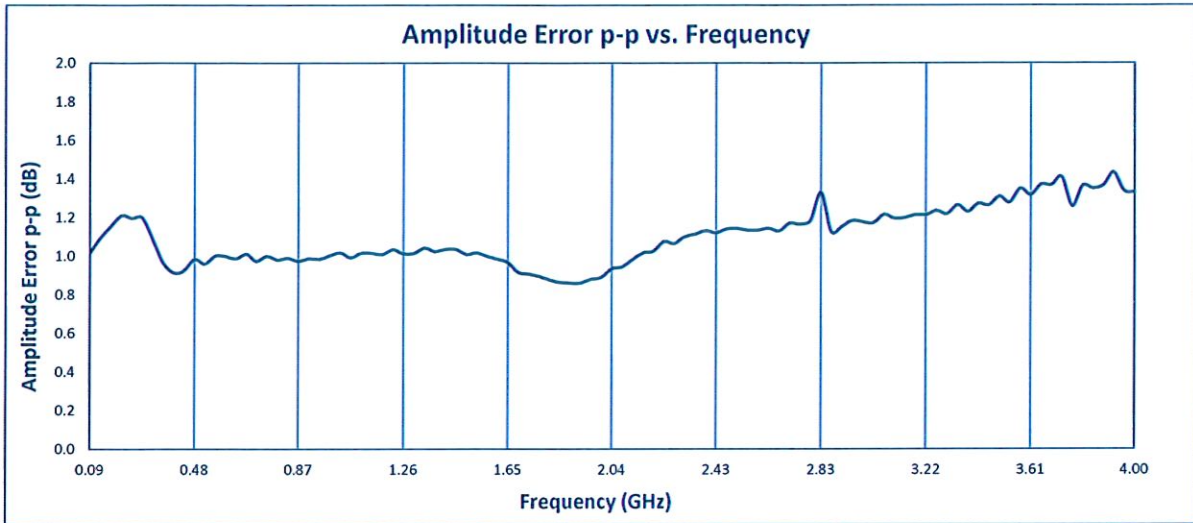
Programed Phase Shift	Average Phase Shift	Average Signed Phase Error	Flatness
DEG	DEG	DEG	DEG
0.70	0.73	-0.03	0.39
1.41	1.46	-0.05	0.45
2.81	2.92	-0.11	0.49
5.63	5.78	-0.16	0.71
11.25	11.32	-0.07	0.97
22.50	22.62	-0.12	1.31
45.00	45.44	-0.44	2.78
90.00	90.27	-0.27	3.02
180.00	180.61	-0.61	3.72

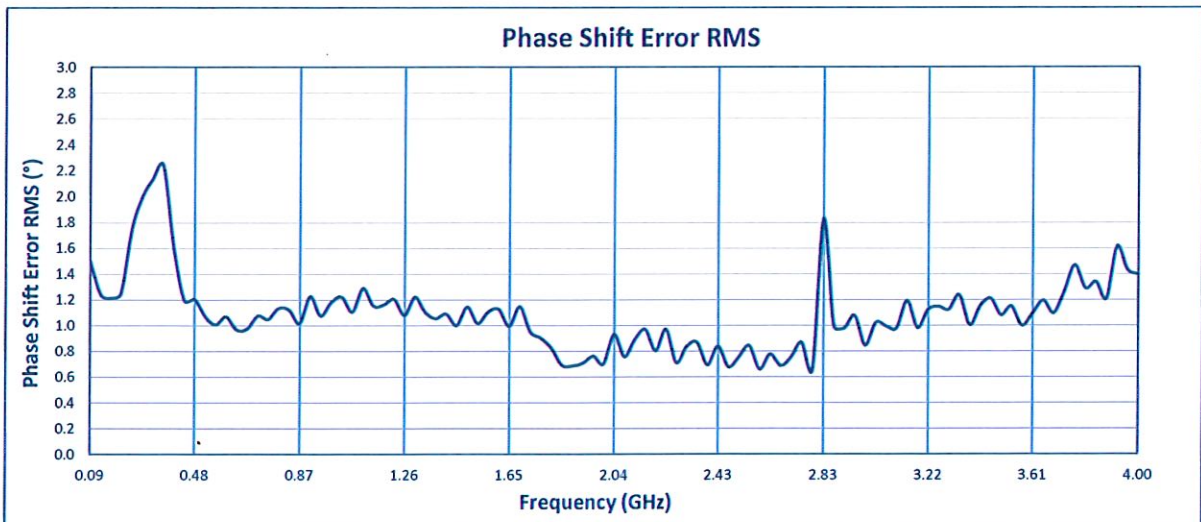
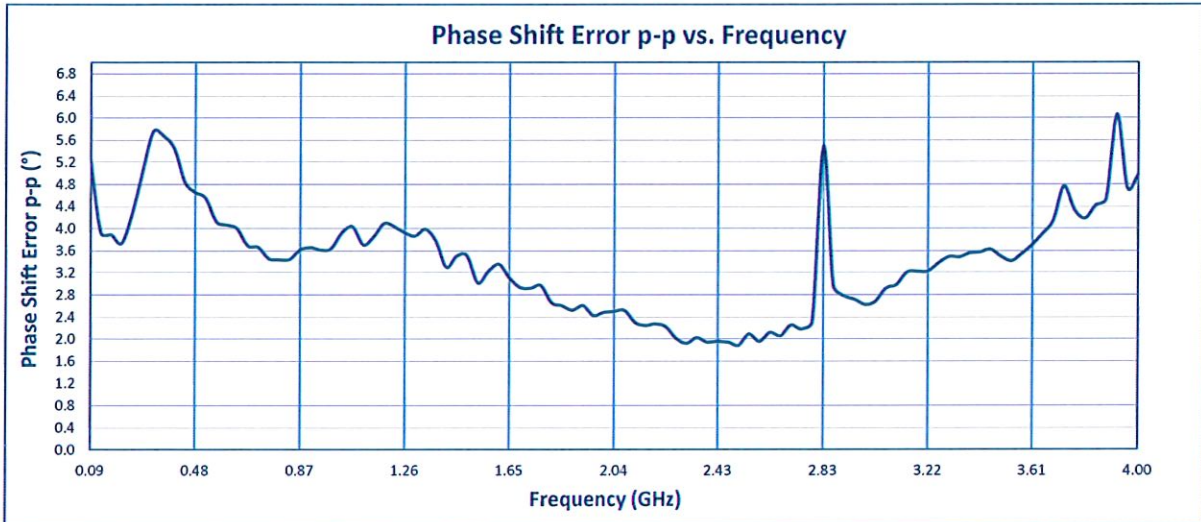
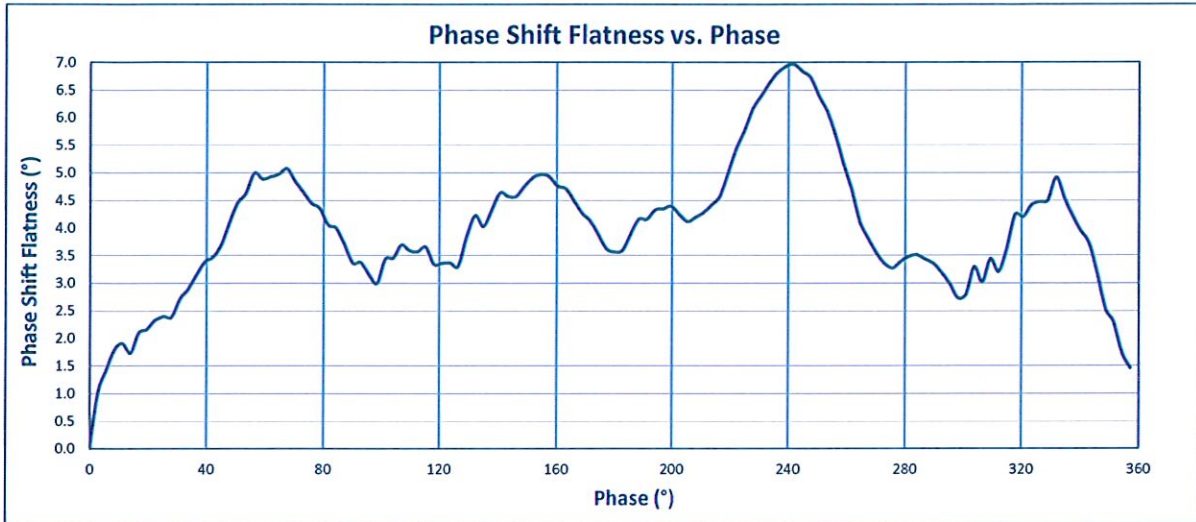
Programed Phase Shift	Average Phase Shift	Average Signed Phase Error	Flatness
DEG	DEG	DEG	DEG
33.75	34.09	-0.34	2.89
67.50	68.13	-0.63	5.07
101.25	101.72	-0.47	3.44
135.00	135.57	-0.57	4.03
168.75	169.53	-0.78	4.27
202.50	202.39	0.11	4.24
236.25	235.71	0.54	6.81
270.00	269.37	0.63	3.55
303.75	302.90	0.85	3.29
337.50	336.98	0.52	4.22

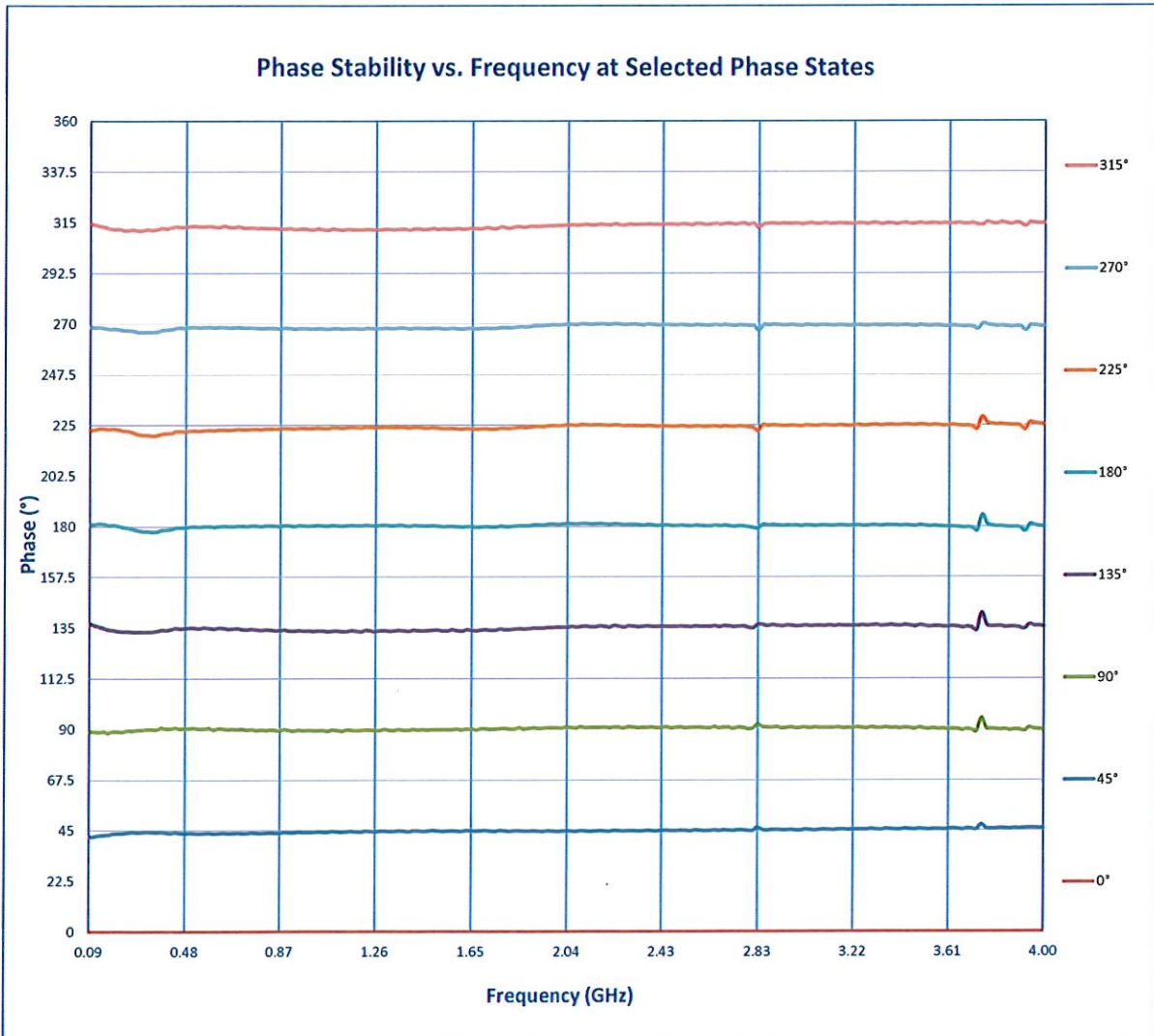
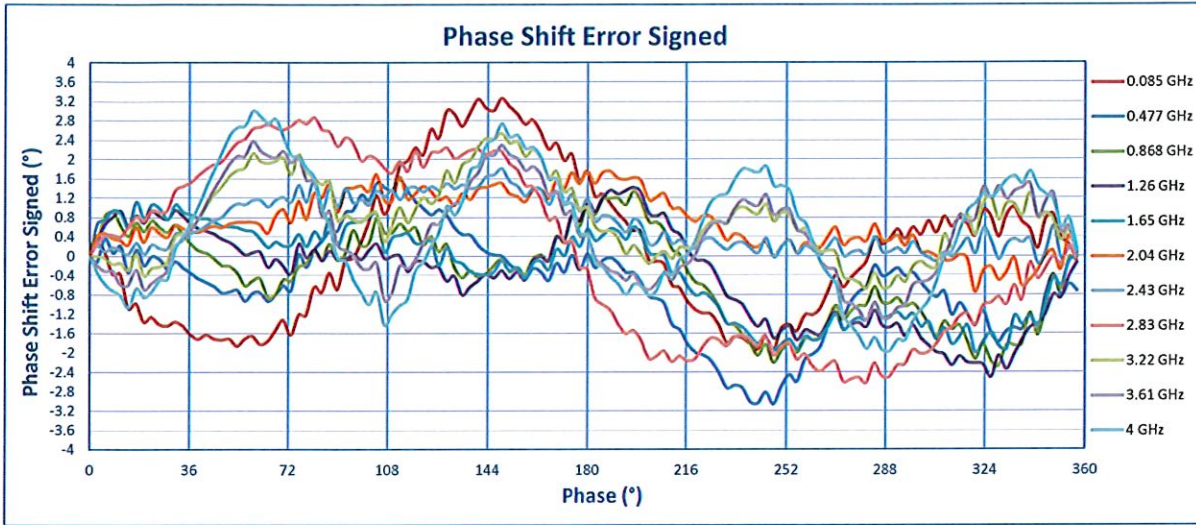
QA/QC Approval: \_\_\_\_\_

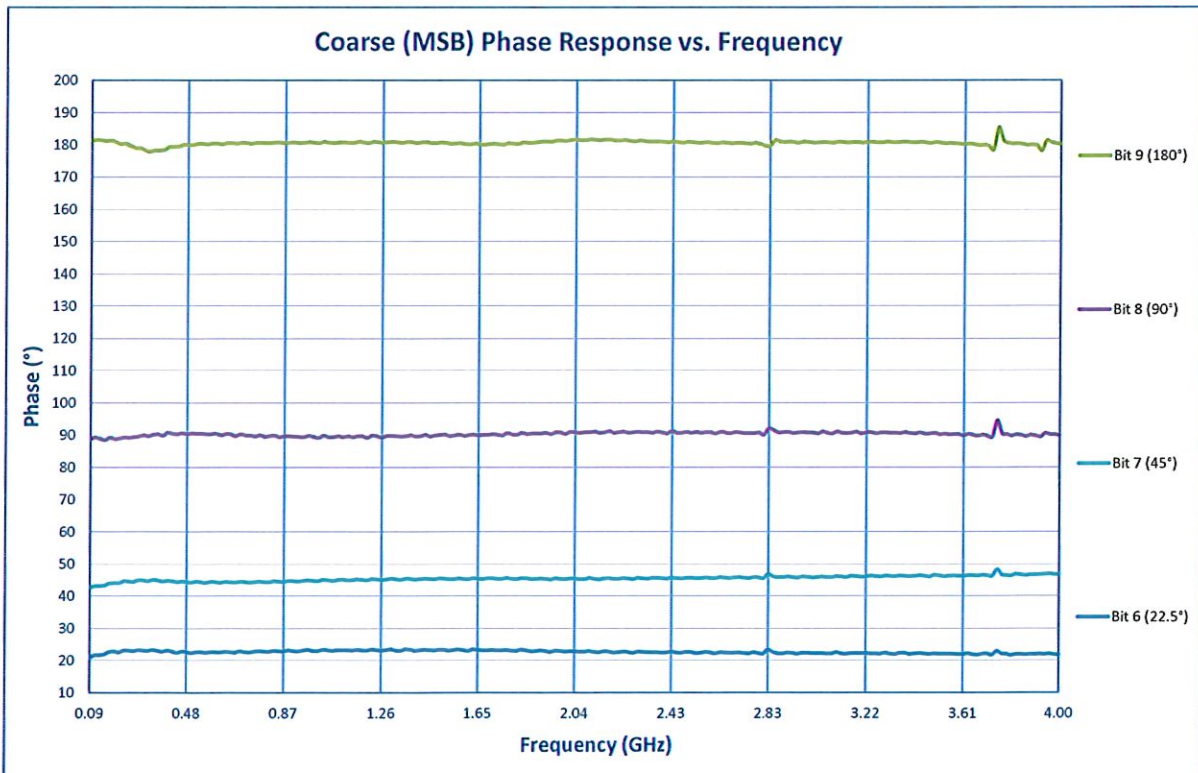
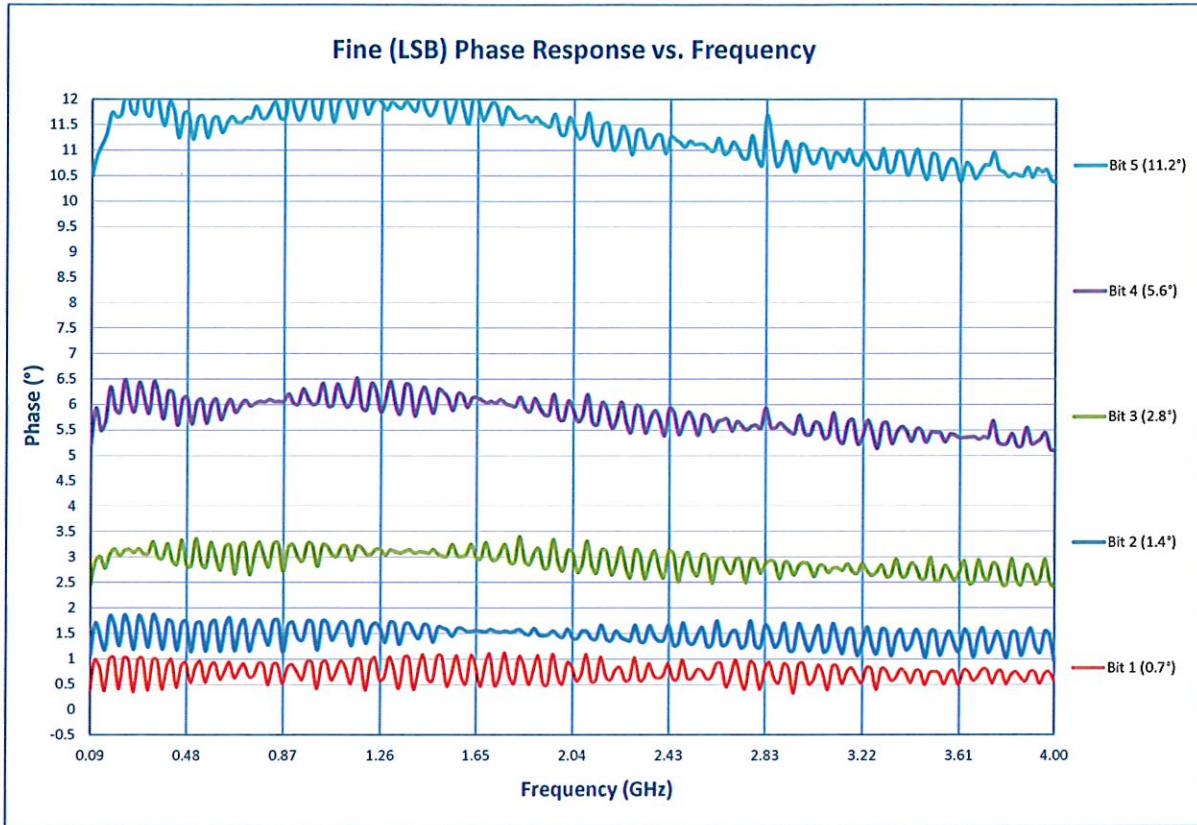
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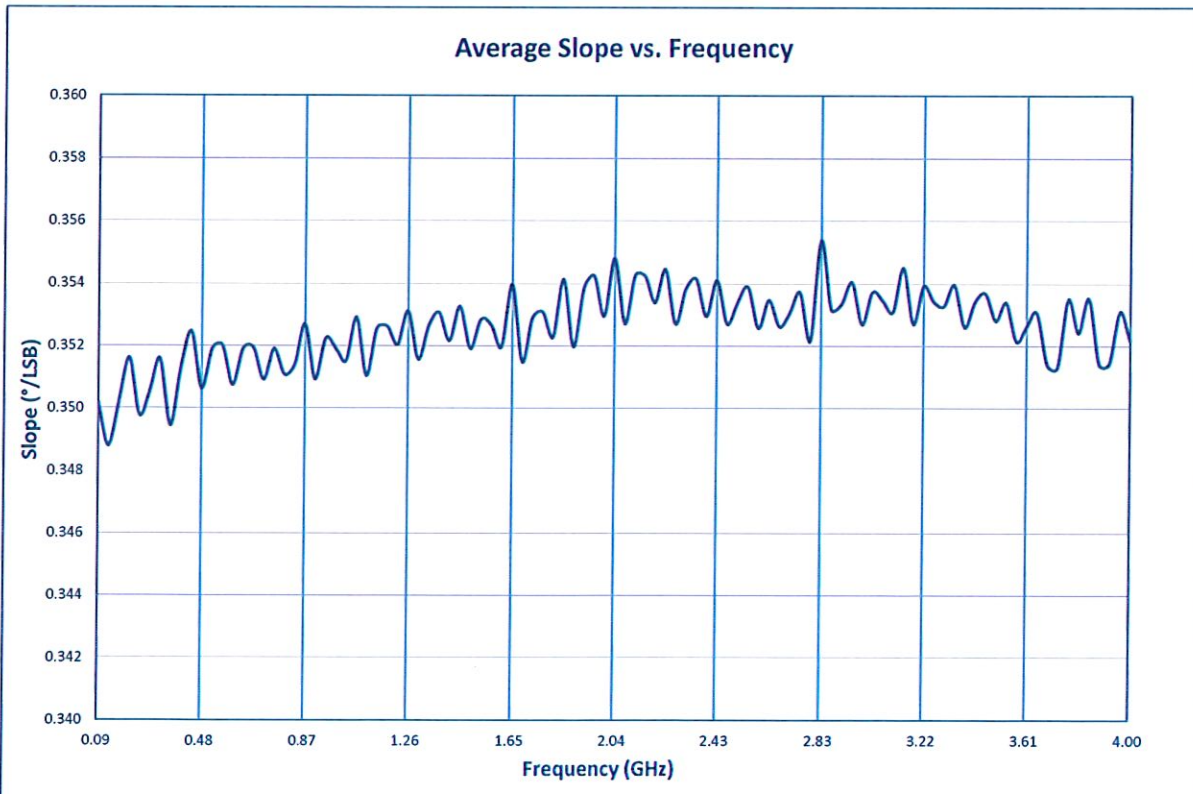
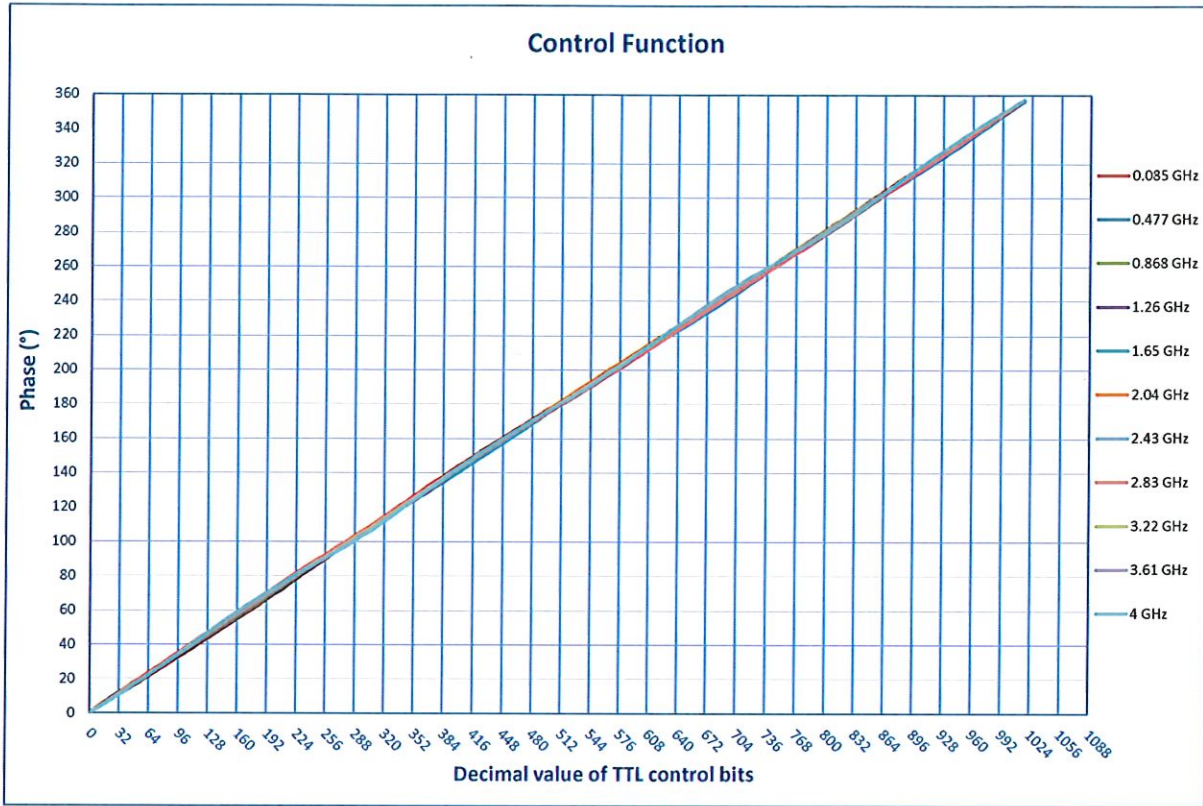


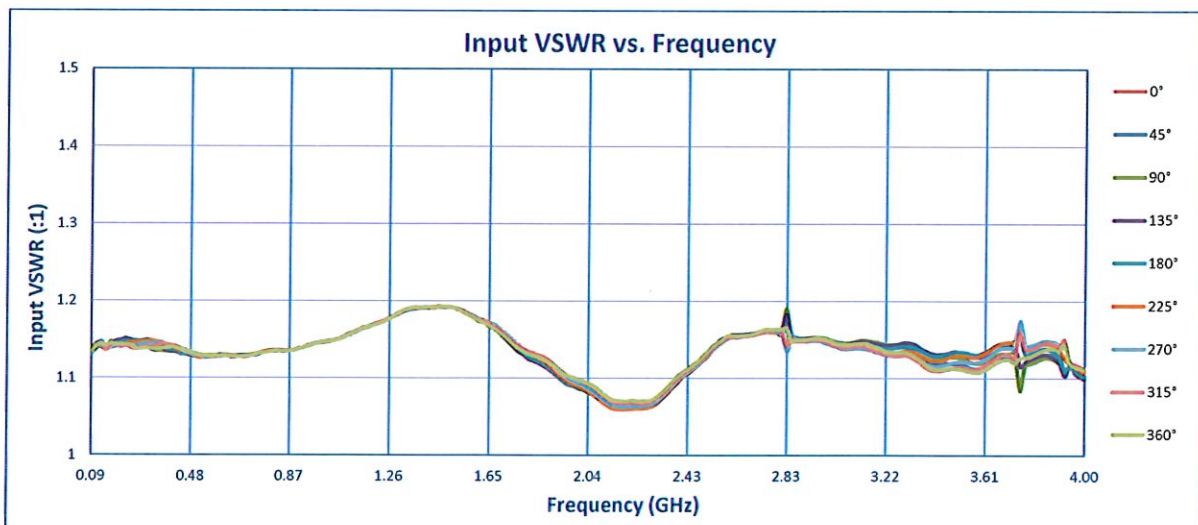
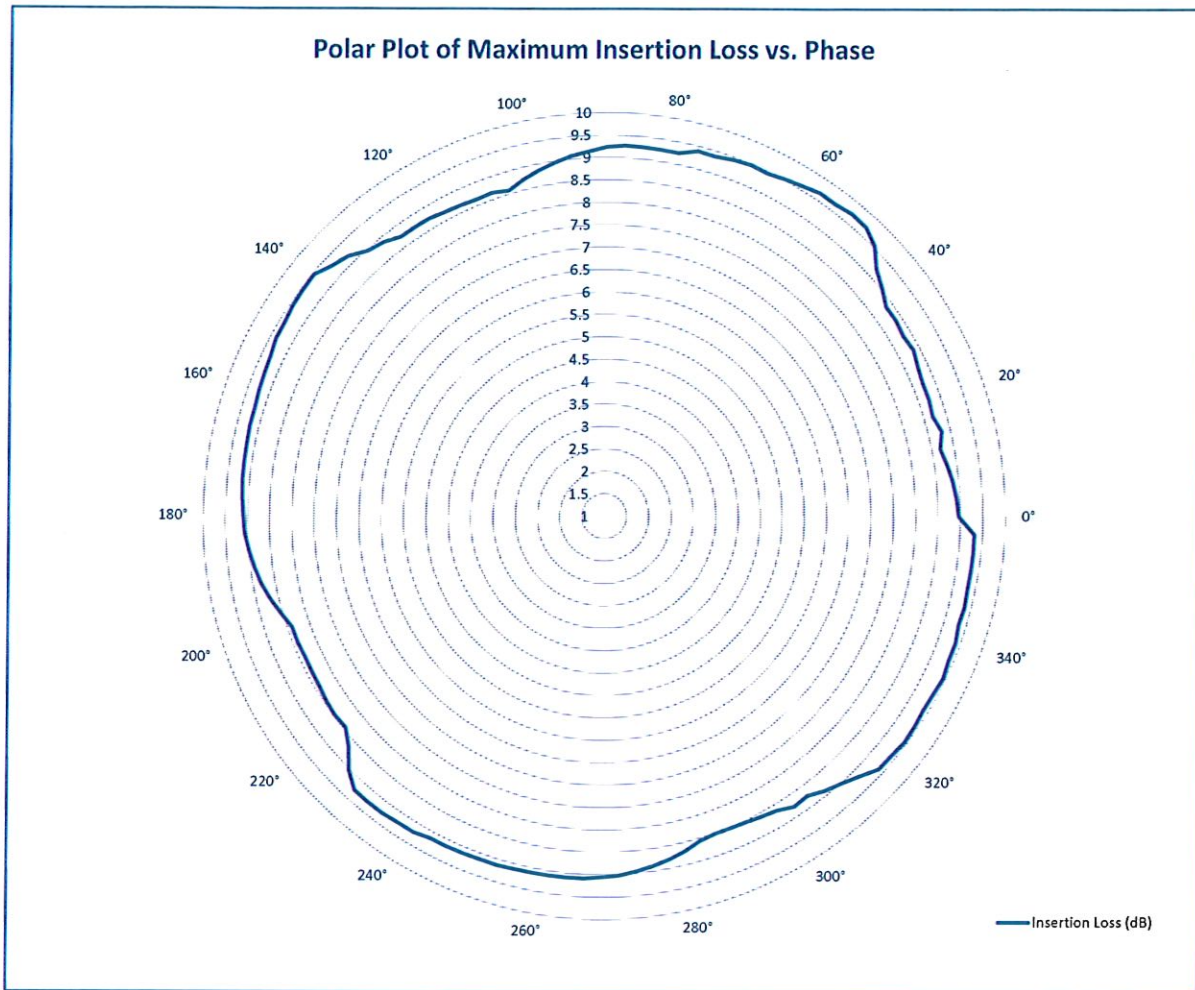












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ON  
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