SUMMARY TEST DATA
ON
PLDRO-1D645-13DBM-INT-SFF

Customer: _____ **Tested By:** V. Pratap
SO No: _____ **Temperature:** 25°C
Model No: PLDRO-1D645-13DBM-INT-SFF **Date:** 10/26/2026
Serial No: PL63119/2642 **Drawing No:** 27655500 **Rev:** A1

TEST ITEM	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC
1	Output Frequency	1.645 GHz	**1.645 GHz**	
2	Frequency Accuracy	±1ppm MAX.	**<1 ppm**	
3	Output Level	+13 dBm MIN.	**16.3 dBm**	
4	Spurious	-80 dBc MAX.	**-85.3 dBc (See Graph)**	
5	Harmonics	-20 dBc MAX.	**-29.1 dBc (See Graph)**	
6	Phase Noise	-102 dBc/Hz MAX. @ 100 Hz -126 dBc/Hz MAX. @ 1 kHz -131 dBc/Hz MAX. @ 10 kHz -131 dBc/Hz MAX. @ 100 kHz -140 dBc/Hz MAX. @ 1 MHz	**-115 dBc/Hz @ 100 Hz** **-132 dBc/Hz @ 1 kHz** **-140 dBc/Hz @ 10 kHz** **-141 dBc/Hz @ 100 kHz** **-146 dBc/Hz @ 1 MHz**	
7	DC Supply Voltage	+15V @ 450 mA MAX. (Steady); 850 mA Max (Surge)	**15V @ 350 mA (Steady); 525 mA (Surge)**	
8	Lock Detector	TTL HIGH In Lock	**TTL HIGH In Lock**	

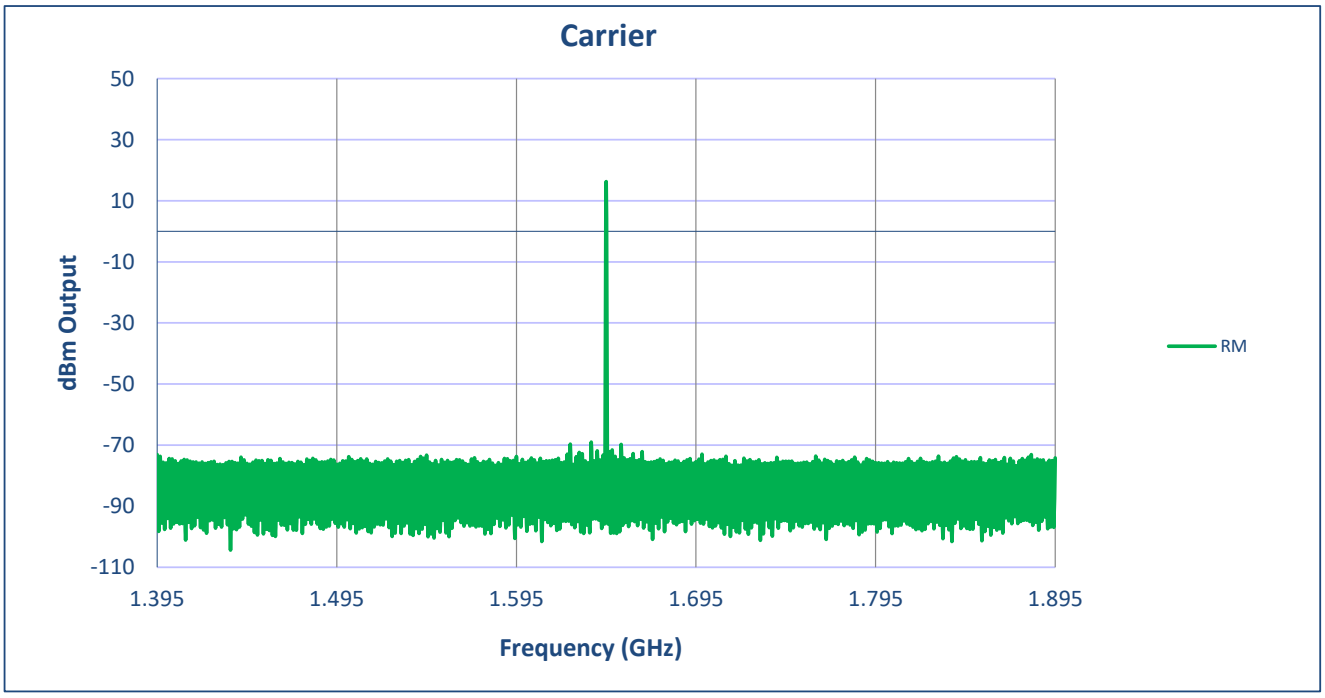
* FOR ALL OTHER SPECIFICATIONS, SEE TYPICAL CHARACTERISTICS ON THE PMI WEBSITE

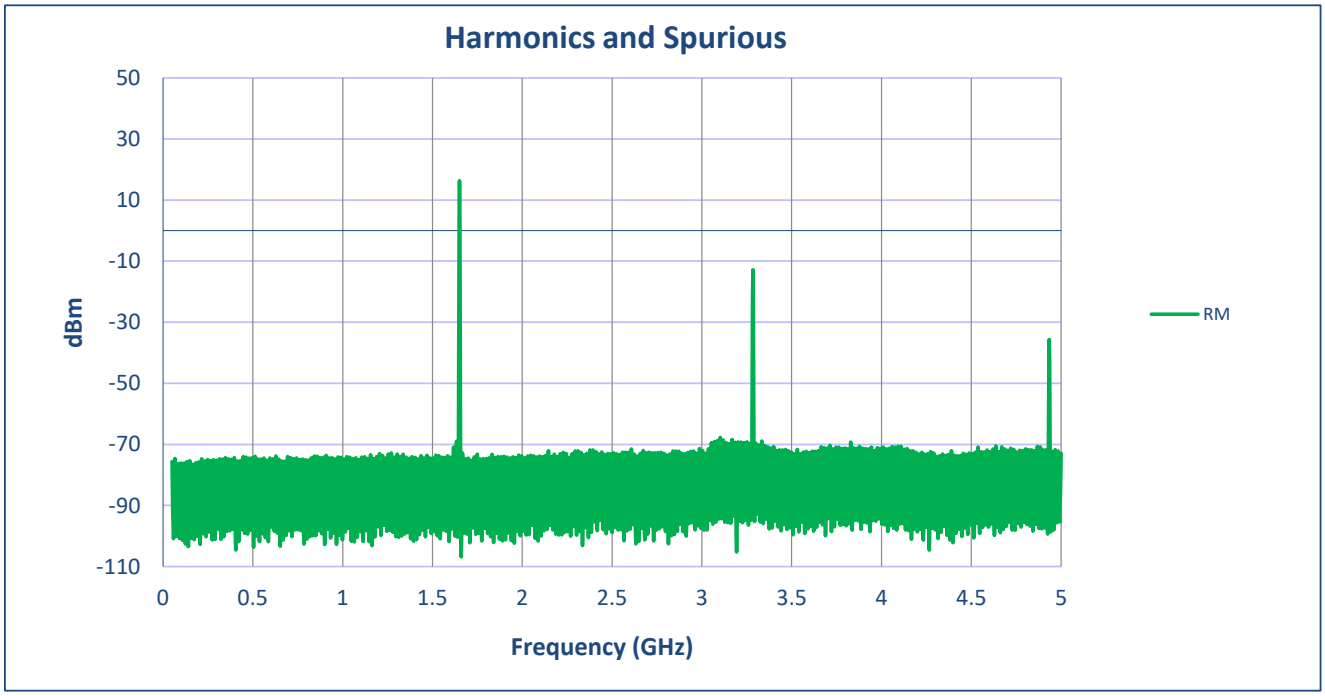
QA/QC Approval: _____ **Date:** _____

7309-A Grove Road Frederick, MD 21704 USA Phone: (301)662-5019 Fax: (301)662-1731
Email: sales@pmi-rf.com

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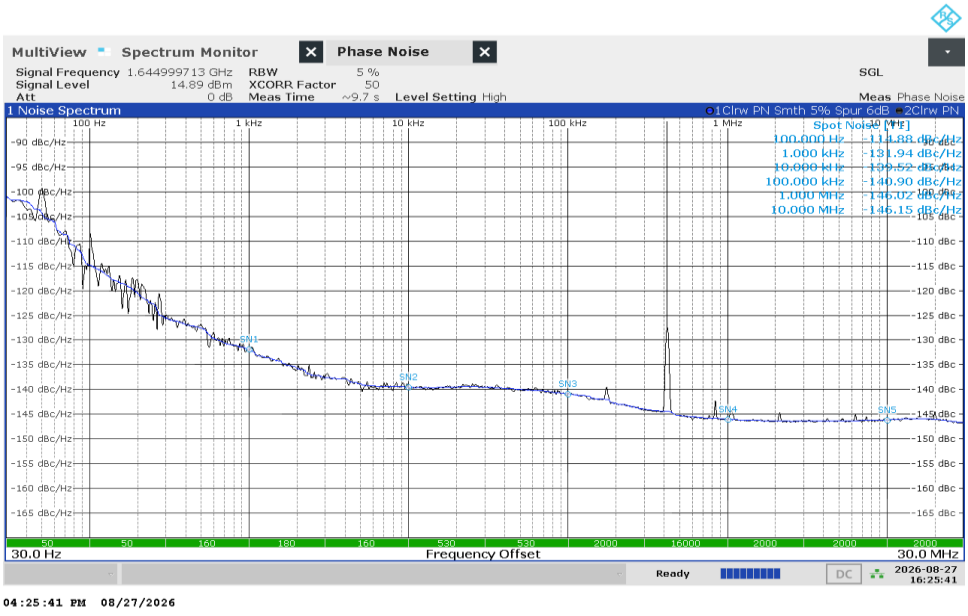


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Phase Noise Curve



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