

DESCRIPTION

PMI MODEL PMSN-3DR-05-STANDARD-OPT26-LVTTL-OPT28 IS A SINGLE POLE THREE THROW REFLECTIVE SWITCH MODULE, WITH VERY LOW INSERTION LOSS, HIGH ISOLATION, HIGH POWER (1 WATT) AND WITH HIGH SPEED (20 ns) INTEGRAL CMOS DRIVER, DESIGNED FOR 2.0 GHz TO 8.0 GHz OPERATION.

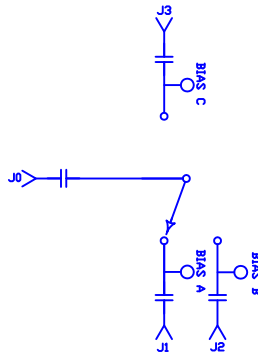
SPECIFICATIONS

- FREQUENCY: 2.0-8.0 GHz
 - INSERTION LOSS: 1.2dB @ 2.0-4.0 GHz MAX
1.5dB @ 4.0-8.0 GHz MAX
 - ISOLATION: 60dB MIN
 - VSWR: 1.75:1 MAX
 - POWER HANDLING: REFLECTIVE 1 WATT CW OR PEAK
(WITHOUT PERFORMANCE DEGRAGATION)
 - SWITCHING SPEED: RISE TIME: 10 ns MAX
FALL TIME: 10 ns MAX
ON TIME: 20 ns MAX
OFF TIME: 20 ns MAX
 - SURVIVAL POWER: 1 WATT AVG, 75 WATTS PEAK (1 sec max. pulse width)
 - POWER SUPPLY: +5 V ±5% @ 75 mA
-12 V @ 30mA MAX
 - CONTROL INPUT IMPEDANCE: 3.3/5.0V CMOS LOGIC
 - CONTROL LOGIC: "0"=ON @ 0V TO 1.2V
"1"=OFF @ 3.3V TO 5.5V
 - CONNECTOR: SMA FEMALE
 - SIZE 1.31 X 1.25 X 0.24
 - WEIGHT 1.50 OUNCE TYPICAL
 - FINISH: PAINTED BLUE
- *ROHS AND NON-ROHS AVAILABLE

ENVIRONMENTAL RATINGS

- TEMPERATURE: -65°C TO +85°C (OPERATING)
-65°C TO +125°C (STORAGE)
- HUMIDITY: MIL-STD-202F, METHOD 103B COND. B
- SHOCK: MIL-STD-202F, METHOD 213B COND. B
- VIBRATION: MIL-STD-202F, METHOD 204D COND. B
- ALTITUDE: MIL-STD-202F, METHOD 105C COND. B
- TEMPERATURE CYCLE: MIL-STD-202F, METHOD 107D COND. A

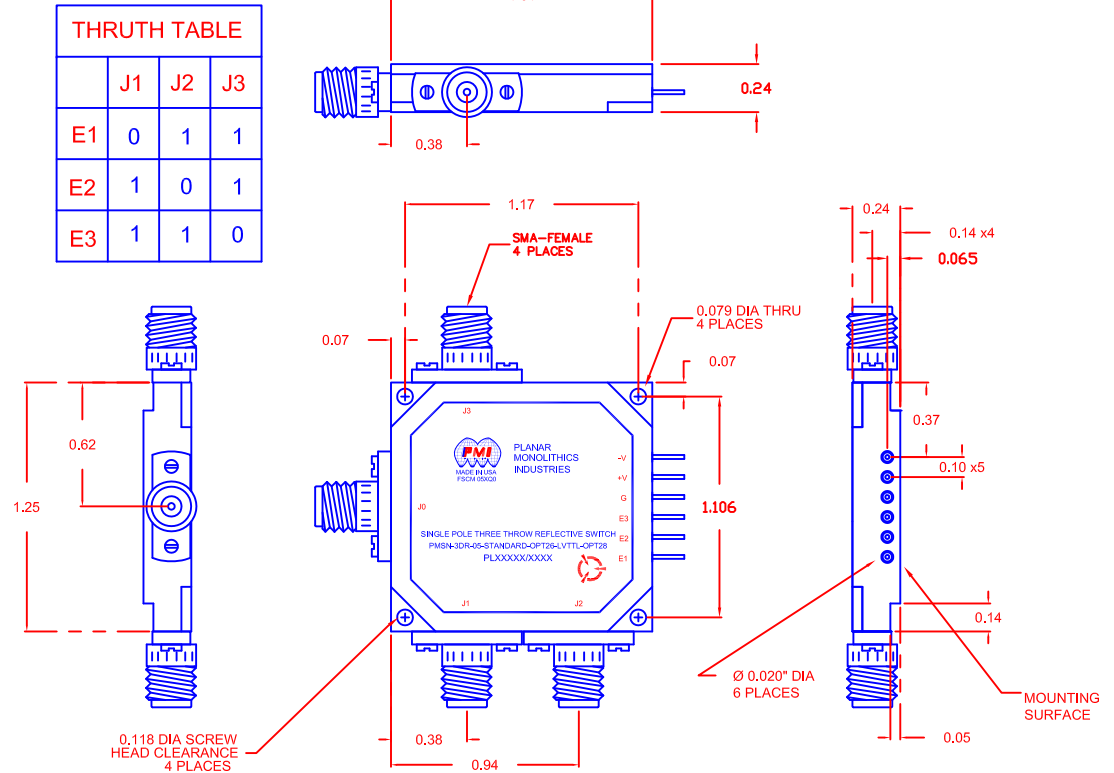
BLOCK DIAGRAM



ALL DIMENSIONS ARE IN INCHES
TOLERANCES:
X.XX ±0.020
X.XXX ±0.010

REVISIONS				
ZONE	REV.	DESCRIPTION	DATE	APPROVED
	-	PRELIMINARY	11/27/19	

MECHANICAL OUTLINE



PMI CONFIDENTIAL AND PROPRIETARY

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APPROVALS		DATE	TITLE		
DRAWN <i>JPU</i>		12/19/16	PRODUCT FEATURE		
REDRAWN <i>BW</i>		11/27/19	PMSN-3DR-05-STANDARD-OPT26-LVTTL-OPT28		
ISSUED			SIZE A	FSCM NO. 05XQ0	DWG NO. PRELIMINARY
			SCALE N: S	SHEET 1 OF 1	

NOTE: SPECIFICATIONS WILL VARY OVER OPERATING TEMPERATURE
NOTE: THE ABOVE SPECIFICATIONS ARE SUBJECT TO CHANGE OR REVISION