



SUMMARY TEST DATA ON PMSN-4DT-561R875-NG-H02

CUSTOMER: _____
 SO: _____
 MODEL NO: PMSN-4DT-561R875-NG-H02
 SERIAL NO: PL49771/2443

TESTED BY: J. ESCANO
 TEMPERATURE: +25°C
 DATE: 9/19/2024
 DRAWING NO: 27646280 REV: A1

TEST ITEM NO:	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC																													
1	Frequency Range	0.7 to 1.3 GHz	0.7 to 1.3 GHz																														
2	Path Gain	-4.0 dB ± 0.6 dB	-4.3 dB																														
3	Port to Port Gain Deviation	±0.4 dB Max (Room Temp)	±0.18 dB																														
4	Insertion Loss Gain Deviation	0.5 dB or -1 dB Max from Room Temp (Over Operating Temp)	-0.48 dB																														
5	RF Input Power	+3 dBm CW (Operating) +25 dBm CW (Survival)	Pass																														
6	Noise Figure	15 dB Max (Room Temp)	+25°C: 14.8 dB -40°C: 14.4 dB +85°C: 15.5 dB																														
7	Input P1dB	+6 dBm Min	+25°C: +16.3 dBm -40°C: +17.7 dBm +85°C: +15.9 dBm																														
8	Input IP3	+15.5 dBm Min	+30.77 dBm																														
9	Harmonics	-20 dBc Max, Pin = 3 dBm -65 dBc Max, Pin = -20 dBm	-47 dBc -68.6 dBc																														
10	Spurious	-60 dBc Max, Pin = 3 dBm -65 dBc Max, Pin = -20 dBm	-78.9 dBc -72.2 dBc																														
11	RF Isolation	65 dB Min from Unselected Input to Output, 35 dB Min from Output to Selected Input, 70 dB Min from Isolation Input to Input, -60 dBm Max Spectral Energy Leakage From 0.7 to 1.3 GHz	78.79 dB 47.57 dB 93.63 dB -70.5 dBm																														
12	Insertion Phase Variation	±1.5° Max for any 50 MHz Segment	+25°C: ±0.94° -40°C: ±0.96° +85°C: ±0.91°																														
13	Insertion Gain Variation	±0.3 dB Max for any 50 MHz Segment	+25°C: ±0.04 dB -40°C: ±0.04 dB +85°C: ±0.04 dB																														
14	VSWR	Input: 1.4:1 Max Output: 1.6:1 Max	Input: 1.34:1 Output: 1.1:1																														
15	RF Leakage	-60 dB Max	-70 dBm																														
16	DC Power Supply	+5 VDC ±3.8% @ 320 mA Max -12 VDC ±3.8% @ 50 mA Max	+5 V @ 280 mA -12 V @ 29 mA																														
17	Switch Control Characteristics	"0" = ON "1" = OFF	Pass																														
18	Control Lines	<table border="1" style="width: 100%; text-align: center;"> <thead> <tr> <th colspan="4">CONTROL LINES</th> <th rowspan="2">SWITCH PATH SELECTED</th> </tr> <tr> <th>E1</th> <th>E2</th> <th>E3</th> <th>E4</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>1</td> <td>1</td> <td>1</td> <td>J1 TO J5</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>1</td> <td>J2 TO J5</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>1</td> <td>J3 TO J5</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>0</td> <td>J4 TO J5</td> </tr> </tbody> </table>	CONTROL LINES				SWITCH PATH SELECTED	E1	E2	E3	E4	0	1	1	1	J1 TO J5	1	0	1	1	J2 TO J5	1	1	0	1	J3 TO J5	1	1	1	0	J4 TO J5	Pass	
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7309-A Grove Road Frederick, MD 21704 USA Phone: (301)662-5019 FAX: (301)662-1731
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19	DC Block	No voltage detected from RF Ports	Pass	
20	Switching Time*	25 ns Max from 50% TTL to 90% RF Output		
21	Recovery Time*	1 us Max to w/in 0.5 dB & 5° of steady state when input power drops from +25 dBm to 0 dBm		
22	Video Feedthrough*	From 0.5 to 1.3 GHz -70 dBm Max, 30 ns duration -60 dBm Max, 25 ns duration		
23	Power Supply*	Unit returns to previous performance after power supply is restored		

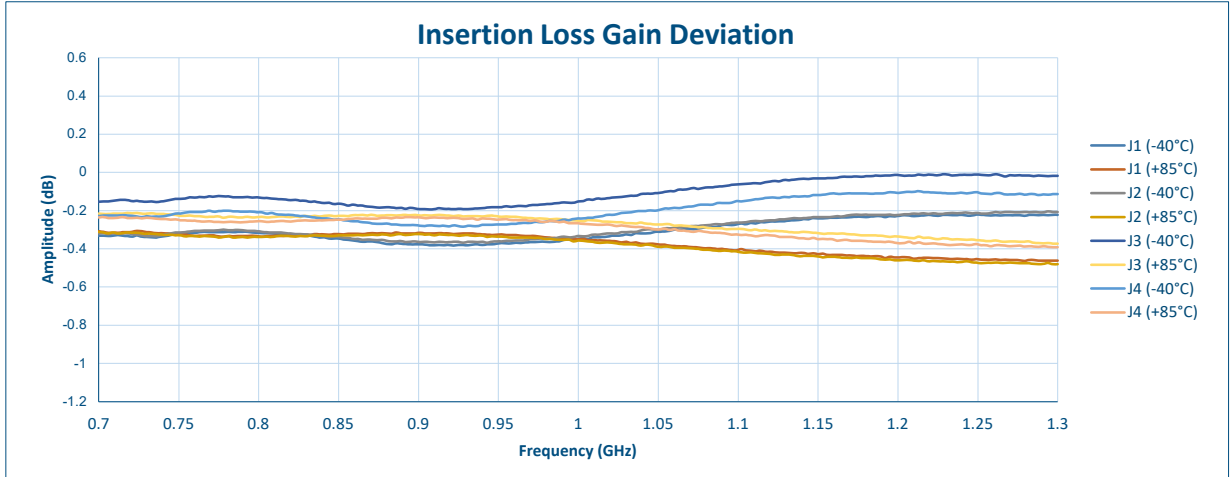
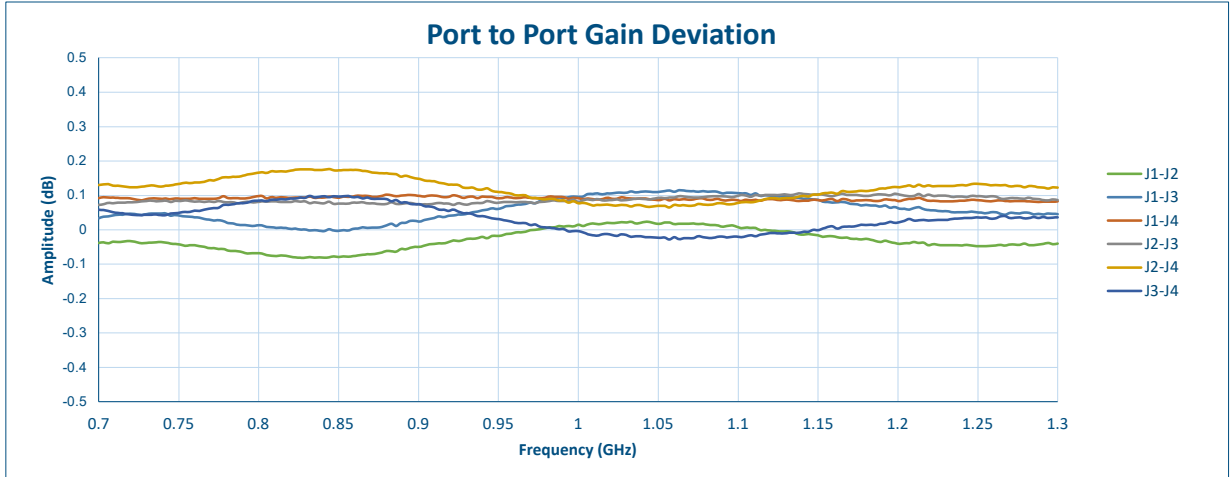
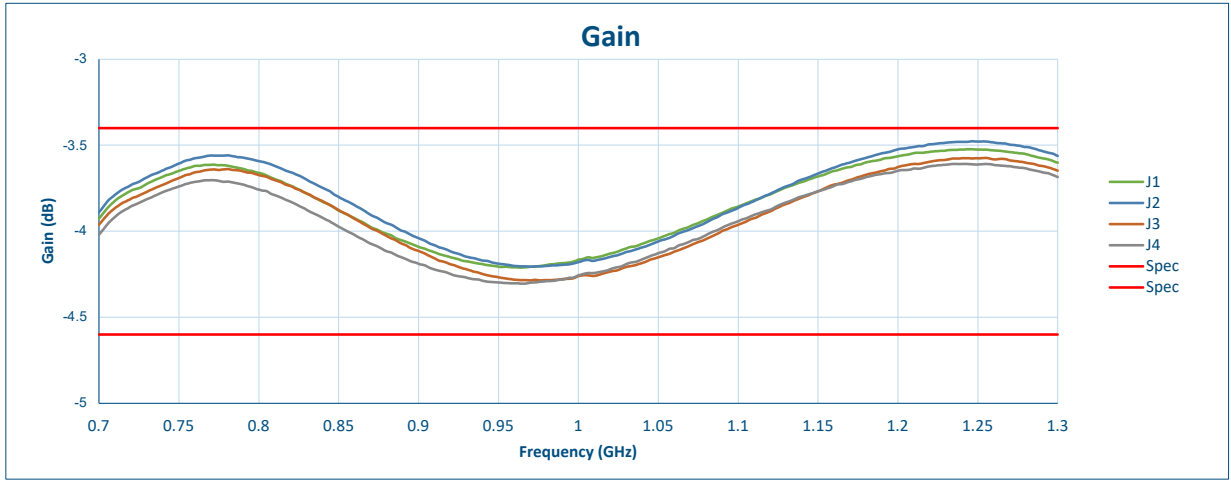
*Test 2 Units Only

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 MECHANICAL REQUIREMENTS**

TEST ITEM NO:	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC
M1	Outline	See ATP Section 4.5.1 For Details	Pass	
M2	Weight	4 oz. Max	Pass	
M3	RF Connectors	See ATP Section 4.5.3 For Details	Pass	
M4	Solder Pins	See ATP Section 4.5.4 For Details	Pass	
M5	Package Hermetic Seal	See ATP Section 4.5.5 For Details	Pass	
M6	Mounting Position	See ATP Section 4.5.6 For Details	Pass	
M7	Marking	See ATP Section 4.5.7 For Details	Pass	
M8	Additional Marking	See ATP Section 4.5.8 For Details	Pass	
M9	Workmanship	See ATP Section 4.5.9 For Details	Pass	

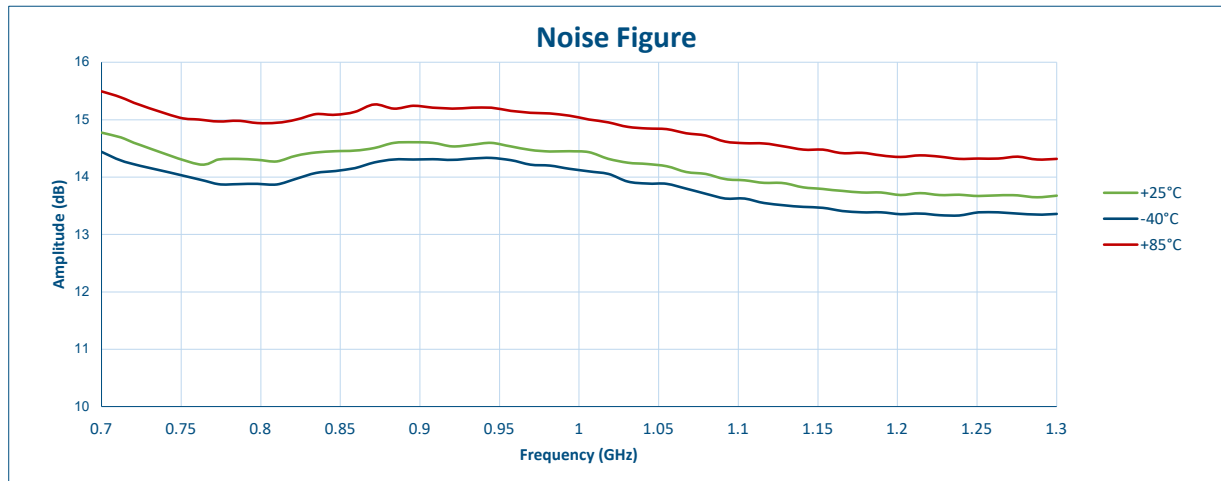
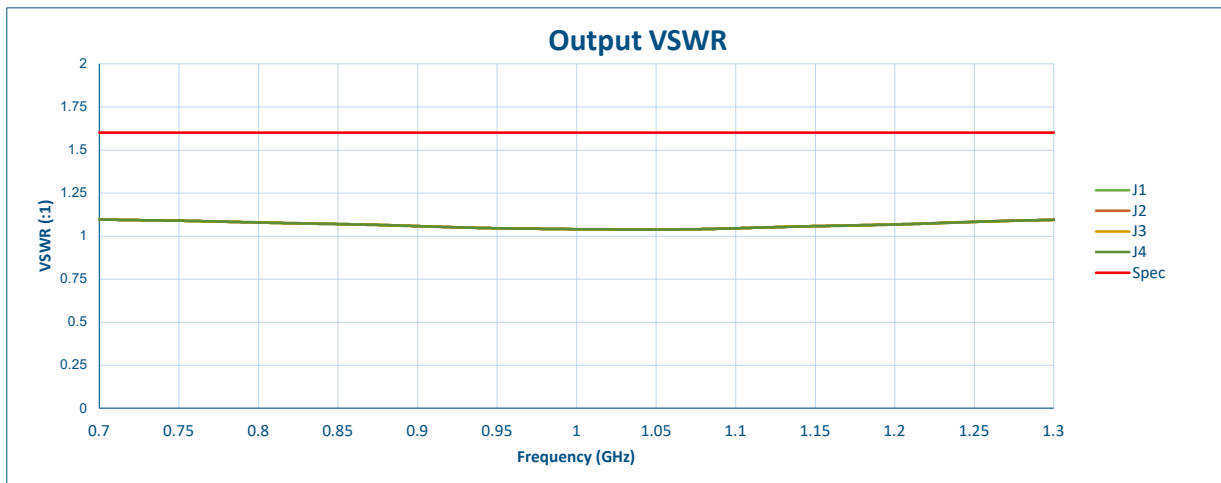
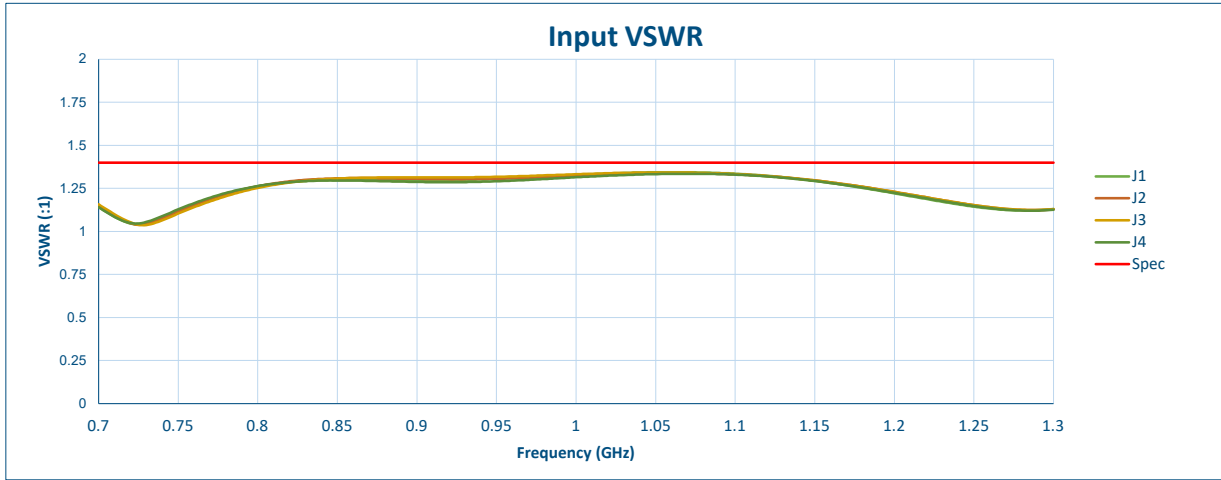
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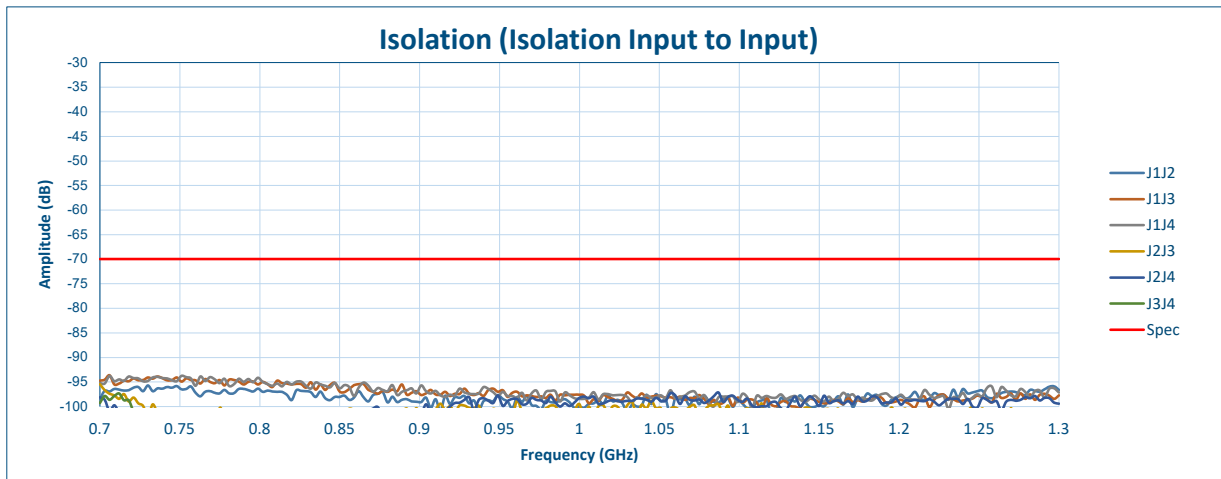
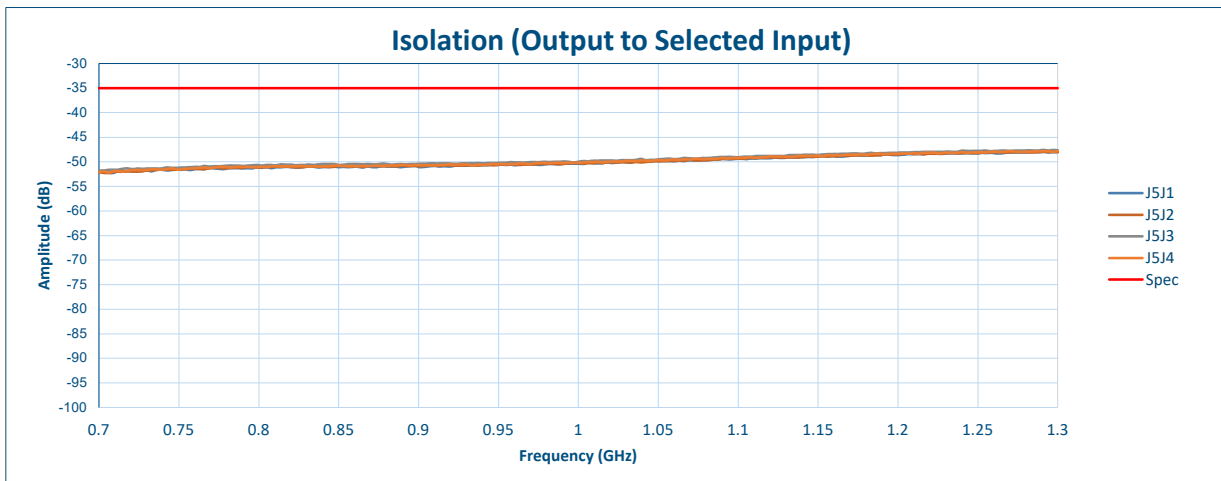
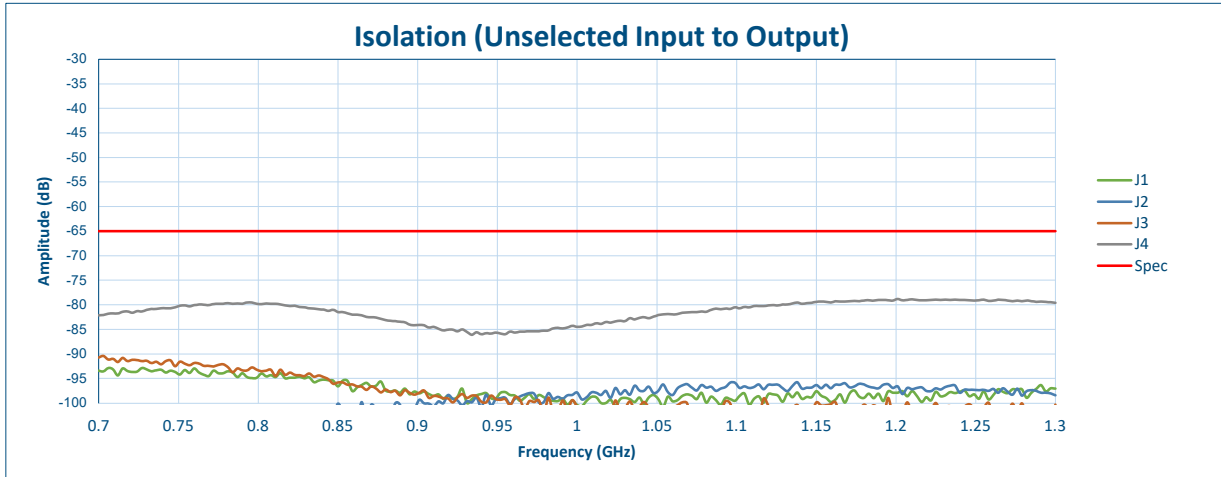
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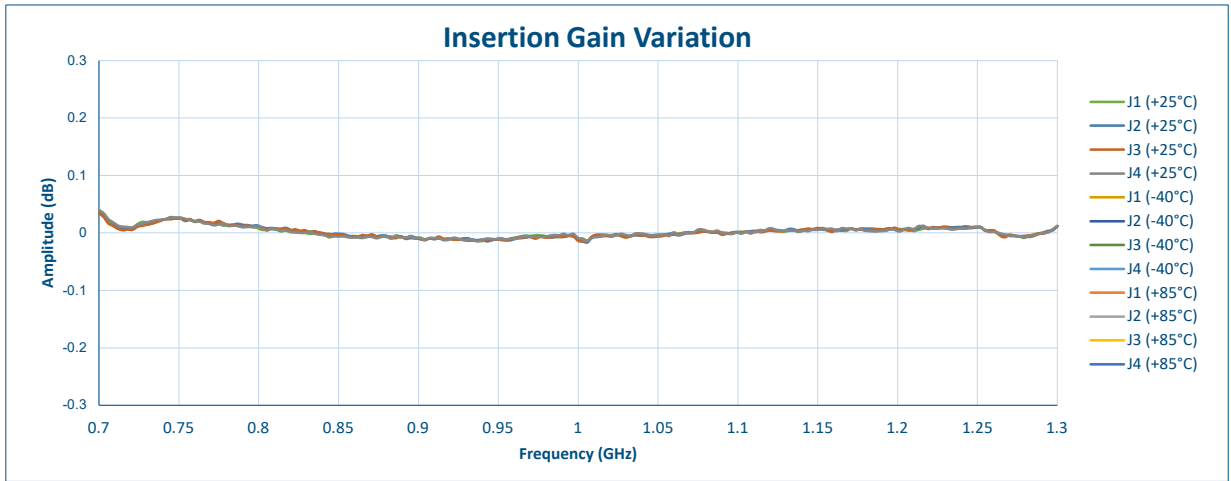
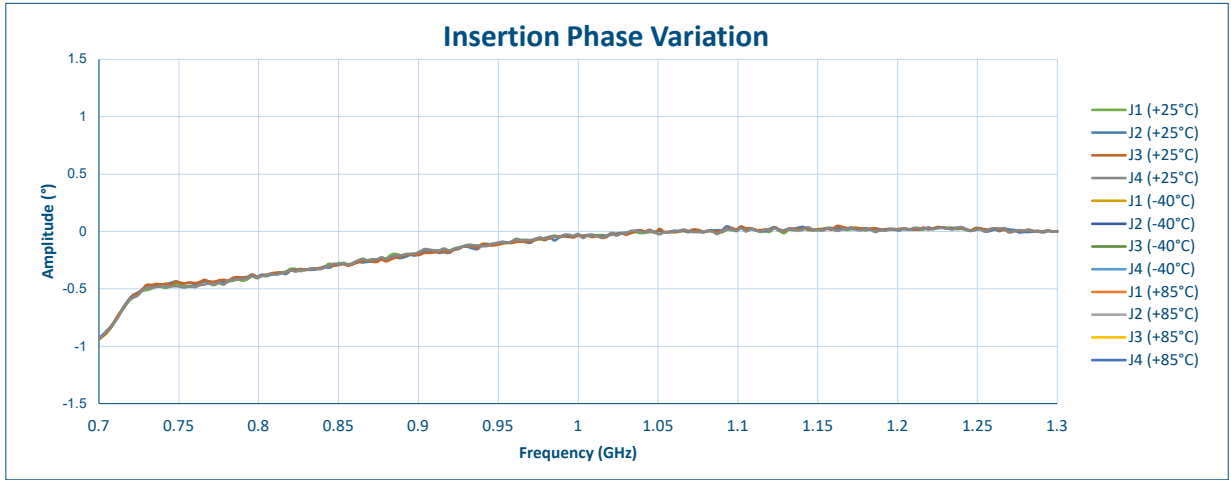
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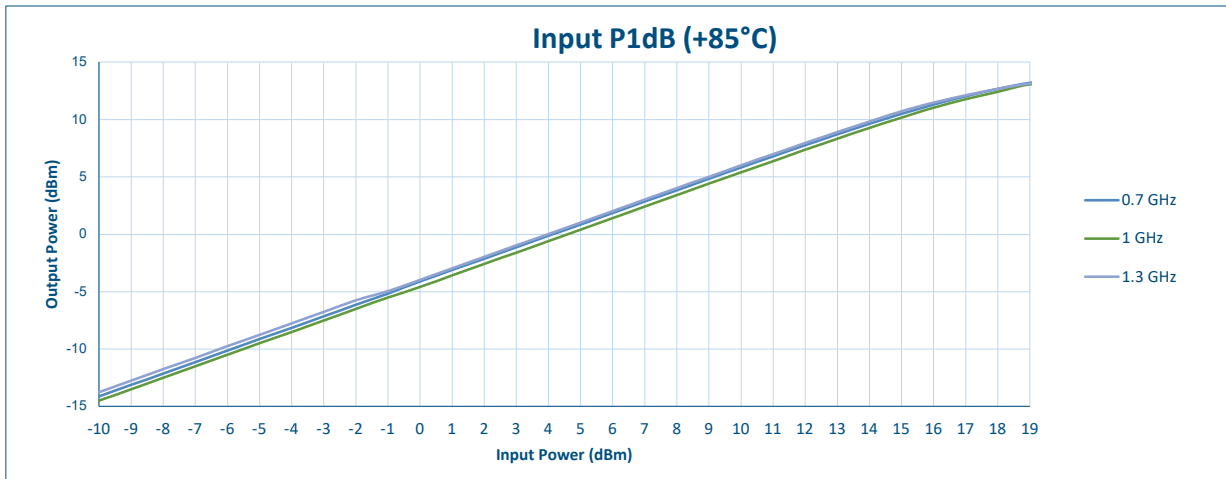
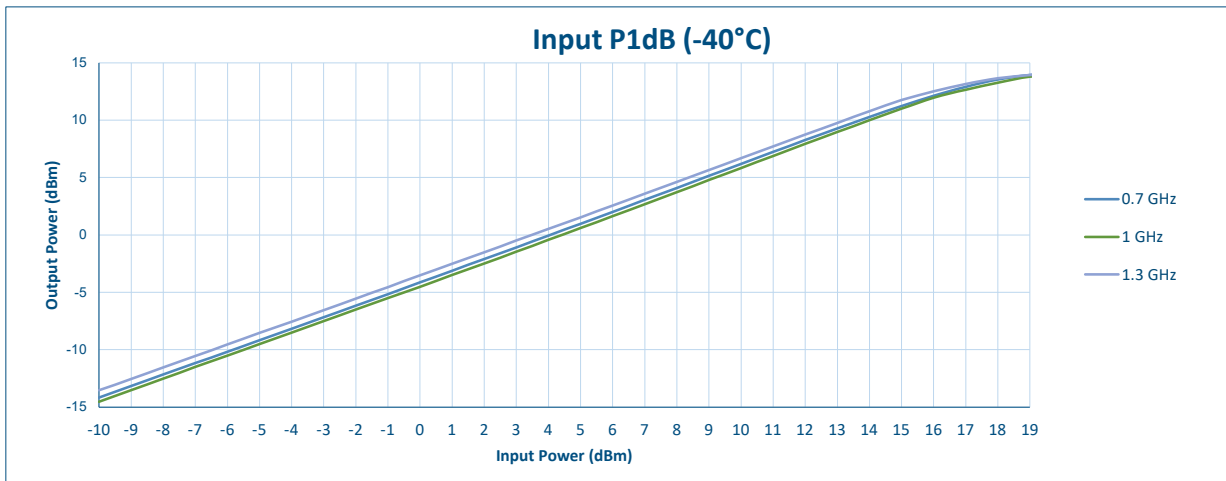
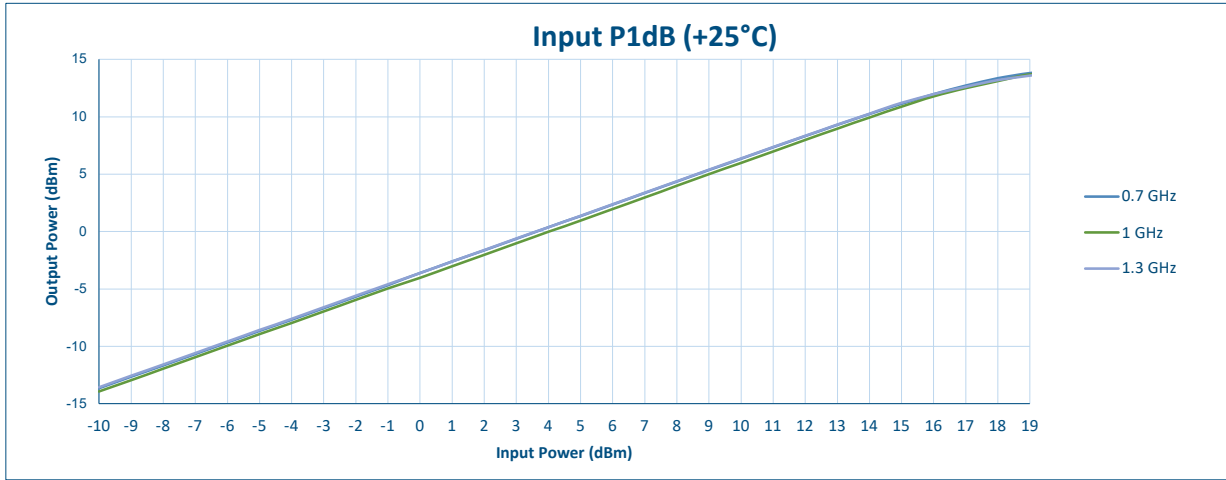
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