



**SUMMARY TEST DATA
ON
SDLVA-2G18G-50MV-100-NRF-2**

PL54578/2531

Customer: _____
 SO No: _____
 Model No: SDLVA-2G18G-50MV-100-NRF-2
 Serial No: PL54578/2531

Tested By: RCombs
 Temperature: +25° C
 Date: 7/31/2025
 Drawing No: 27650000 Rev: _____

TEST ITEM NO.	PARAMETERS	SPECIFIED VALUE	TEST RESULTS			
			65°C	25°C	-30°C	Pass/Fail
1	Frequency lower	2GHz	2	2	2	PASS
	Frequency upper	18GHz	18	18	18	PASS
2	Frequency Flatness max	±2dB	1.01	0.91	1.11	PASS
3	Input Return Loss 50Ω system	Manual plot	See Typical	See Typical	See Typical	PASS
4	TSS:	-69 dBm Minimum	-74.3	-72.1	-75.7	PASS
5	VSWR:	2.0:1 (Input)	1.26:1			
6	Power Input:	+17 dBm CW Maximum	PASS			
7	Logging range Top	5dBm	5	5	5	PASS
	Logging range Bottom	-67dBm	-67	-67	-67	PASS
8	Log slope Max	51.5 mV/dB	50.7	50.5	50.7	PASS
	Log slope Min	48.5 mV/dB	49.7	49.2	49	PASS
9	Min step slope	> 20 mV/dB	PASS	PASS	PASS	PASS
10	Log Linearity max*	±1.35 dB (±1dB AMB)	0.99	0.80	0.93	PASS
11	Output load	100Ω	PASS	PASS	PASS	PASS
12	Log Offset:	±100mV	0.028	-0.049	-0.038	PASS
13	Pulse Range:	30 ns to CW	PASS	PASS	PASS	PASS
14	Recovery Time:	60 ns Typical	See Typical	62 ns	See Typical	PASS
15	Rise Time:	10 ns (6 ns Typical)	See Typical	7.8 ns	See Typical	PASS
16	Fall Time:	40 ns Typical	See Typical	39.3 ns	See Typical	PASS
17	DC Supply:	+15V or +12V @ 350 mA	281	268	236	PASS
		+15V or -12V @ 180 mA	109	100	87	PASS
18	Avg -67	200±100mV	0.194	0.187	0.189	PASS
	Avg +5	3800±100mV	3.794	3.792	3.803	PASS