

**SUMMARY TEST DATA
ON
PLDRO-12000-13DBM-SFF**

Customer: _____
SO: _____
Model No: PLDRO-12000-13DBM-SFF
Serial No: PL54701/2532

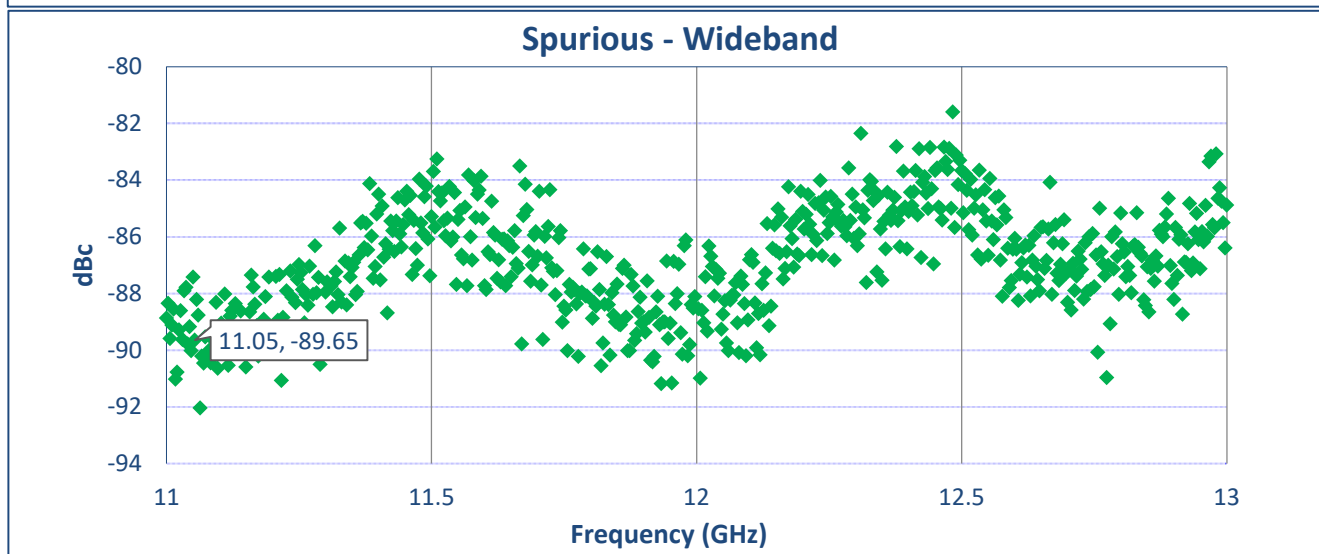
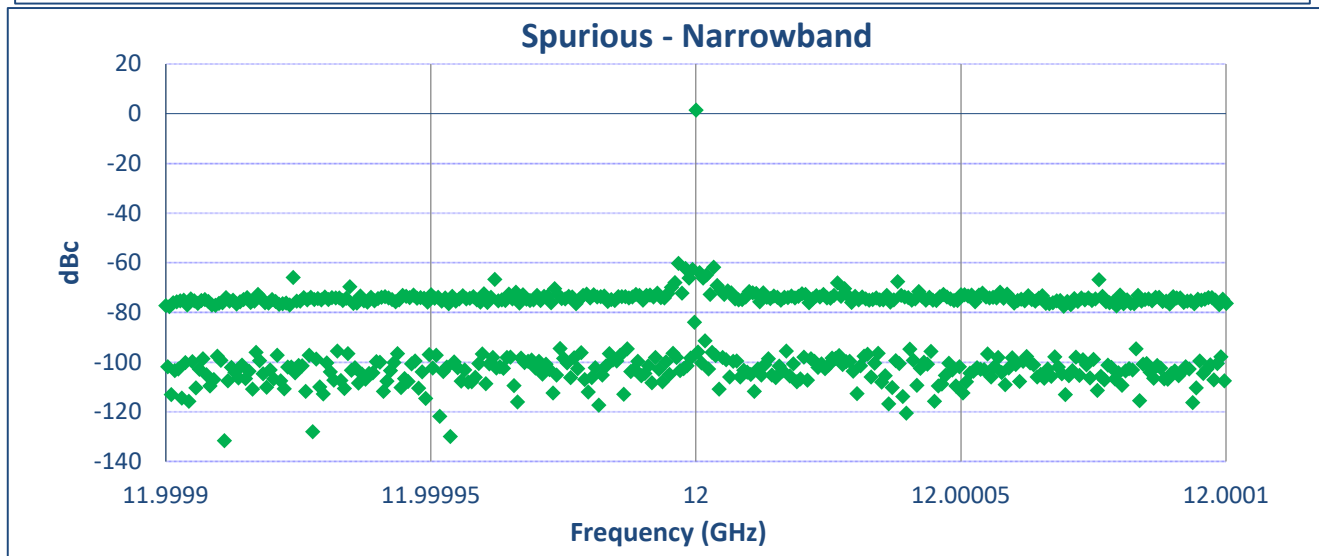
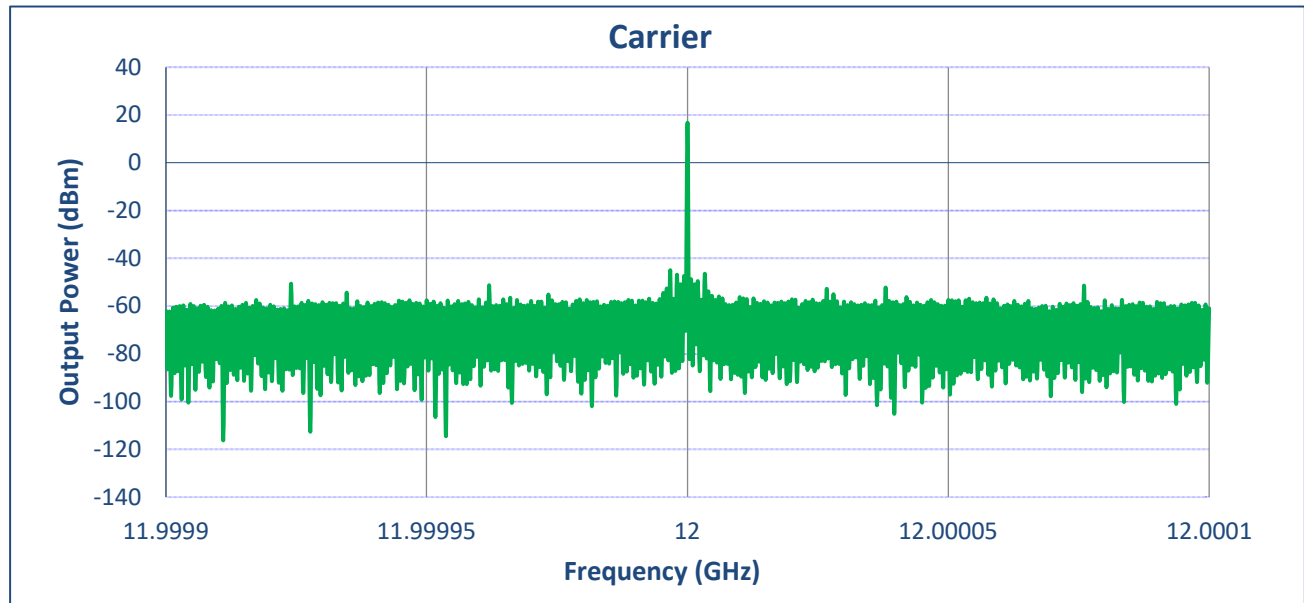
Tested By: E. Kretz
Temperature: 25°C
Date: 8/5/2025
Drawing No: 27643460 Rev: A1

TEST ITEM	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC
1	Output Frequency	12000 MHz	12000 MHz	
2	Reference Frequency	100 MHz (Sine Wave)	100 MHz (Sine Wave)	
3	Input Level	-3 to +3 dBm	-3 to +3 dBm	
4	Output Level	+13 dBm Min.	18.1dBm	
5	Spurious	-60 dBc Max. (FO $\pm$ 100kHz) -75 dBc Max. (FO >100kHz)	-81.6 dBc	
6	Harmonics	-20 dBc Max.	-43.79 dBc	
7	Output Phase Noise	-130 dBc/Hz Max. @ 1 MHz	-142 dBc/Hz	
8	Supply Voltage	+12 V @ 300 mA Max.	+12 V @ 256 mA	
9	Lock Detector	TTL High = Lock	TTL High = Lock	

QA/QC Approval _____

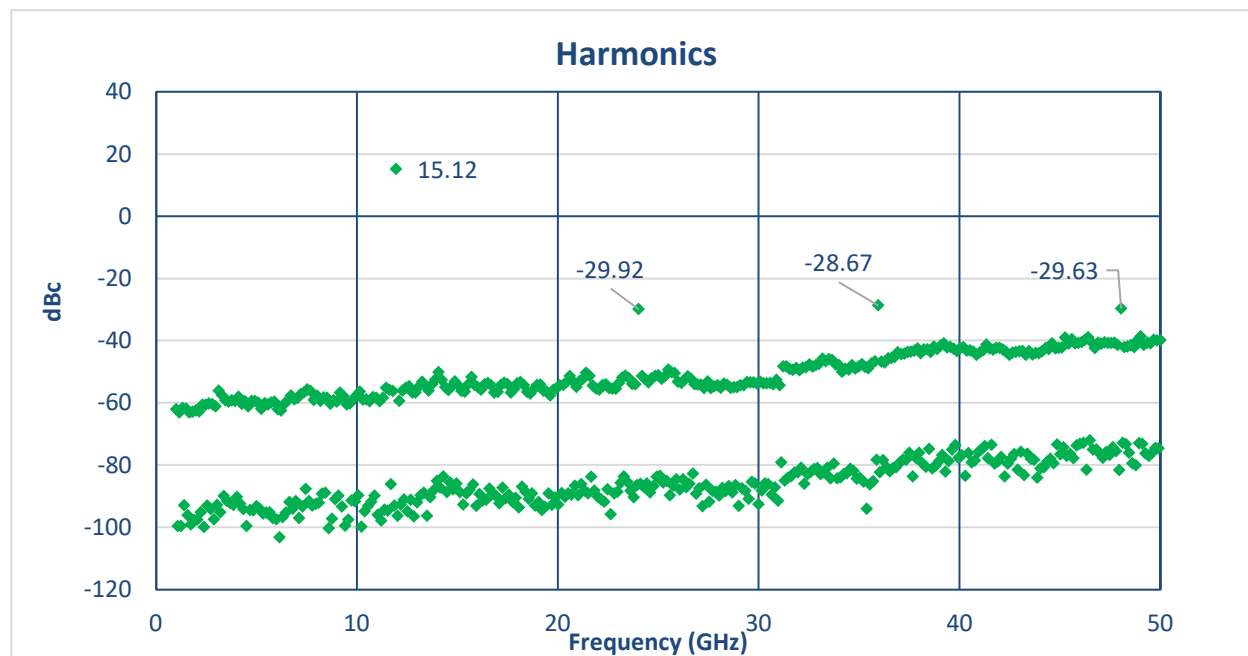
Date: _____

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Harmonics



Phase Noise

