

**SUMMARY TEST DATA  
ON  
P2T-18G42G-60-R-55-292FF**

Customer: \_\_\_\_\_  
 SO No: \_\_\_\_\_  
 Model No: P2T-18G42G-60-R-55-292FF  
 Serial No: PL55610/2540

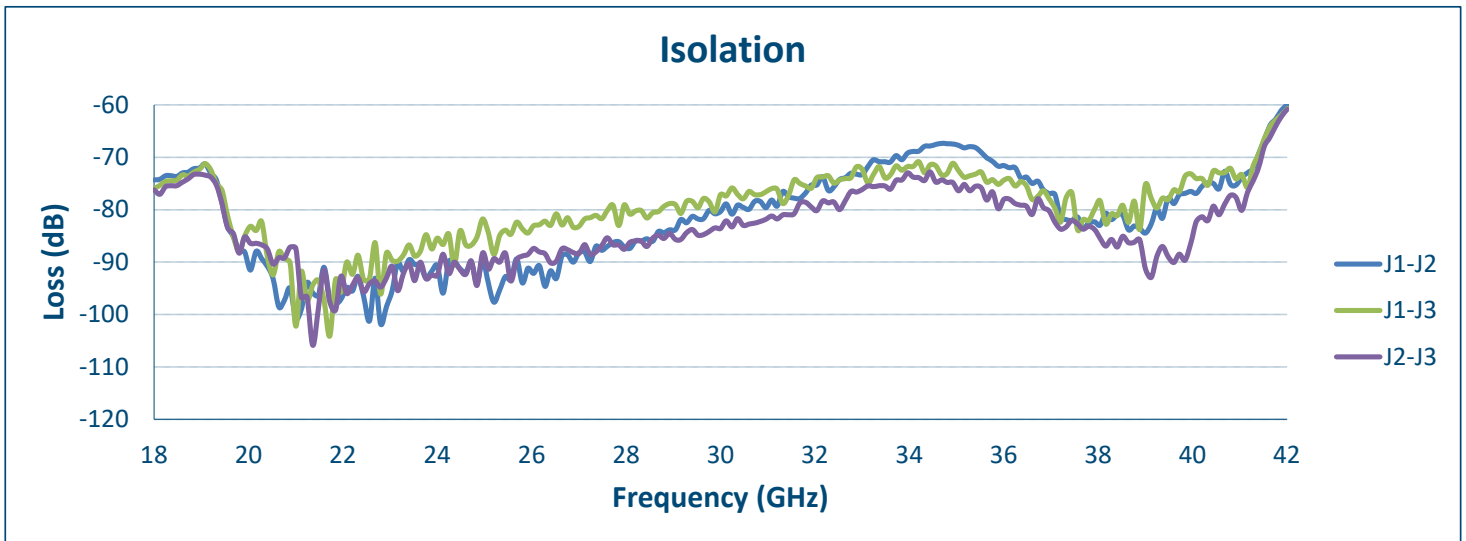
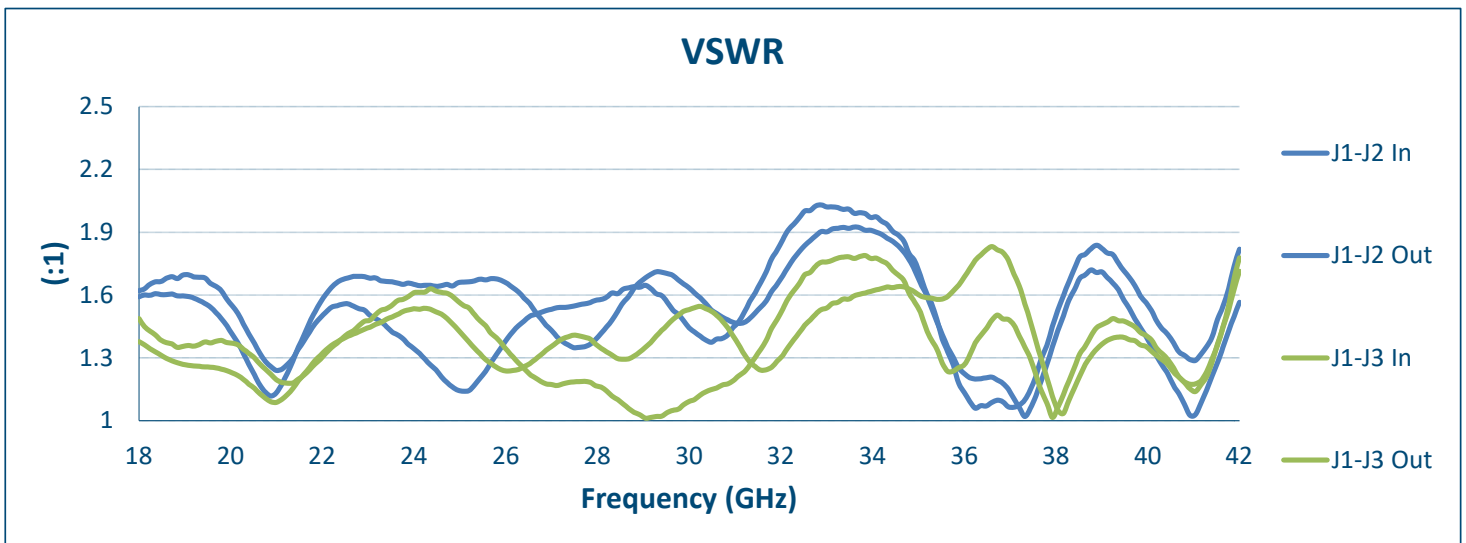
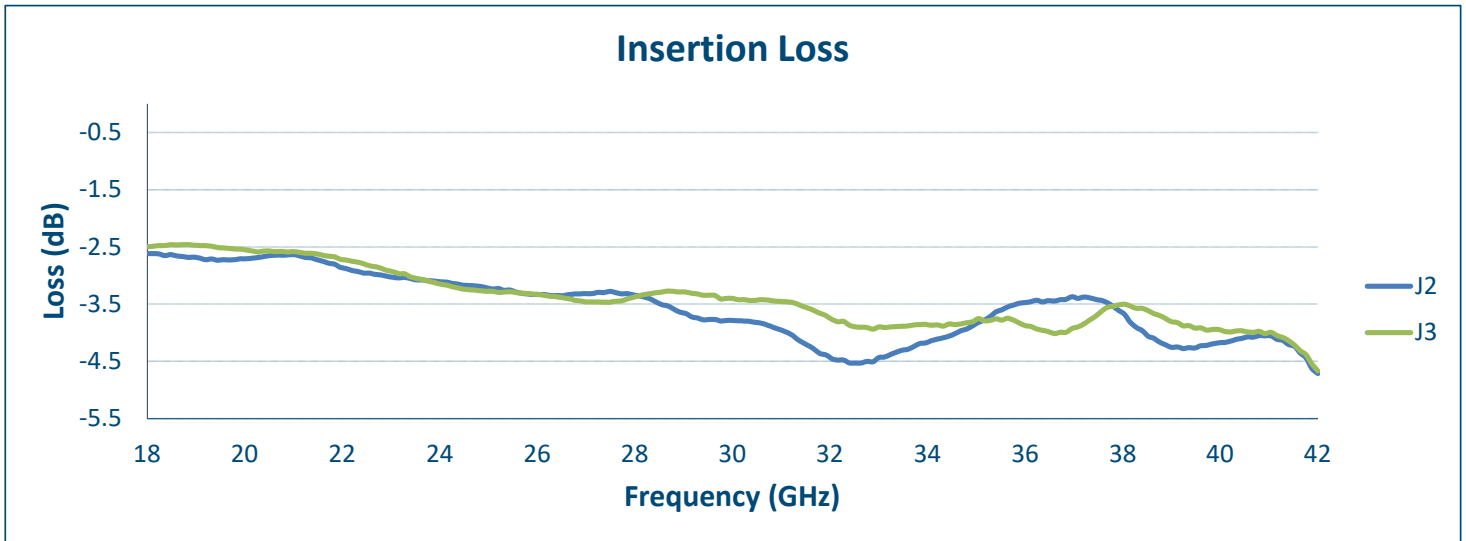
Tested By: S. O'Neill  
 Temperature: 25°C  
 Date: 10/7/2025  
 Drawing No: 27652480      Rev: A1

| TEST ITEM NO. | PARAMETERS       | SPECIFIED VALUE   | TEST RESULTS                                | QA QC |
|---------------|------------------|---|---|-------|
| 1             | Frequency Range: | 18 GHz TO 42 GHz  | 18 GHz TO 42 GHz                            |       |
| 2             | VSWR:<br>In/Out  | 2.5:1 Max   | In 2.03:1<br>Out 1.93 :1<br>See Plot        |       |
| 3             | Insertion Loss:  | 4.5 dB Goal<br>5.5 dB Max   | 4.71 dB<br>See Plot                         |       |
| 4             | Isolation:       | 60 dB Min.  | 60 dB<br>See Plot                           |       |
| 5             | Switching Speed: | DRIVER DELAY AND SWITCH TIME (ON) (50% TTL TO INSERTION LOSS): <b>200 ns Max</b>  | 166 ns                                      |       |
|               |                  | DRIVER DELAY AND SWITCH TIME (OFF) (50% TTL TO INSERTION LOSS): <b>200 ns Max</b> | 122 ns                                      |       |
| 6             | DC Supply:       | +5 VDC ±5% @ 175 mA Max.<br>-5 VDC +5% TO -12 VDC -5% @ 75 mA Max.                | +5 VDC @ 23 mA<br>-5 VDC TO -12 VDC @ 26 mA |       |
| 7             | Control Signal:  | TTL LOGIC<br>0: J2 ENABLED<br>1: J3 ENABLED                                       | TTL LOGIC<br>0: J2 ENABLED<br>1: J3 ENABLED |       |

QA/QC Approval: \_\_\_\_\_

Date: \_\_\_\_\_

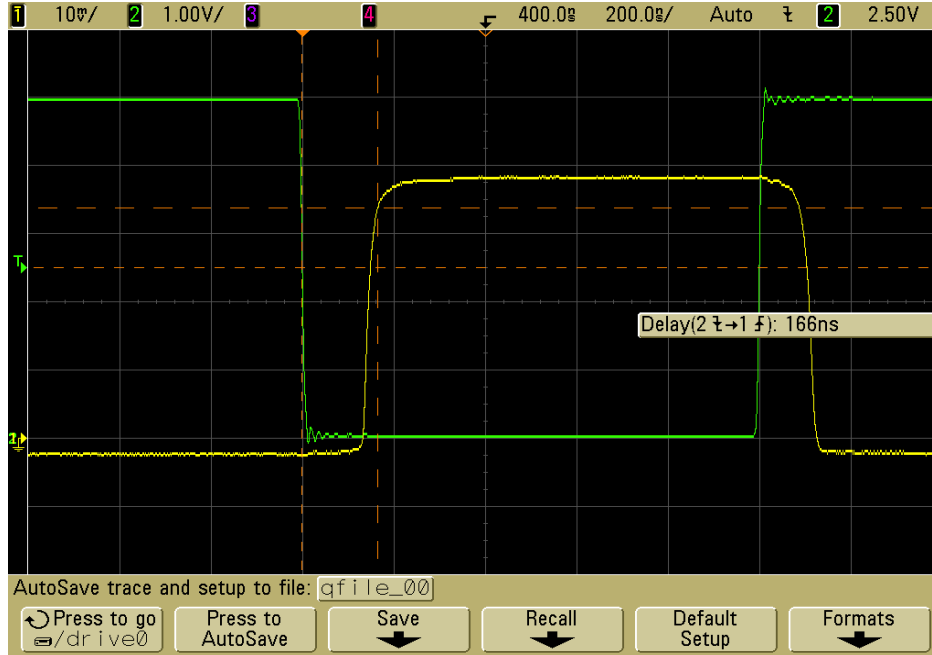
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# SUMMARY TEST DATA ON P2T-18G42G-60-R-55-292FF

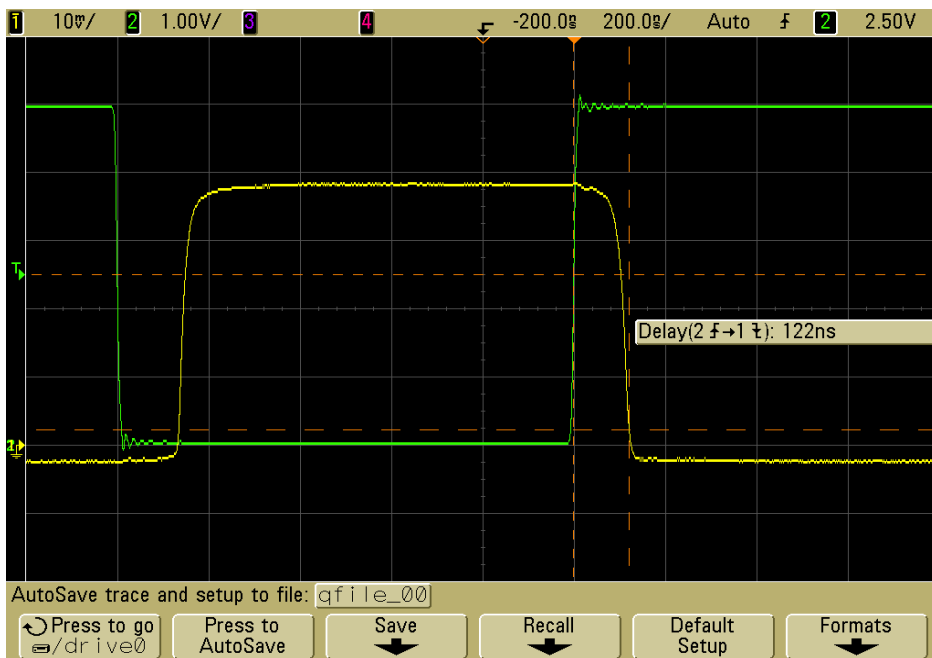
## J2 Rise Time Delay

Green=TTL Yellow=RF



## J2 Fall Time Delay

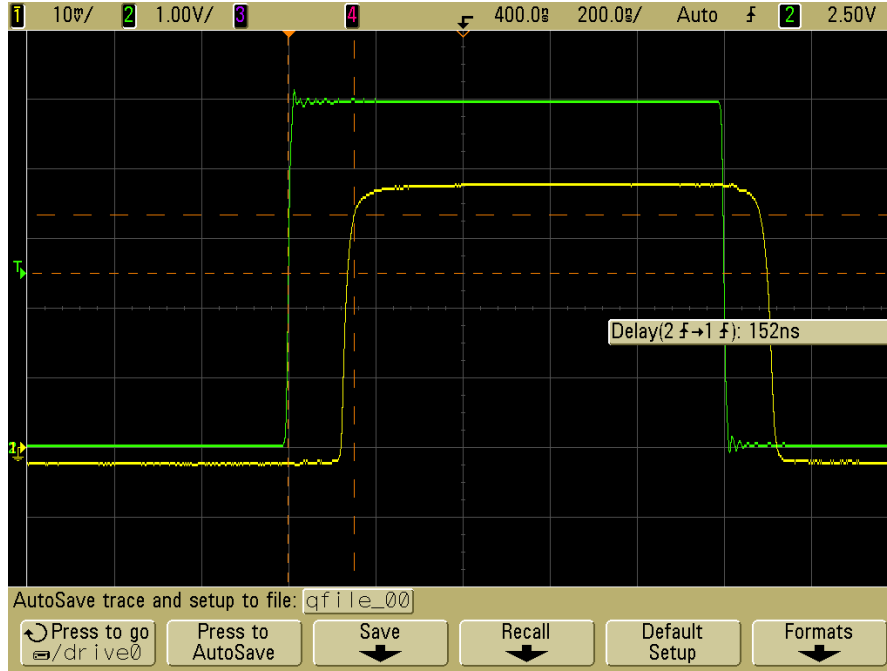
Green=TTL Yellow=RF



# SUMMARY TEST DATA ON P2T-18G42G-60-R-55-292FF

## J3 Rise Time Delay

Green=TTL Yellow=RF



## J3 Fall Time Delay

Green=TTL Yellow=RF

