

**SUMMARY TEST DATA
ON
PLDRO-2D5G-100M-EXT-SMA**

Customer: _____
SO No: _____
Model No: PLDRO-2D5G-100M-EXT-SMA
Serial No: PL54124/2528

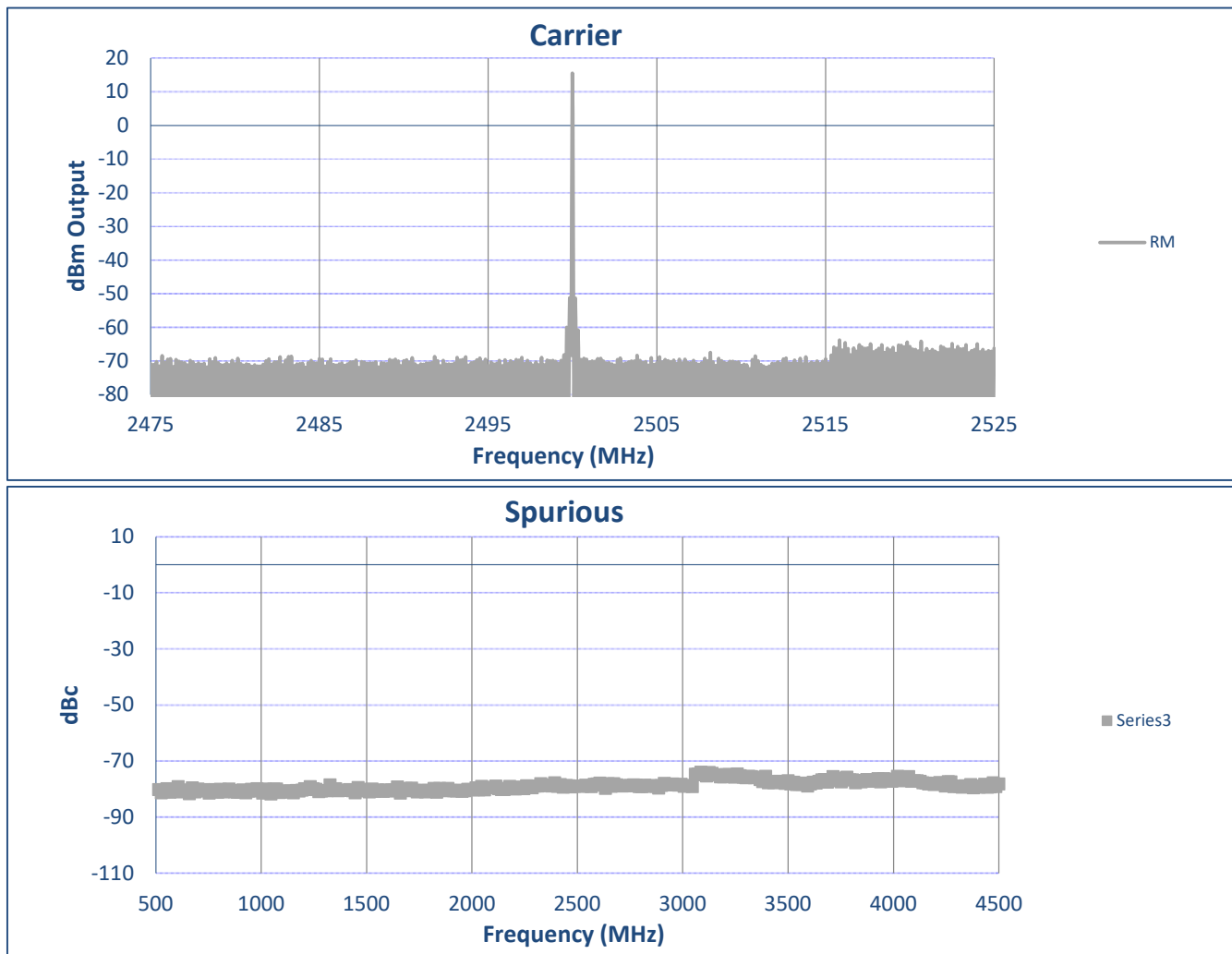
Tested By: E. Kretz
Temperature: 25°C
Date: 7/10/2025
Drawing No: 27651780 Rev: A1

TEST ITEM	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC
1	Output Frequency	2500 MHz	2500 MHz	
2	Output Level	13 dBm MIN.	15.5 dBm	
3	Spurious	-70 dBc MAX.	-74 dBc	
4	Harmonics	-20 dBc MAX.	43.51 dBc	
5	Phase Noise	-100 dBc/Hz TYP. @ 100Hz -120 dBc/Hz MAX. @ 1 kHz -126 dBc/Hz MAX. @ 10 kHz -126 dBc/Hz MAX. @ 100 kHz -140 dBc/Hz MAX. @ 1 MHz	PASS	
6	DC Supply Voltage	+15V @ 300 mA MAX.	15V @ 224 mA	
7	Lock Detector	TTL HIGH In Lock	Pass	

QA/QC Approval: _____ Date: _____

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Phase Noise Curve

