

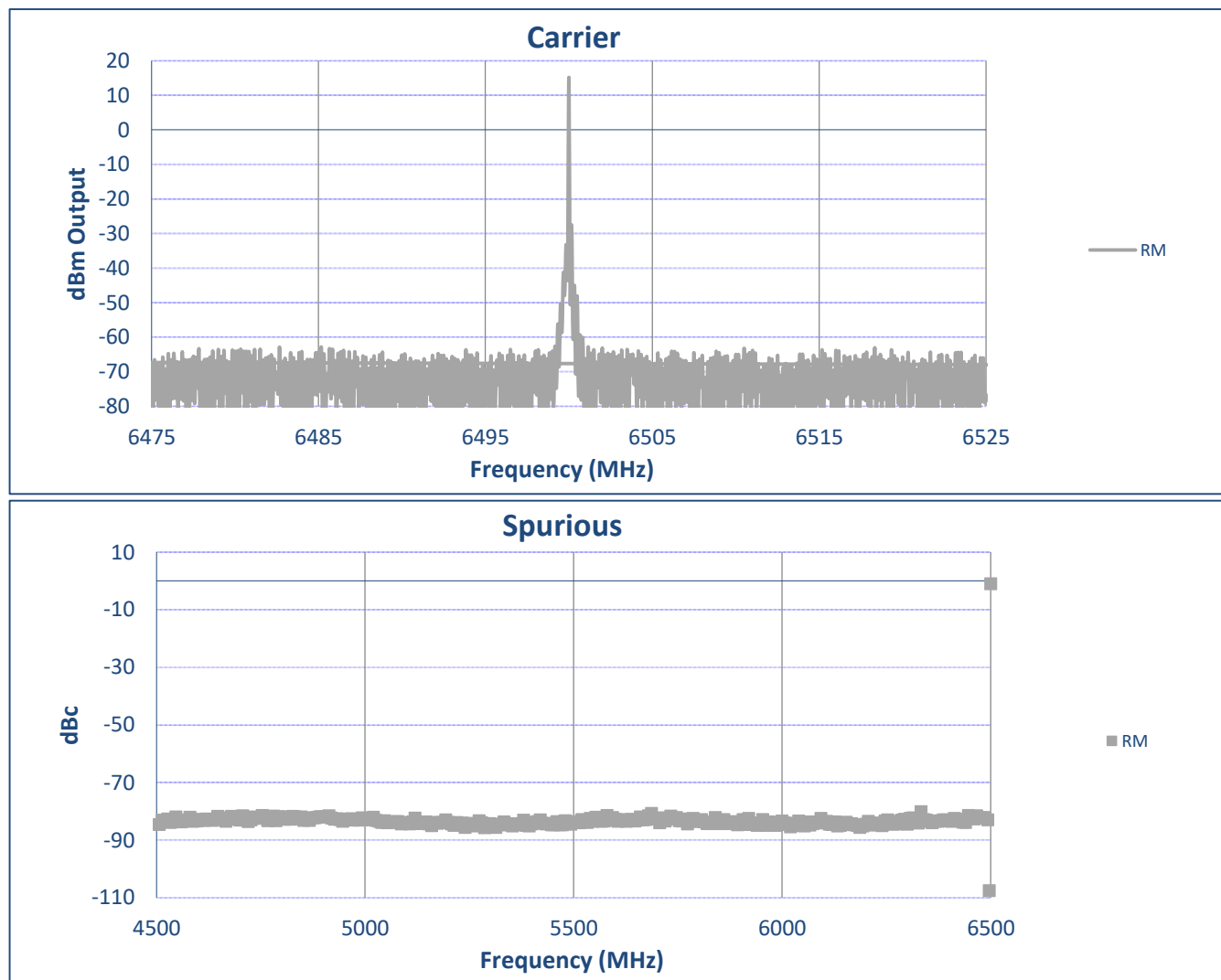


**SUMMARY TEST DATA
ON
PLDRO-6500-13DBM-SFF**

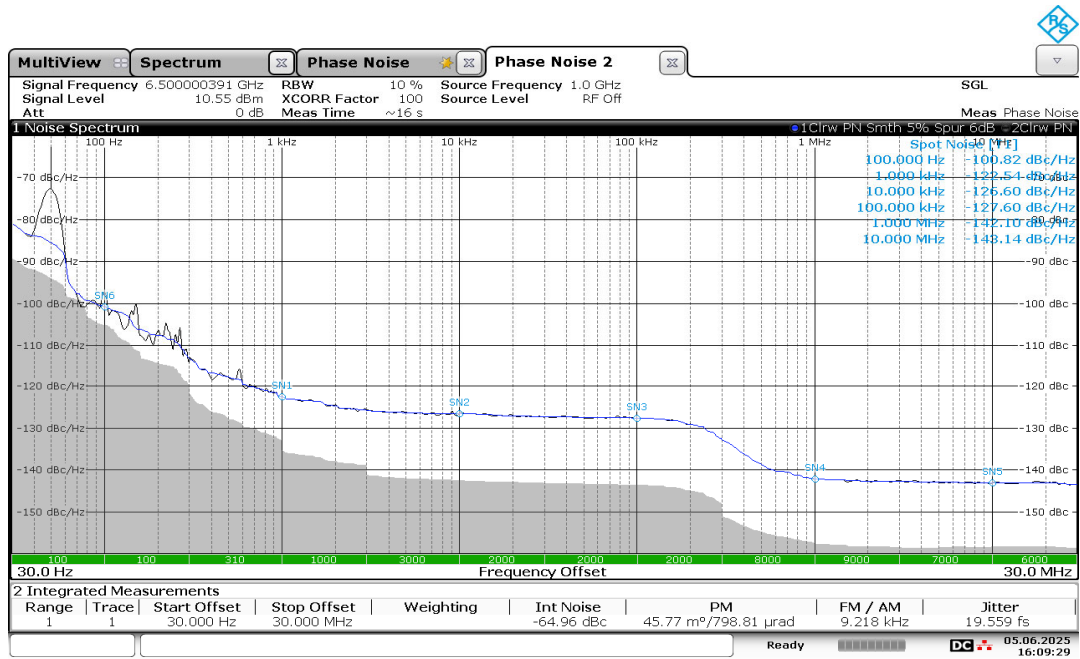
Customer: _____
SO No: _____
Model No: **PLDRO-6500-13DBM-SFF**
Serial No: **PL54129/2528**

Tested By: **Eric Kretz**
Temperature: **25°C**
Date: **7/11/2025**
Drawing No: **27643440** Rev: **A1**

TEST ITEM	PARAMETERS	SPECIFIED VALUE	TEST RESULTS	QA/QC
1	Output Frequency	6500 MHz	6500 MHz	
2	Output Level	13 dBm MIN.	15.2 dBm	
3	Spurious	-75 dBc MAX.	-80.3 dBc	
4	Harmonics	-20 dBc MAX.	27.86 dBc	
5	Phase Noise	-135 dBc/Hz MAX. @ 1 MHz	PASS	
6	DC Supply Voltage	12V @ 300 mA MAX.	12V @ 230 mA	
7	Lock Detector	TTL HIGH In Lock	Pass	



Phase Noise Curve



16:09:30 05.06.2025